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Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# MGA-637P8

## High Linearity Low Noise Amplifier



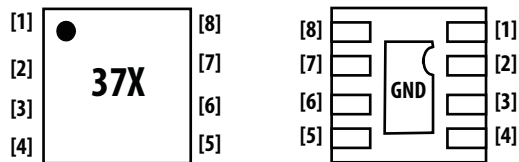
### Data Sheet

#### Description

Avago Technologies' MGA-637P8 is an economical, easy-to-use GaAs MMIC Low Noise Amplifier (LNA). This LNA has low noise and high linearity achieved through the use of Avago Technologies' proprietary 0.25  $\mu\text{m}$  GaAs Enhancement-mode pHEMT process. It is housed in the miniature 2.0 x 2.0 x 0.75 mm<sup>3</sup> 8-pin Dual-Flat-Non-Lead (DFN) package. The device is designed for optimum use from 1.5 GHz up to 2.5 GHz. The compact footprint and low profile coupled with low noise, high gain and high linearity make this an ideal choice as a low noise amplifier for cellular infrastructure applications such as LTE, GSM, CDMA, W-CDMA, CDMA2000 & TD-SCDMA. For optimum performance at lower frequency from 450 MHz up to 1.5 GHz, MGA-636P8 is recommended. For optimum performance at higher frequency from 2.5 GHz up to 4 GHz, MGA-638P8 is recommended. All these 3 products, MGA-636P8, MGA-637P8 and MGA-638P8 share the same package and pinout configuration.

#### Pin Configuration and Package Marking

2.0 x 2.0 x 0.75 mm<sup>3</sup> 8-lead DFN



TOP VIEW

- Pin 1 – Not Used
- Pin 2 – RFinpu
- Pin 3 – Vbias2
- Pin 4 – Not Used
- Center paddle – GND

BOTTOM VIEW

- Pin 5 – Vbias1
- Pin 6 – PwrDwn
- Pin 7 – RFoutput
- Pin 8 – Not Used

Note:  
 Package marking provides orientation and identification  
 "37" = Product Code  
 "X" = Month Code

It is recommended to ground Pin1, 4 and 8 which are Not Used.

#### Features

- High linearity performance.
- Low Noise Figure.
- GaAs E-pHEMT Technology<sup>[1]</sup>.
- Low cost small package size.
- Integrated with active bias and option to access FET gate.
- Integrated power down control pin.

#### Specifications

1.7 GHz; 4.8 V, 75 mA


- 17.3 dB Gain
- 0.52 dB Noise Figure
- 11 dB Input Return Loss
- +22.5 dBm Input IP3
- +21.9 dBm Output Power at 1 dB gain compression

#### Applications

- Cellular infrastructure applications such as LTE, GSM, CDMA, W-CDMA, CDMA2000 & TD-SCDMA.
- Other low noise applications.

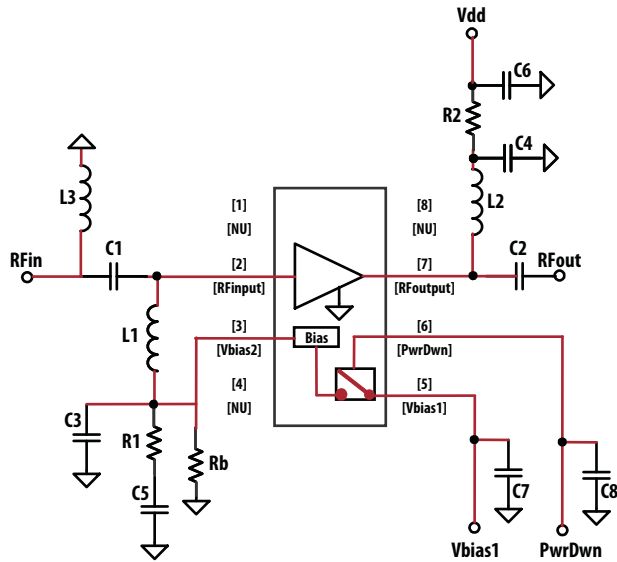
Note:

1. Enhancement mode technology employs positive Vgs, thereby eliminating the need of negative gate voltage associated with conventional depletion mode devices.



**Attention: Observe precautions for handling electrostatic sensitive devices.**  
 ESD Machine Model = 80 V  
 ESD Human Body Model = 350 V  
 Refer to Avago Application Note A004R: Electrostatic Discharge, Damage and Control.

## Simplified Schematic [1]



Note:

1. Device is turned ON when PwrDwn pin is applied with 0V or left open. Device is turned OFF when PwrDwn pin is applied with 3.3 V

## Absolute Maximum Rating [1] $T_A=25^\circ\text{C}$

| Symbol       | Parameter                           | Units            | Absolute Maximum |
|--------------|-------------------------------------|------------------|------------------|
| $V_{dd}$     | Device Voltage, RF output to ground | V                | 5.5              |
| $I_{dd}$     | Drain Current                       | mA               | 125              |
| $V_{bias1}$  | Bias Voltage                        | V                | 5.5              |
| $V_{PwrDwn}$ | Power Down Voltage                  | V                | 5.5              |
| $P_{in,max}$ | CW RF Input Power                   | dBm              | +24              |
| $P_{diss}$   | Total Power Dissipation             | W                | 0.6              |
| $T_j$        | Junction Temperature                | $^\circ\text{C}$ | 150              |
| $T_{stg}$    | Storage Temperature                 | $^\circ\text{C}$ | -65 to 150       |

## Thermal Resistance

Thermal Resistance [2]  
 $(V_{dd} = 4.8\text{ V}, I_{dd} = 75\text{ mA})$   
 $\theta_{jc} = 56.3^\circ\text{C/W}$

Notes:

1. Operation of this device in excess of any of these limits may cause permanent damage.
2. Thermal resistance measured using Infra-Red Measurement Technique.
3. Power dissipation with unit turned on. Board temperature  $T_c$  is  $25^\circ\text{C}$ . Derate at  $17.8\text{ mW}/^\circ\text{C}$  for  $T_c > 115.9^\circ\text{C}$ .

## Electrical Specifications<sup>[1, 4]</sup>

$T_A = 25^\circ\text{C}$ ,  $V_{dd} = V_{bias1} = 4.8\text{V}$ , RF measurement at 1.7 GHz, measured on demo board in Figure 5 with component listed in Table1.

| Symbol              | Parameter and Test Condition  | Units | Min. | Typ. | Max. |
|---------------------|---|-------|------|------|------|
| I <sub>dd</sub>     | Bias Current  | mA    | 59   | 75   | 101  |
| I <sub>PwrDwn</sub> | Current at V <sub>PwrDwn</sub> pin when V <sub>PwrDwn</sub> = 3.3 V (Power Down mode) | mA    | –    | 0.15 | –    |
| Gain                | Gain  | dB    | 16   | 17.3 | 19   |
| NF <sup>[2]</sup>   | Noise Figure  | dB    | –    | 0.52 | 0.75 |
| IIP3 <sup>[3]</sup> | Input Third Order Intercept Point   | dBm   | 21   | 22.5 | –    |
| OP1dB               | Output Power at 1dB Gain Compression  | dBm   | –    | 21.9 | –    |
| IRL                 | Input Return Loss, 50 Ω source  | dB    | –    | 13   | –    |
| ORL                 | Output Return Loss, 50 Ω load   | dB    | –    | 15.8 | –    |

Notes:

1. Measurements at 1.7 GHz obtained using demo board described in Figure 5.
2. For NF data, board losses of the input have not been de-embedded.
3. IIP3 test condition:  $F_{RF1} = 1.700\text{ GHz}$ ,  $F_{RF2} = 1.701\text{ GHz}$  with input power of -10 dBm per tone.
4. Use proper bias, heatsink and derating to ensure maximum channel temperature is not exceeded. See absolute maximum ratings and application note for more details.

## Product Consistency Distribution Charts<sup>[1, 2]</sup>

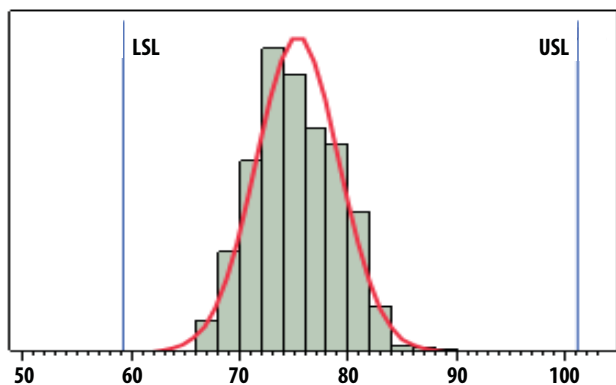


Figure 1. I<sub>dd</sub>, LSL = 59 mA, nominal = 75 mA, USL = 101 mA

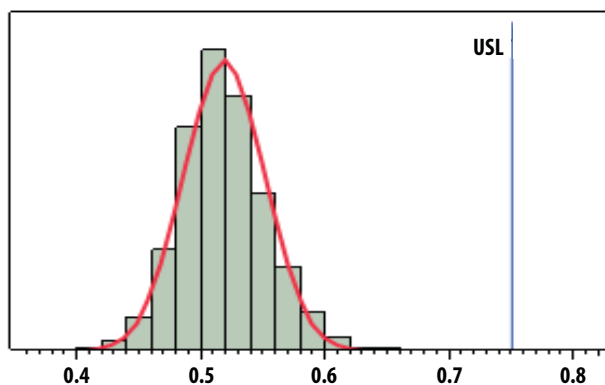


Figure 2. NF, nominal = 0.52 dB, USL = 0.75 dB

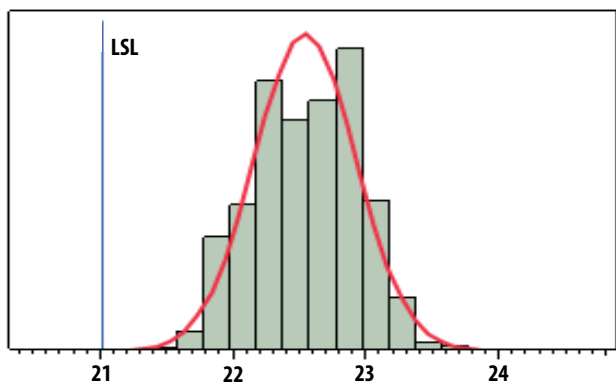


Figure 3. IIP3, LSL = 21 dBm, nominal = 22.5 dBm

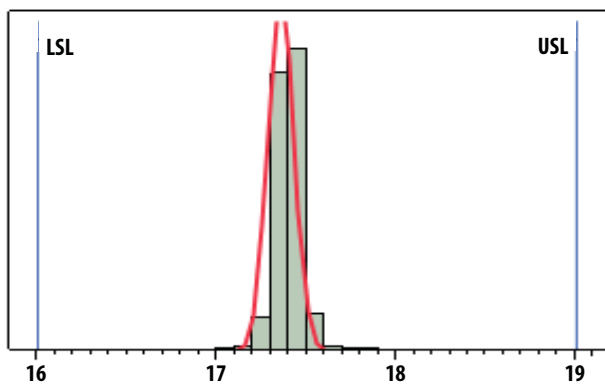


Figure 4. Gain, LSL = 16 dB, nominal = 17.3 dB, USL = 19 dB

Notes:

1. Distribution data sample size is 500 samples taken from 3 different wafer lots. Future wafers allocated to this product may have nominal values anywhere between the upper and lower limits.
2. Circuit trace losses have not been de-embedded from measurements above.

## Demo Board Layout

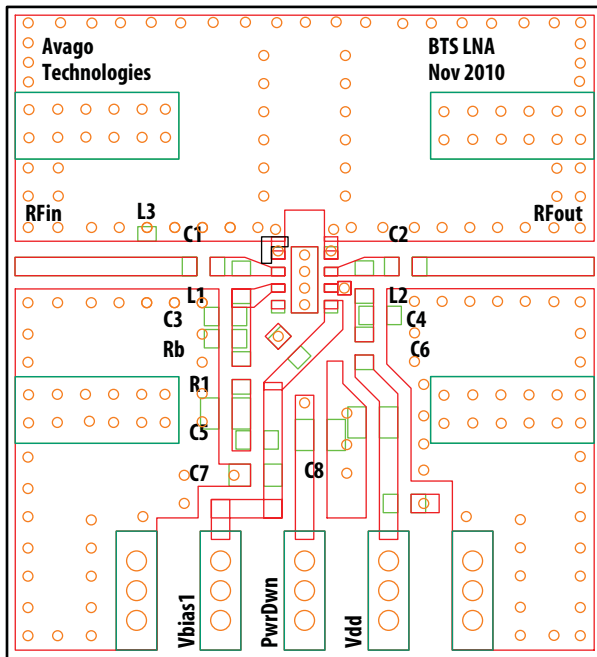
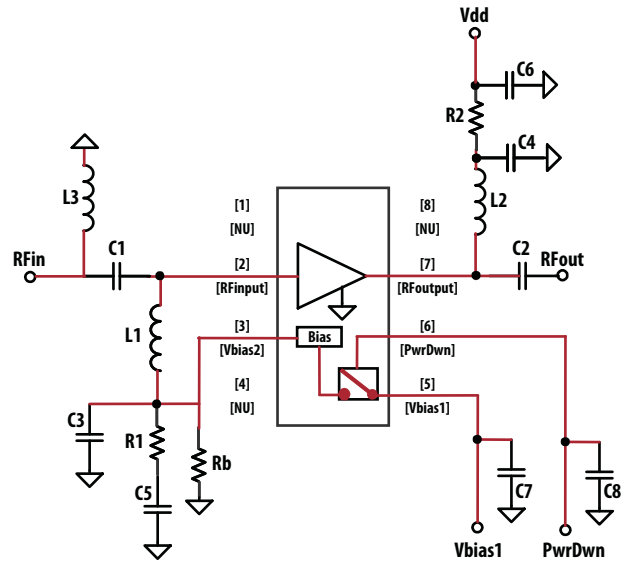


Figure 5. Demo Board Layout Diagram

- Recommended PCB material is 10 mils Rogers RO4350.
- Suggested component values may vary according to layout and PCB material.

## Demo Board Schematic



Truth Table

|                 | V <sub>PwrDwn</sub> (V) |
|-----------------|-------------------------|
| LNA Mode        | 0 or open               |
| Power Down Mode | 3.3                     |

Figure 6. Demo Board Schematic Diagram

### Notes:

- The schematic is shown with the assumption that similar PCB is used for all MGA-636P8, MGA-637P8 and MGA-638P8.
- Detail of the components needed for this product is shown in Table 1.

Table 1. Component list for 1.7 GHz matching

| Part           | Size | Value           | Detail Part Number |
|----------------|------|-----------------|--------------------|
| C1             | 0402 | 100 pF (Murata) | GRM1555C1H101JD01D |
| C2             | 0402 | 150 pF (Murata) | GRM1555C1H151JD01D |
| C5, C6, C7, C8 | 0603 | 4.7 μF (Murata) | GRM188R60J475KE19D |
| C3, C4         | 0402 | Not Used        |                    |
| L1, L2         | 0402 | 8.2 nH (Toko)   | LLP1005-FH8N2C     |
| L3             | 0402 | Not Used        |                    |
| Rb             | 0402 | 750 ohm (Rohm)  | MCR004YZPJ751      |
| R1, R2         | 0402 | 0 ohm (Rohm)    | MCR01MZPJ000       |

### Notes:

- C1, C2 are DC blocking capacitors
- L1 input match for NF
- L2 output match for IP3
- C5, C6, C7, C8 are bypass capacitors
- Rb is the biasing resistor

## Typical Performance

RF performance at  $T_A = 25^\circ\text{C}$ ,  $V_{dd} = 4.8\text{ V}$ ,  $I_{dd} = 75\text{ mA}$ , measured using 50 ohm input and output board unless stated otherwise. IIP3 test condition:  $F_{RF1}-F_{RF2} = 1\text{ MHz}$  with input power of  $-10\text{ dBm}$  per tone.

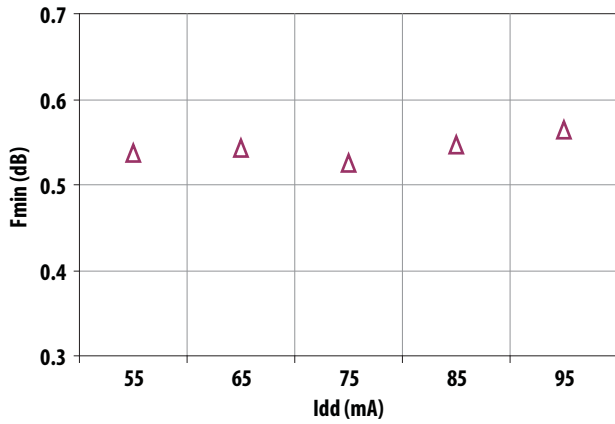


Figure 7. Fmin vs Idd at 4.8 V at 1.7 GHz

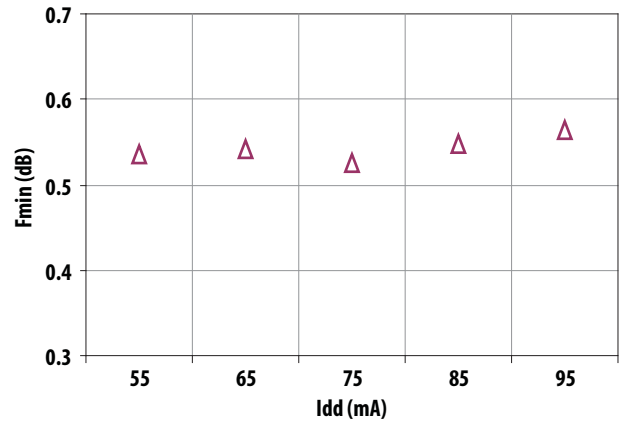


Figure 8. Fmin vs Idd at 4.8 V at 1.9 GHz

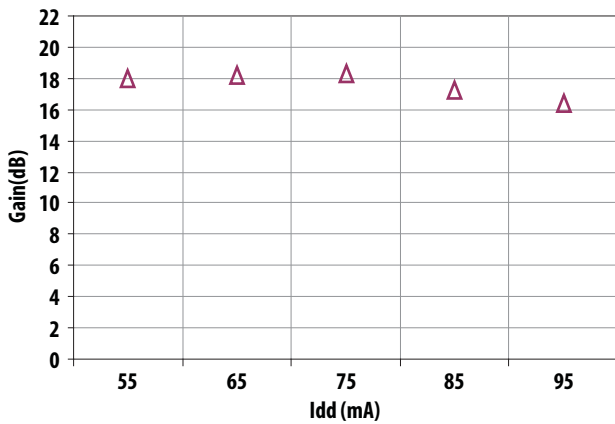


Figure 9. Gain vs Idd at 4.8 V Tuned for Optimum IIP3 and Fmin at 1.7 GHz

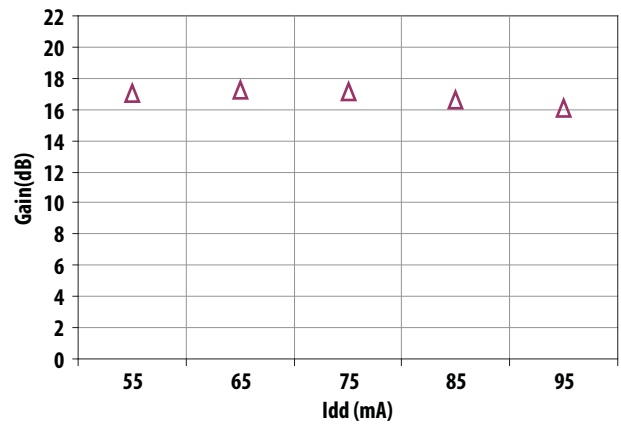


Figure 10. Gain vs Idd at 4.8 V Tuned for Optimum IIP3 and Fmin at 1.9 GHz

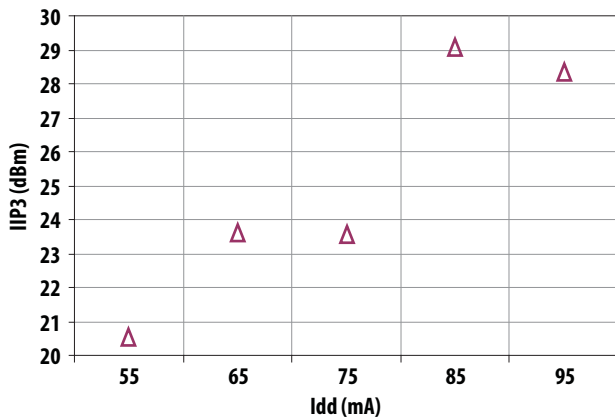


Figure 11. IIP3 vs Idd at 4.8 V Tuned for Optimum IIP3 and Fmin at 1.7 GHz

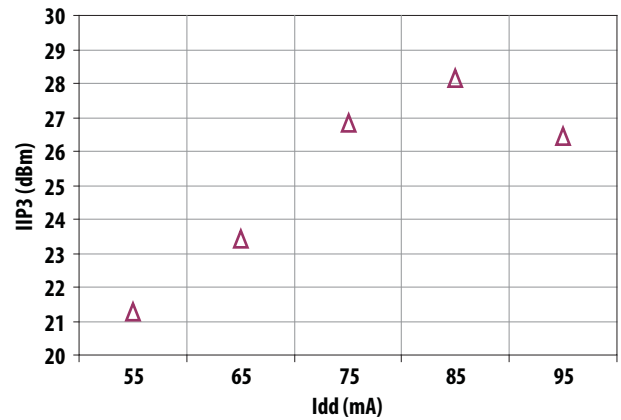


Figure 12. IIP3 vs Idd at 4.8 V Tuned for Optimum IIP3 and Fmin at 1.9 GHz

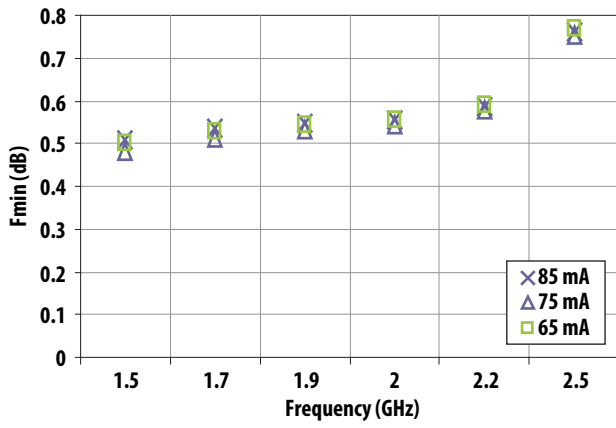


Figure 13. Fmin vs Frequency and Idd at 4.8 V

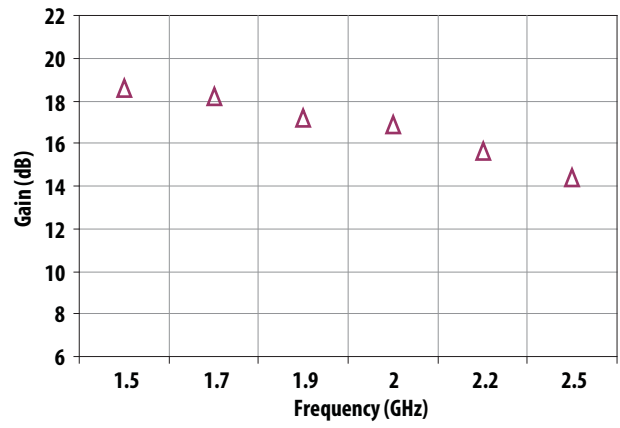


Figure 14. Gain vs Frequency for Optimum IIP3 and Fmin at 4.8 V 75 mA

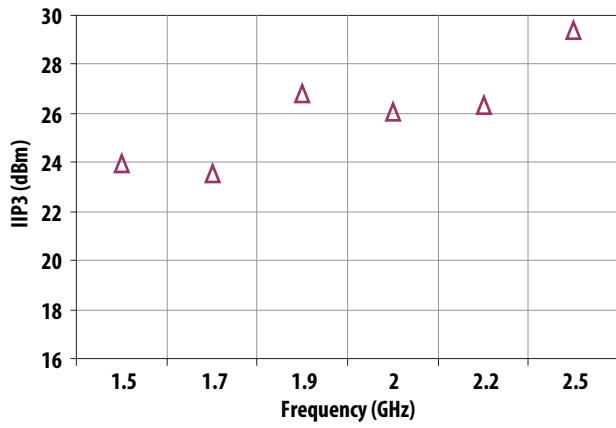
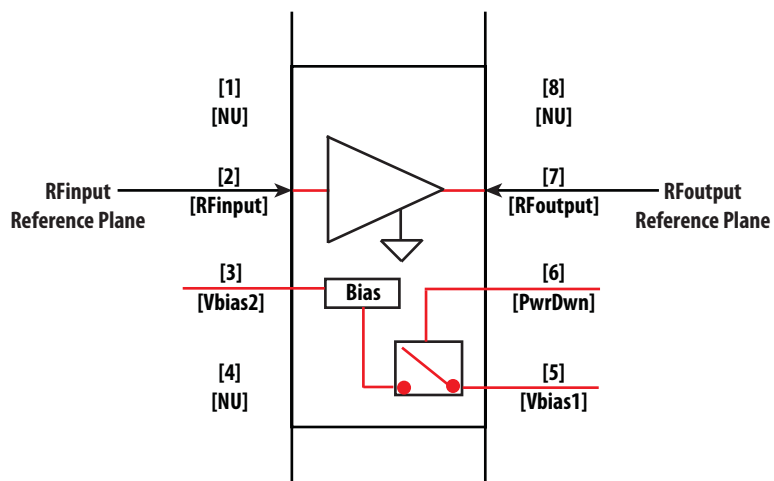


Figure 15. IIP3 vs Frequency for Optimum IIP3 and Fmin at 4.8 V 75 mA

Below is the table showing the MGA-637P8 Reflection Coefficient Parameters tuned for Maximum IIP3, Vdd = 4.8 V, Idd = 75 mA.

| Frequency<br>(GHz) | Gamma Load position |       | IIP3<br>(dBm) |
|--------------------|---------------------|-------|---------------|
|                    | Magnitude           | Angle |               |
| 1.5                | 0.27                | 99.9  | 25.3          |
| 1.7                | 0.27                | 100   | 26            |
| 1.9                | 0.27                | 119.9 | 27.2          |
| 2.0                | 0.27                | 120   | 28            |
| 2.2                | 0.36                | 129.7 | 28.7          |
| 2.5                | 0.36                | 143.9 | 30.2          |



**Figure 16. RFinput and RFoutput Reference Plane**

Notes:

1. The Maximum IIP3 values are calculated based on Load pull measurements on approximately 100 different impedances using Focus Load pull test system.
2. Measurements are conducted on 0.010 inch thick ROGER 4350. The input reference plane is at the end of the RFin pin and the output reference plane is at the end of the RFout pin as shown in Figure 16.



## Typical Performance

RF performance at  $T_A = 25^\circ\text{C}$ ,  $V_{dd} = V_{bias1} = 4.8\text{ V}$ ,  $I_{dd} = 75\text{ mA}$ , LNA mode, measured on demo board in Figure 5. Signal = CW unless stated otherwise. Application Test Circuit is shown in Figure 6 and Table 1. IIP3 test condition:  $F_{RF1}-F_{RF2} = 1\text{ MHz}$  with input power of  $-10\text{ dBm}$  per tone.

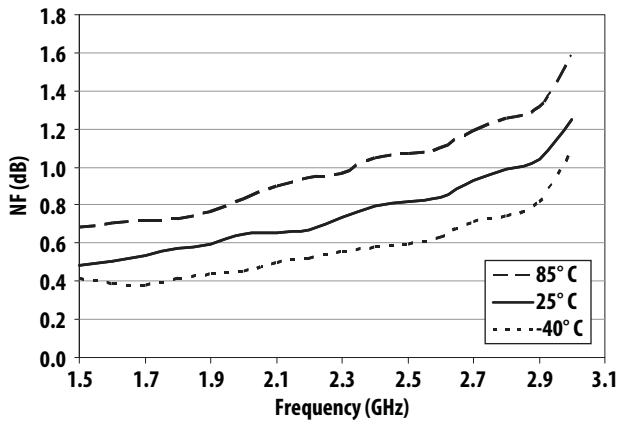


Figure 17. NF vs Frequency vs Temperature

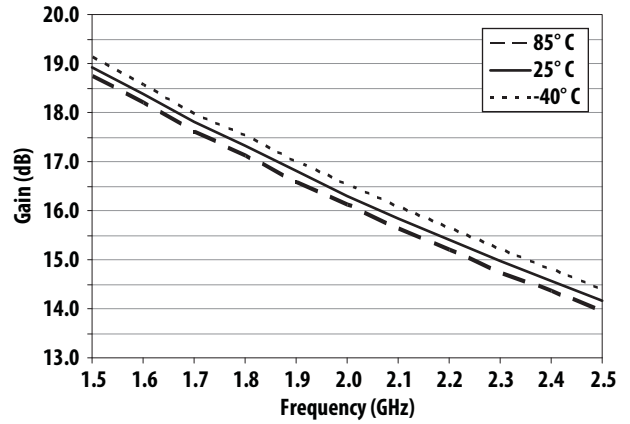


Figure 18. Gain vs Frequency vs Temperature

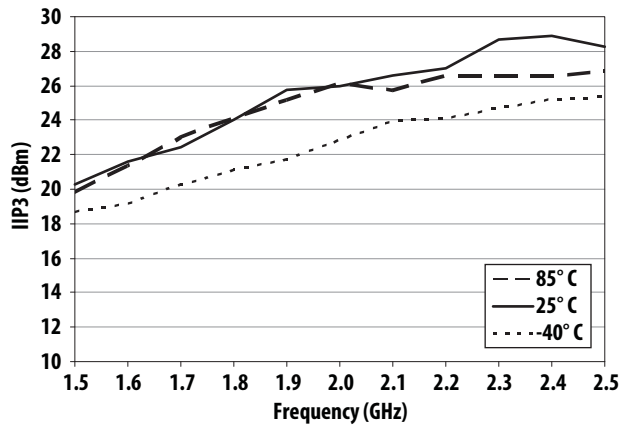


Figure 19. IIP3 vs Frequency vs Temperature

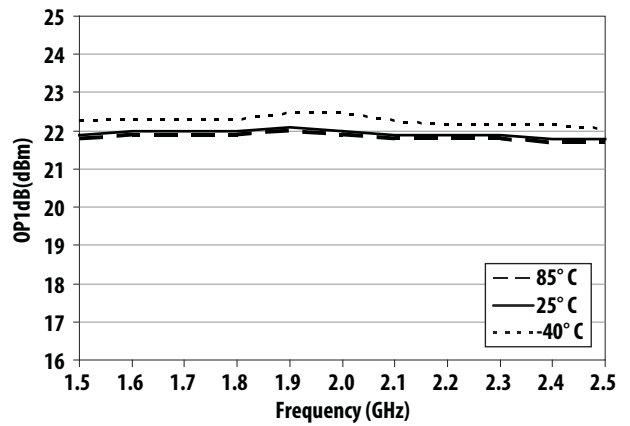


Figure 20. OP1dB vs Frequency vs Temperature

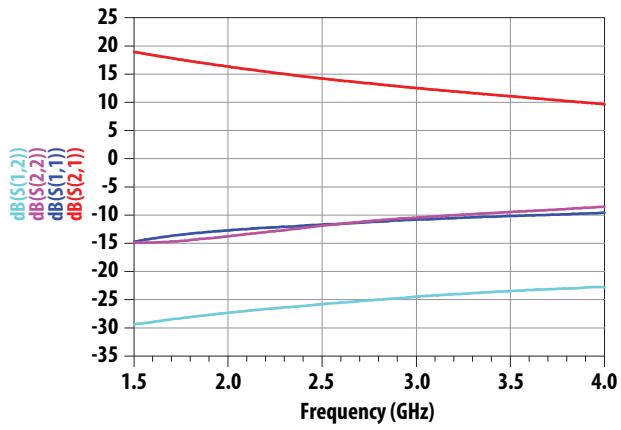


Figure 21. Input Return Loss, Output Return Loss, Gain, Reverse Isolation vs Frequency

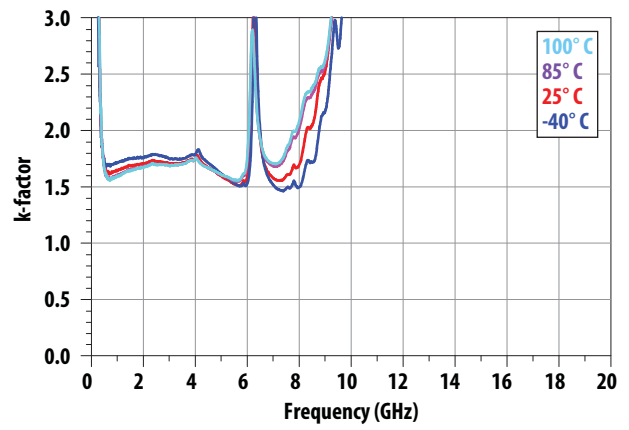


Figure 22. k-factor vs Frequency vs Temperature

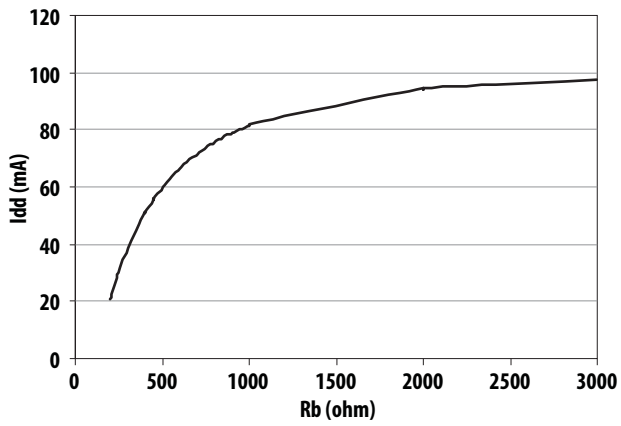


Figure 23. Idd vs Rb

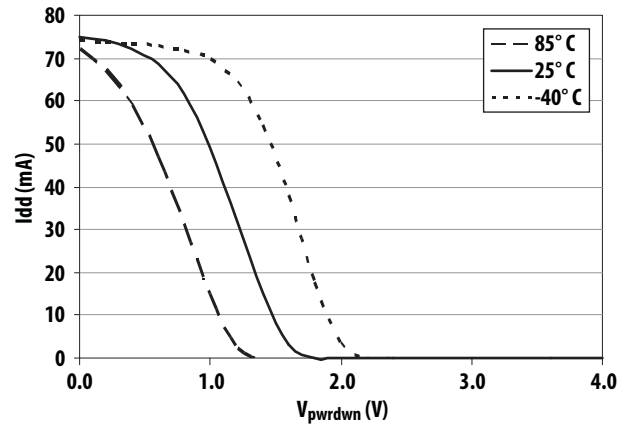


Figure 24. Idd vs  $V_{pwrdown}$

## Typical Scattering Parameters, Vdd = 4.8 V, Idd = 75 mA

### LNA SPAR (100 MHz – 20 GHz)

| Freq (GHz) | S11 (dB) | S11 (ang) | S21 (dB) | S21 (ang) | S12 (dB) | S12 (ang) | S22 (dB) | S22 (ang) |
|------------|----------|-----------|----------|-----------|----------|-----------|----------|-----------|
| 0.1        | -0.329   | -22.999   | 33.852   | 148.157   | -48.343  | 89.846    | -3.56    | -28.96    |
| 0.5        | -5.991   | -75.335   | 26.793   | 101.778   | -37.553  | 68.482    | -9.343   | -42.722   |
| 0.7        | -7.885   | -88.201   | 24.381   | 90.298    | -35.261  | 67.398    | -10.033  | -45.831   |
| 0.9        | -9.209   | -98.163   | 22.444   | 81.171    | -33.47   | 66.028    | -10.32   | -49.973   |
| 1.0        | -9.702   | -101.134  | 21.599   | 77.242    | -32.687  | 65.327    | -10.275  | -51.379   |
| 1.5        | -11.151  | -116.34   | 18.278   | 59.797    | -29.648  | 61.018    | -10.152  | -63.539   |
| 1.7        | -11.276  | -122.907  | 17.226   | 53.244    | -28.7    | 58.653    | -10.128  | -70.304   |
| 1.9        | -11.28   | -128.572  | 16.273   | 46.935    | -27.88   | 56.39     | -10.016  | -77.005   |
| 2.0        | -11.229  | -131.135  | 15.822   | 43.855    | -27.484  | 55.174    | -9.922   | -80.341   |
| 2.5        | -10.769  | -141.135  | 13.846   | 29.093    | -25.85   | 49.214    | -9.277   | -96.744   |
| 3          | -10.175  | -148.444  | 12.169   | 15.07     | -24.576  | 43.459    | -8.47    | -110.824  |
| 3.5        | -9.562   | -154.544  | 10.721   | 1.694     | -23.511  | 37.989    | -7.648   | -122.637  |
| 4          | -8.981   | -159.914  | 9.465    | -10.717   | -22.487  | 33.134    | -6.837   | -133.403  |
| 4.5        | -8.422   | -166.312  | 8.43     | -23.396   | -21.619  | 27.803    | -6.326   | -143.965  |
| 5          | -7.886   | -174.086  | 7.427    | -36.283   | -20.85   | 22.415    | -5.792   | -154.912  |
| 5.5        | -7.37    | 176.754   | 6.479    | -49.17    | -20.128  | 17.017    | -5.329   | -166.087  |
| 6          | -6.889   | 166.657   | 5.61     | -62.254   | -19.395  | 11.192    | -4.972   | -177.579  |
| 7          | -5.56    | 147.502   | 3.617    | -88.076   | -18.321  | -1.075    | -4.149   | 160.414   |
| 8          | -4.329   | 135.731   | 1.376    | -110.949  | -17.688  | -11.478   | -3.34    | 143.283   |
| 9          | -3.729   | 126.937   | -0.866   | -131.216  | -16.886  | -19.591   | -3.081   | 130.105   |
| 10         | -3.502   | 113.258   | -2.61    | -152.59   | -15.681  | -30.852   | -2.366   | 113.034   |
| 11         | -3.208   | 97.368    | -4.934   | -177.297  | -14.905  | -46.367   | -2.241   | 88.557    |
| 12         | -3.079   | 83.346    | -7.811   | 158.326   | -14.427  | -62.459   | -1.971   | 64.563    |
| 13         | -3.133   | 68.38     | -11.487  | 136.173   | -14.497  | -77.811   | -1.813   | 46.387    |
| 14         | -3.002   | 54.811    | -15.566  | 121.173   | -14.455  | -87.355   | -2.709   | 36.535    |
| 15         | -2.865   | 48.736    | -18.501  | 113.896   | -13.135  | -90.676   | -3.722   | 50.249    |
| 16         | -3.111   | 45.795    | -24.606  | 83.96     | -12.225  | -107.4    | -2.615   | 28.754    |
| 17         | -3.355   | 38.022    | -39.471  | -2.18     | -12.552  | -124.888  | -2.917   | 0.394     |
| 18         | -3.245   | 27.651    | -28.848  | -107.903  | -12.801  | -138.791  | -3.083   | -12.005   |
| 19         | -3.219   | 14.019    | -24.346  | -130.67   | -13.289  | -153.698  | -2.647   | -13.451   |
| 20         | -3.289   | -1.387    | -21.594  | -148.155  | -13.144  | -167.611  | -2.637   | -8.352    |

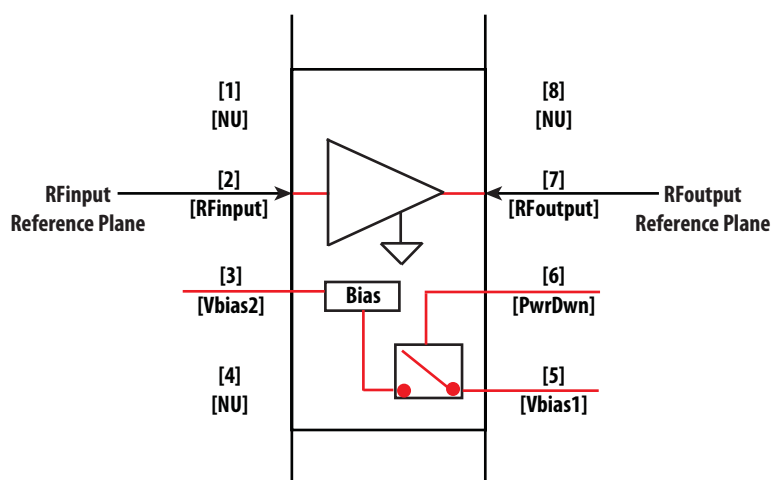


Figure 25. RFinput and RFoutput Reference Plane

### Typical Noise Parameters, $V_{dd} = 4.8\text{ V}$ , $I_{dd} = 75\text{ mA}$

| Freq<br>GHz | Fmin<br>dB | $\Gamma_{opt}$<br>Mag. | $\Gamma_{opt}$<br>Ang. | $R_n/50$ |
|-------------|------------|------------------------|------------------------|----------|
| 1.5         | 0.48       | 0.129                  | 142.7                  | 0.041    |
| 1.7         | 0.51       | 0.151                  | 151.5                  | 0.041    |
| 1.9         | 0.53       | 0.174                  | 160.3                  | 0.038    |
| 2           | 0.54       | 0.185                  | 164.7                  | 0.037    |
| 2.2         | 0.58       | 0.207                  | 173.5                  | 0.037    |
| 2.5         | 0.75       | 0.241                  | 186.6                  | 0.048    |

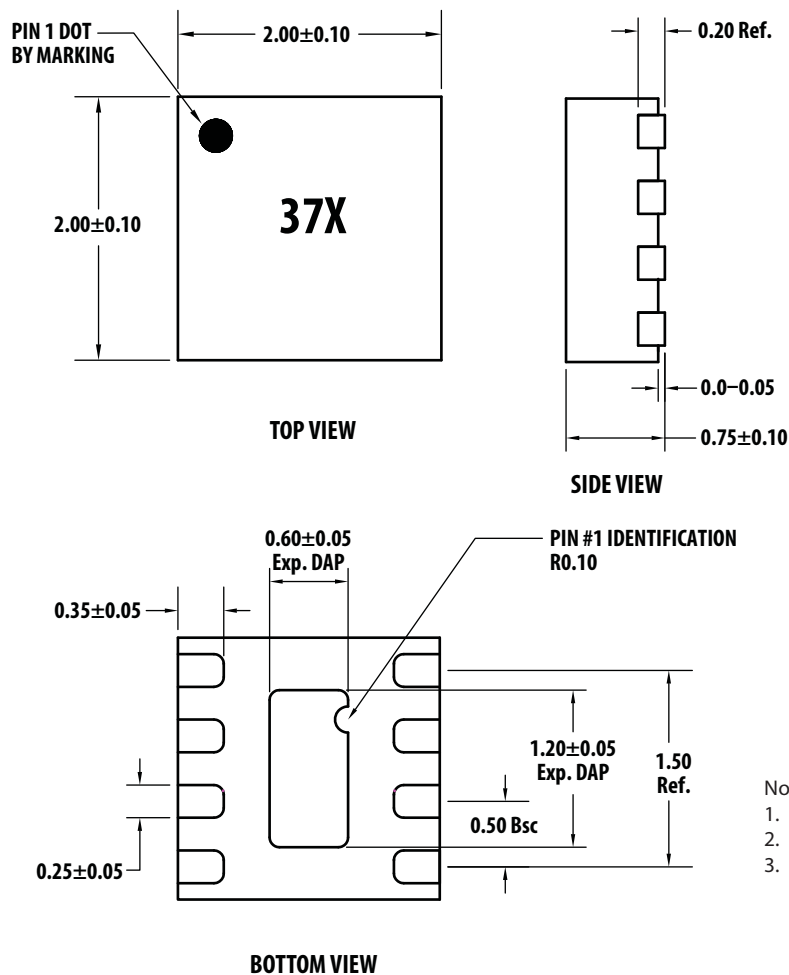
Notes:

1. The Fmin values are based on noise figure measurements at 100 different impedances using Focus source pull test system. From these measurements a true Fmin is calculated.
2. Scattering and noise parameters are measured on coplanar waveguide made on 0.010 inch thick ROGER 4350. The input reference plane is at the end of the RFinpin and the output reference plane is at the end of the RFoutpin as shown in Figure 25.

### Part Number Ordering Information

| Part Number    | No. of Devices | Container      |
|----------------|----------------|----------------|
| MGA-637P8-BLKG | 100            | Antistatic Bag |
| MGA-637P8-TR1G | 3000           | 7 inch Reel    |

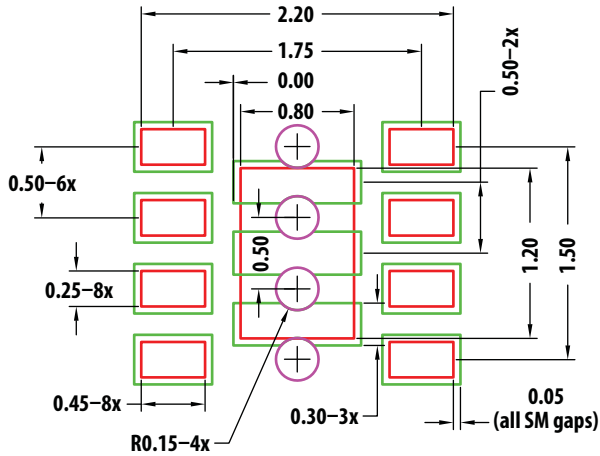
### DFN2X2 Package Dimensions



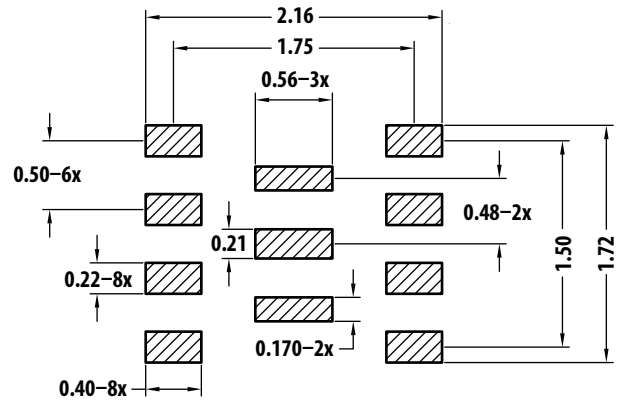
Notes:

1. All dimensions are in millimeters.
2. Dimensions are inclusive of plating.
3. Dimensions are exclusive of mold ash and metal burr.

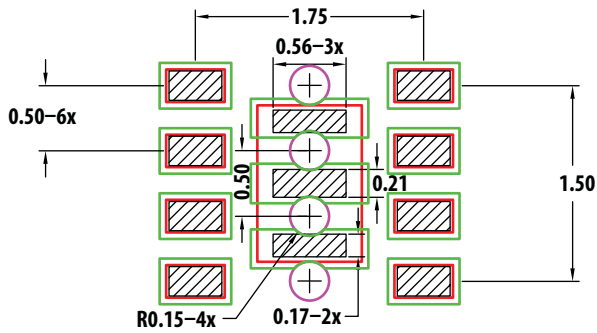
## Recommended PCB Land Pattern and Stencil Design



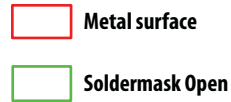
PCB Land Pattern



Stencil Design



Combines PCB & Stencil Design

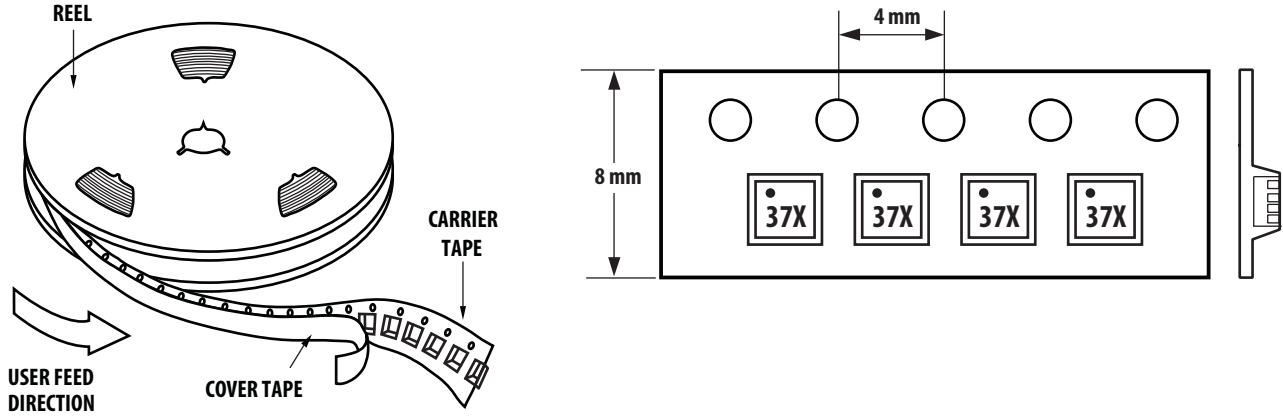


All Dimension are in millimeters

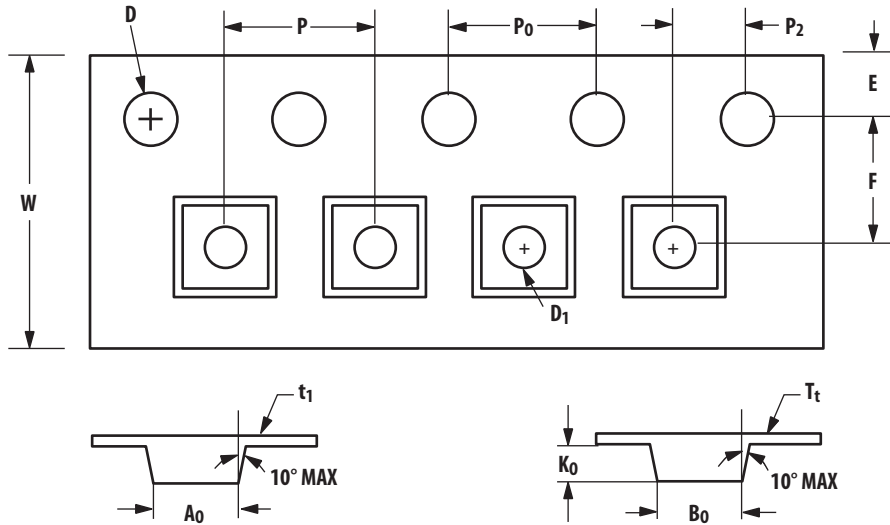
Notes:

1. Stencil thickness is 0.1 mm (4 mils).
2. All dimensions are in mm unless otherwise specified.

## Device Orientation

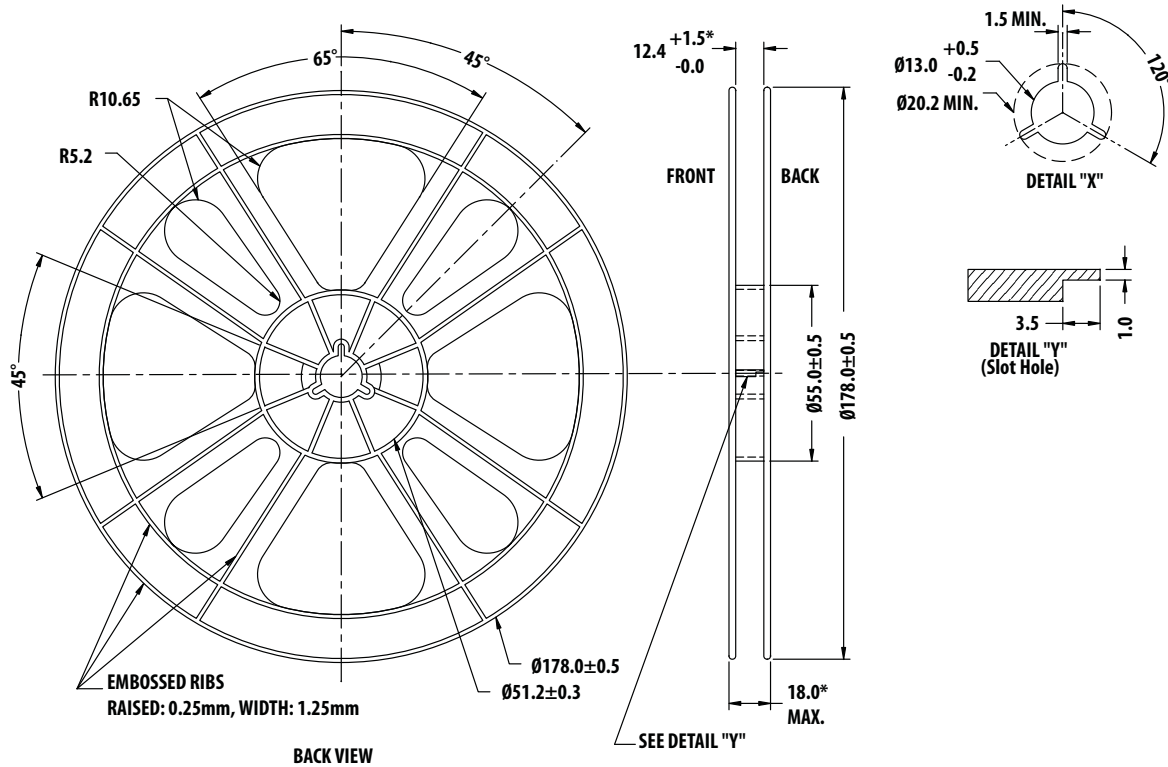
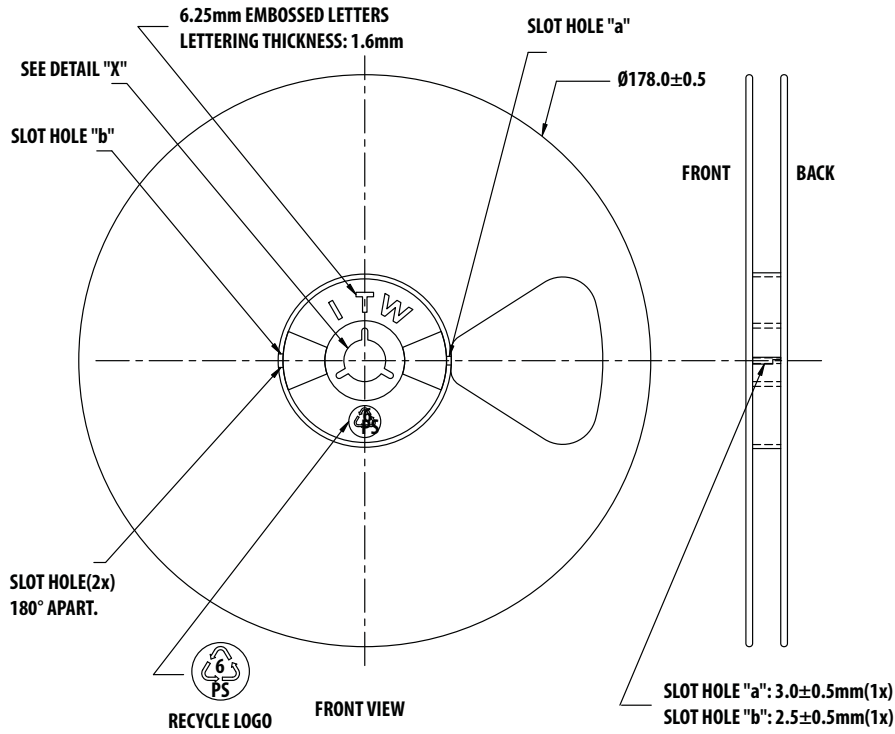


## Tape Dimensions



|              | DESCRIPTION                              | SYMBOL   | SIZE (mm)         | SIZE (INCHES)       |
|--------------|--|----------|-------------------|---------------------|
| CAVITY       | LENGTH                                   | $A_0$    | $2.30 \pm 0.05$   | $0.091 \pm 0.004$   |
|              | WIDTH                                    | $B_0$    | $2.30 \pm 0.05$   | $0.091 \pm 0.004$   |
|              | DEPTH                                    | $K_0$    | $1.00 \pm 0.05$   | $0.039 \pm 0.002$   |
|              | PITCH                                    | $P$      | $4.00 \pm 0.10$   | $0.157 \pm 0.004$   |
|              | BOTTOM HOLE DIAMETER                     | $D_1$    | $1.00 + 0.25$     | $0.039 + 0.002$     |
|              | PERFORATION                              | DIAMETER | $D$               | $1.50 \pm 0.10$     |
| PITCH        |  | $P_0$    | $4.00 \pm 0.10$   | $0.157 \pm 0.004$   |
| POSITION     |  | $E$      | $1.75 \pm 0.10$   | $0.069 \pm 0.004$   |
|              |  | $F$      | $3.50 \pm 0.05$   | $0.138 \pm 0.002$   |
| CARRIER TAPE | WIDTH                                    | $W$      | $8.00 \pm 0.30$   | $0.315 \pm 0.012$   |
|              | THICKNESS                                | $t_1$    | $0.254 \pm 0.02$  | $0.010 \pm 0.0008$  |
| COVER TAPE   | WIDTH                                    | $C$      | $5.4 \pm 0.10$    | $0.205 \pm 0.004$   |
|              | TAPE THICKNESS                           | $T_t$    | $0.062 \pm 0.001$ | $0.0025 \pm 0.0004$ |
| DISTANCE     | CAVITY TO PERFORATION (WIDTH DIRECTION)  | $F$      | $3.50 \pm 0.05$   | $0.138 \pm 0.002$   |
|              | CAVITY TO PERFORATION (LENGTH DIRECTION) | $P_2$    | $2.00 \pm 0.05$   | $0.079 \pm 0.002$   |

# Reel Dimensions – 7 inch



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