

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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MGV12052R2M-10

PHYSICAL DIMENSIONS:

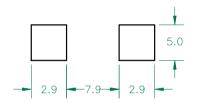
A 13.50 ± 0.50 B 12.60 ± 0.30

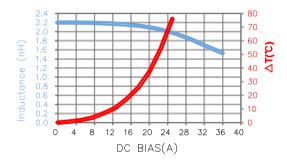
C 5.00 ± 0.30

 $D = 3.60 \pm 0.50$

 $E 2.30 \pm 0.50$

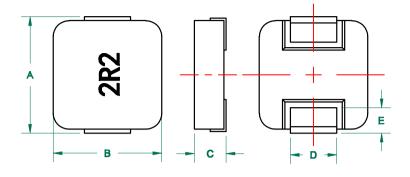
LAND PATTERNS FOR REFLOW SOLDERING





	Min	Nom	Max
INDUCTANCE (uH) L @ 100 KHz/0.25V ± 20%	1.76	2.20	2.64
DCR (Ω)			0.0055

Saturation Current ³ Isat (A)	32.00
Temperature Rise Current Irms⁴(A)	20.00











NOTES: UNLESS OTHERWISE SPECIFIED

- 1.COMPONENTS SHOULD BE ADEQUATELY PREHEATED BEFORE SOLDERING.
- 2.OPERATION TEMPERATURE RANGE: -40°C~+125°C
- 3.SATURATION CURRENT Isat IS DEFINED AS MAXIMUM AMOUNT OF CURRENT BY WHICH INDUCTANCE WILL DROP BY TYPICAL VALUE OF 25% OF INITIAL INDUCTANCE ($Ta=25\pm5^{\circ}C$).
- 4.TEMPERATURE RISE CURRENT (Irms): DEFINITION OF TEMPERATURE RISE CURRENT: DC CURRENT THAT CAUSES THE TEMPERATURE RISE ($\Delta T = 40\,^{\circ}\text{C}$) FROM 25°C AMBIENT.

	DIMENSIONS ARE IN mm.		This print is the property of Laird						
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				reserved.					
				PROJECT/PART NUMBER:	RE			DRAWN BY:	
С	CHANGE TOLERANCE OF D FROM 0.30	03/26/13	QIU	MGV12052R2M-10			OWER UCTOR	QIU	
В	CORRECT MARKING AND DIMENSION	03/21/12	QU	DATE: 1/30/12	CALE:	NTS	SHEET:	'	
Α	ORIGINAL DRAFT	1/30/12	QIU						
REV	DESCRIPTION	DATE	INT		00L #	_	1	of 1	