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The Infinite Bandwidth Company™

MIC5235

Ultra-Low Quiescent Current, 150 mA μ Cap LDO Regulator

Final Information

General Description

The MIC5235 is a 150mA highly accurate, low dropout regulator with high input voltage and ultra low ground current. This combination of high voltage and low ground current makes the MIC5235 ideal for USB and portable electronics applications, using 1-cell, 2-cell or 3-cell Li-Ion battery inputs.

A μ Cap LDO design, the MIC5235 is stable with either ceramic or tantalum output capacitor. It only requires a 2.2 μ F capacitor for stability.

Features of the MIC5235 includes enable input, thermal shutdown, current limit, reverse battery protection, and reverse leakage protection.

Available in 3.0V and adjustable output voltage versions, the MIC5235 is offered in the IttyBitty™ SOT-23-5 package with a junction temperature range of -40°C to $+125^{\circ}\text{C}$.

Features

- Wide input voltage range: 2.3V to 24V
- Ultra low ground current: 18 μ A
- Low dropout voltage of 310mV at 150mA
- High output accuracy of $\pm 2.0\%$ over temperature
- μ Cap: stable with ceramic or tantalum capacitors
- Excellent line and load regulation specifications
- Zero shutdown current
- Reverse battery protection
- Reverse leakage protection
- Thermal shutdown and current limit protection
- IttyBitty SOT-23-5 package

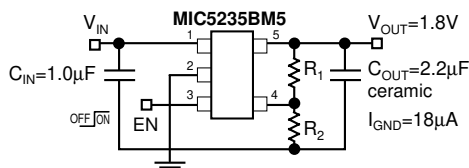
Applications

- USB power supply
- Cellular phones
- Keep-alive supply in notebook and portable computers
- Logic supply for high-voltage batteries
- Automotive electronics
- Battery powered systems

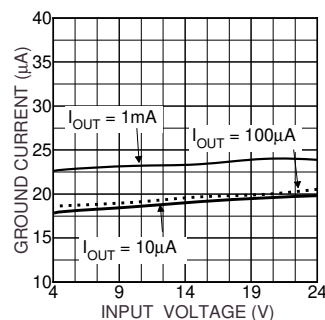
Ordering Information

Part Number	Marking	Voltage	Junction Temp. Range*	Package
MIC5235-3.0BM5	L230	3.0V	-40°C to $+125^{\circ}\text{C}$	SOT-23-5
MIC5235BM5	L2AA	ADJ.	-40°C to $+125^{\circ}\text{C}$	SOT-23-5

Typical Application



Ultra-Low Current Adjustable Regulator Application

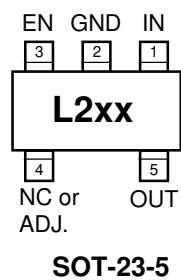


Ground Current vs. Input Voltage

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Pin Configuration



Pin Description

SOT-23-5	Pin Name	Pin Function
1	IN	Supply Input
2	GND	Ground
3	EN	Enable (Input): Logic low = shutdown; logic high = enable
4	NC (fixed)	No Connect
	ADJ (ADJ.)	Adjust (input): Feedback input. Connect to resistive voltage-divider network.
5	OUT	Regulator Output

Absolute Maximum Ratings (Note 1)

Input Supply Voltage	–20V to 38V
Enable Input Voltage	–0.3V to 38V
Power Dissipation	Internally Limited
Junction Temperature	–40°C to +125°C
Storage Temperature	–65°C to +150°C
ESD Rating, Note 3	

Operating Ratings (Note 2)

Input Supply Voltage	2.3V to 24V
Enable Input Voltage	0V to 24V
Junction Temperature (T _J)	–40°C to +125°C
Package Thermal Resistance	
SOT-23-5 (θ _{JA})	235°C/W

Electrical Characteristics

T_A = 25°C with V_{IN} = V_{OUT} + 1V; I_{OUT} = 100μA, **Bold** values indicate –40°C < T_J < +125°C; unless otherwise specified.

Parameter	Condition	Min	Typ	Max	Units
Output Voltage Accuracy	Variation from nominal V _{OUT}	–1.0 –2.0		+1.0 +2.0	% %
Line Regulation	V _{IN} = V _{OUT} + 1V to 24V		0.04		%
Load Regulation	Load = 100μA to 150mA		0.25	1	%
Dropout Voltage	I _{OUT} = 100μA		50		mV
	I _{OUT} = 50mA		230	300	mV
				400	mV
	I _{OUT} = 100mA		270	400	mV
				450	mV
Ground Current	I _{OUT} = 150mA		310	450	mV
				500	mV
	I _{OUT} = 100μA		18	30	μA
				35	μA
	I _{OUT} = 50mA		0.35	0.7	mA
Ground Current in Shutdown	I _{OUT} = 100mA		1	2	mA
	I _{OUT} = 150mA		2	4	mA
Ground Current in Shutdown	V _{EN} ≤ 0.6V; V _{IN} = 24V		0.1	1	μA
Short Circuit Current	V _{OUT} = 0V		350	500	mA
Output Leakage, Reverse Polarity Input	Load = 500Ω; V _{IN} = –15V		–0.1		μA

Enable Input

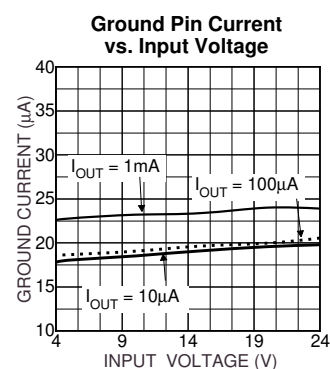
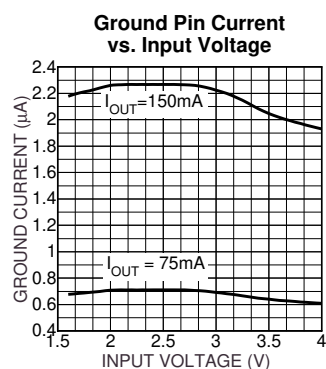
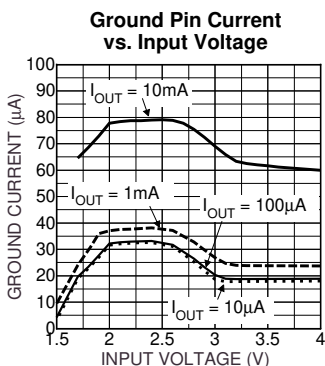
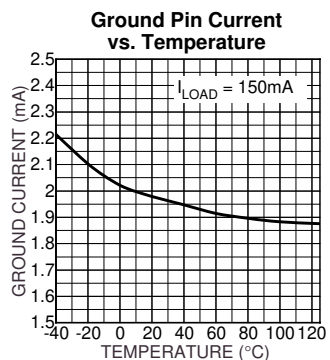
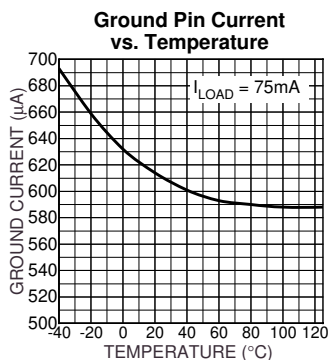
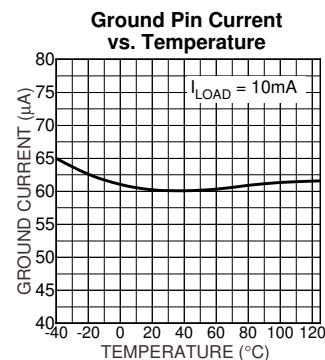
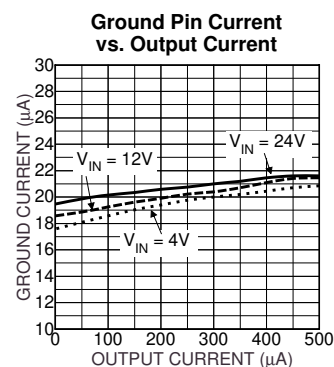
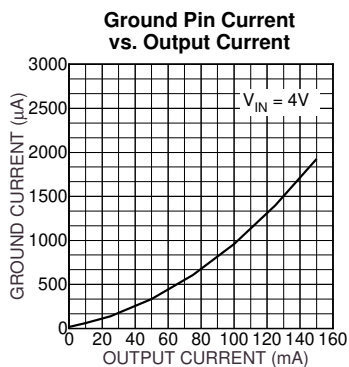
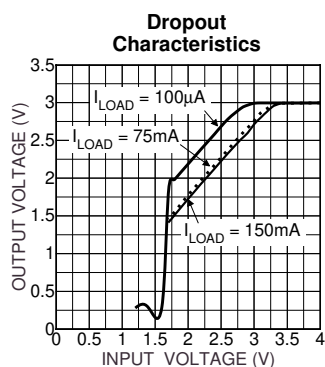
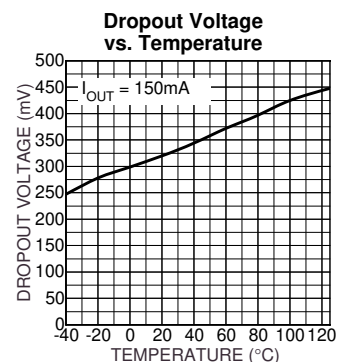
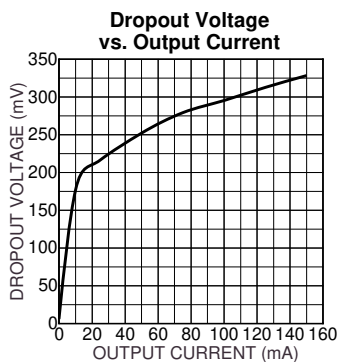
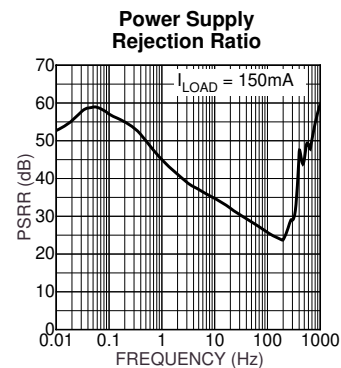
Input Low Voltage	Regulator OFF			0.6	V
Input High Voltage	Regulator ON	2.0			V
Enable Input Current	V _{EN} = 0.6V; Regulator OFF	–1.0	0.01	1.0	μA
	V _{EN} = 2.0V; Regulator ON		0.1	1.0	μA
	V _{EN} = 24V; Regulator ON		0.5	2.5	μA

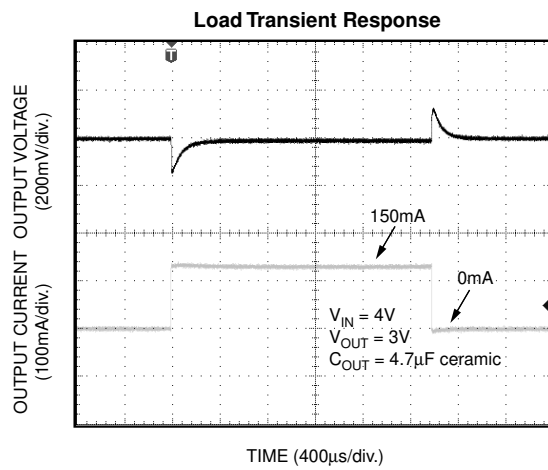
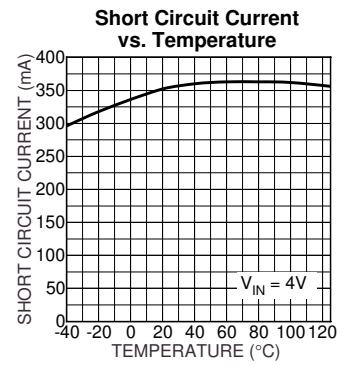
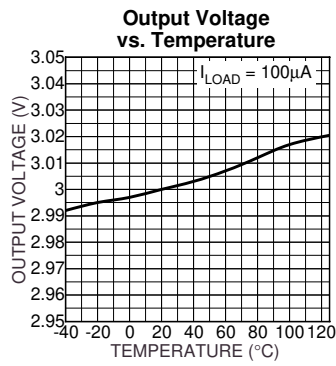
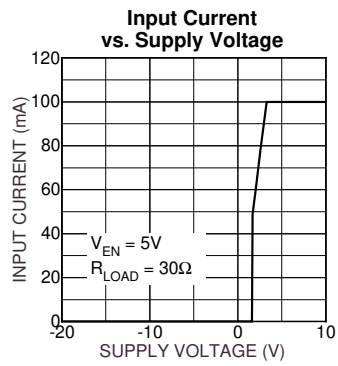
Note 1. Exceeding the absolute maximum rating may damage the device.

Note 2. The device is not guaranteed to function outside its operating rating.

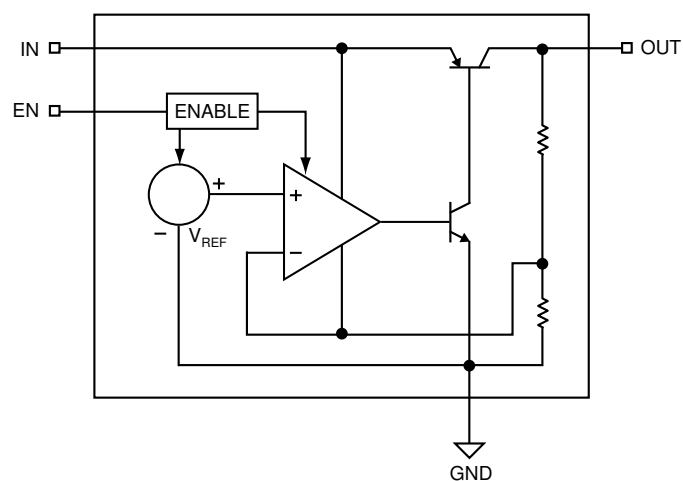
Note 3. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.

Typical Characteristics (MIC5235-3.0BM5)

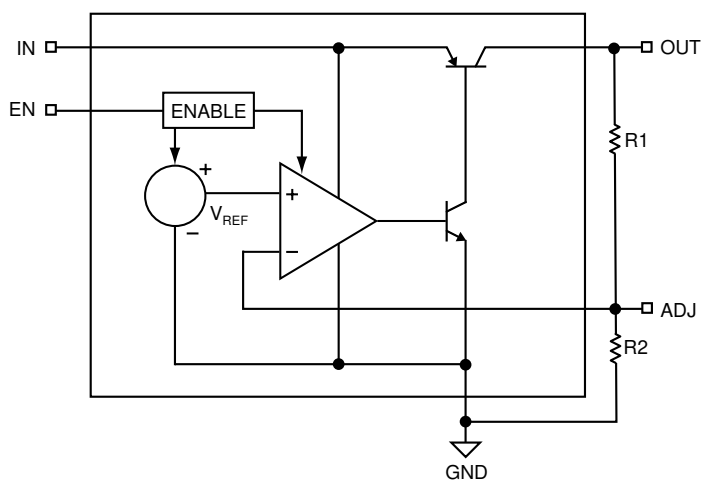




Functional Diagram



Block Diagram – Fixed Output Voltage



Block Diagram – Adjustable Output Voltage

Applications Information

Enable/Shutdown

The MIC5235 comes with an active-high enable pin that allows the regulator to be disabled. Forcing the enable pin low disables the regulator and sends it into a “zero” off-mode-current state. In this state, current consumed by the regulator goes nearly to zero. Forcing the enable pin high enables the output voltage.

Input Capacitor

The MIC5235 has high input voltage capability up to 24V. The input capacitor must be rated to sustain voltages that may be used on the input. An input capacitor may be required when the device is not near the source power supply or when supplied by a battery. Small, surface mount, ceramic capacitors can be used for bypassing. Larger values may be required if the source supply has high ripple.

Output Capacitor

The MIC5235 requires an output capacitor for stability. The design requires 2.2μF or greater on the output to maintain stability. The design is optimized for use with low-ESR ceramic chip capacitors. High ESR capacitors may cause high frequency oscillation. The maximum recommended ESR is 3Ω. The output capacitor can be increased without limit. Larger valued capacitors help to improve transient response.

X7R/X5R dielectric-type ceramic capacitors are recommended because of their temperature performance. X7R-type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change value by as much as 50% and 60% respectively over their operating temperature ranges. To use a ceramic chip capacitor with Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

No-Load Stability

The MIC5235 will remain stable and in regulation with no load unlike many other voltage regulators. This is especially important in CMOS RAM keep-alive application.

Thermal Consideration

The MIC5235 is designed to provide 150mA of continuous current in a very small package. Maximum power dissipation can be calculated based on the output current and the voltage drop across the part. To determine the maximum power dissipation of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_{D(MAX)} = \left(\frac{T_{J(MAX)} - T_A}{\theta_{JA}} \right)$$

$T_{J(MAX)}$ is the maximum junction temperature of the die, 125°C, and T_A is the ambient operating temperature. θ_{JA} is layout dependent; Table 1 shows examples of the junction-to-ambient thermal resistance for the MIC5235.

Package	θ_{JA} Recommended Minimum Footprint
SOT-23-5	235°C/W

Table 1. SOT-23-5 Thermal Resistance

The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT})I_{OUT} + V_{IN}I_{GND}$$

Substituting $P_{D(MAX)}$ for P_D and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. For example, when operating the MIC5235-3.0BM5 at 50°C with a minimum footprint layout, the maximum input voltage for a set output current can be determined as follows:

$$P_{D(MAX)} = \left(\frac{125^\circ\text{C} - 50^\circ\text{C}}{235^\circ\text{C/W}} \right)$$

$$P_{D(MAX)} = 319\text{mW}$$

The junction-to-ambient (θ_{JA}) thermal resistance for the minimum footprint is 235°C/W, from Table 1. It is important that the maximum power dissipation not be exceeded to ensure proper operation. Since the MIC5235 was designed to operate with high input voltages, careful consideration must be given so as not to overheat the device. With Very high input-to-output voltage differentials, the output current is limited by the total power dissipation. Total power dissipation is calculated using the following equation:

$$P_D = (V_{IN} - V_{OUT})I_{OUT} + V_{IN} \times I_{GND}$$

Due to the potential for input voltages up to 24V, ground current must be taken into consideration.

If we know the maximum load current, we can solve for the maximum input voltage using the maximum power dissipation calculated for a 50°C ambient, 319mW

$$\begin{aligned} P_{D(MAX)} &= (V_{IN} - V_{OUT})I_{OUT} + V_{IN} \times I_{GND} \\ 319\text{mW} &= (V_{IN} - 3V)150\text{mA} + V_{IN} \times 2.8\text{mA} \end{aligned}$$

Ground pin current is estimated using the typical characteristics of the device.

$$769\text{mW} = V_{IN} (152.8\text{mA})$$

$$V_{IN} = 5.03\text{V}$$

For higher current outputs only a lower input voltage will work for higher ambient temperatures.

Assuming a lower output current of 20mA, the maximum input voltage can be recalculated:

$$319\text{mW} = (V_{IN} - 3V)20\text{mA} + V_{IN} \times 0.2\text{mA}$$

$$379\text{mW} = V_{IN} \times 20.2\text{mA}$$

$$V_{IN} = 18.8\text{V}$$

Maximum input voltage for a 20mA load current at 50°C ambient temperature is 18.8V, utilizing virtually the entire operating voltage range of the device.

Adjustable Regulator Application

The MIC5235BM5 can be adjusted from 1.24V to 20V by using two external resistors (Figure 1). The resistors set the output voltage based on the following equation:

$$V_{OUT} = V_{REF} \left(1 + \left(\frac{R_1}{R_2} \right) \right)$$

Where $V_{REF} = 1.24V$.

Feedback resistor R2 should be no larger than 300k Ω .

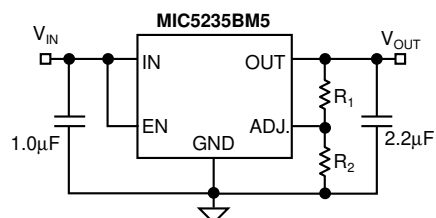
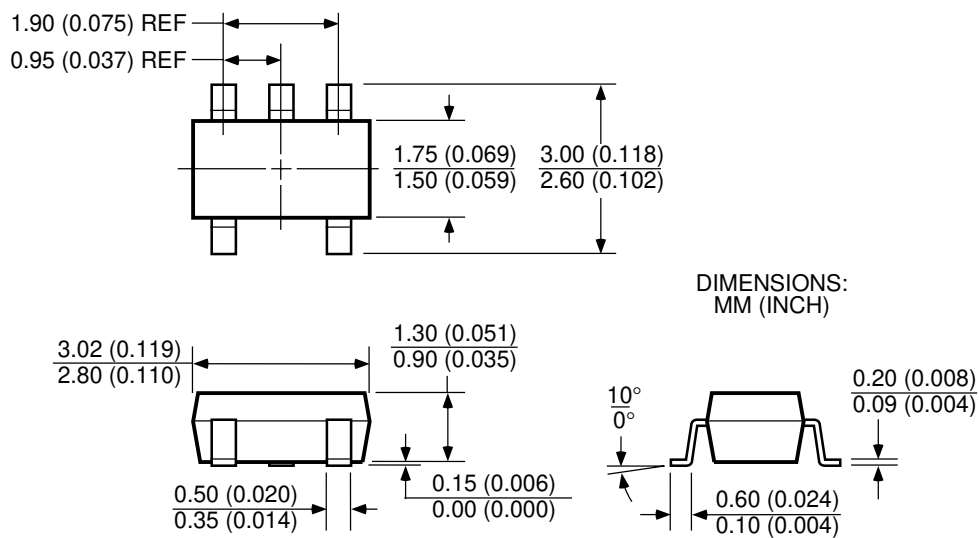


Figure 1. Adjustable Voltage Application

Package Information



SOT-23-5 (M5)

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