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µCap Negative Low-Dropout Regulator

### **General Description**

The MIC5271 is a  $\mu$ Cap 100mA negative regulator in a SOT-23-5 package. With better than 2% initial accuracy, this regulator provides a very accurate supply voltage for applications that require a negative rail. The MIC5271 sinks 100mA of output current at very low dropout voltage (600mV maximum at 100mA of output current).

The  $\mu$ Cap regulator design is optimized to work with low-value, low-cost ceramic capacitors. The output typically requires only a 1 $\mu$ F capacitance for stability.

Designed for applications where small packaging and efficiency are critical, the MIC5271 combines LDO design expertise with IttyBitty<sup>®</sup> packaging to improve performance and reduce power dissipation. Ground current is optimized to help improve battery life in portable applications. The MIC5271 also includes a TTL-compatible enable pin, allowing the user to put the part into a zero-current off mode.

The MIC5271 is available in the 5-pin SOT-23 package for space saving applications and it is available with an adjustable output.

Datasheets and support documentation are available on Micrel's web site at: <u>www.micrel.com</u>.

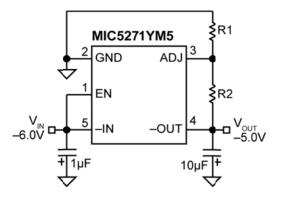
#### **Features**

- · Stable with ceramic or tantalum capacitor
- · Positive and negative enable thresholds
- Low dropout voltage: -500mV @ -100mA
- Low ground current: -25μA @ load = -100μA
- Tight initial accuracy: ±2%
- Tight load and line regulation
- Thermal shutdown and current-limit protection
- IttyBitty 5-pin SOT-23 packaging
- Zero-current off mode

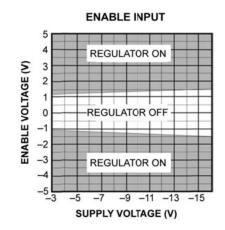
#### Applications

- GaAsFET bias
- · Portable cameras and video recorders
- PDAs
- Battery-powered equipment
- Post-regulation of DC-to-DC converters

## **Typical Application**



**Regulator with Adjustable Negative Voltage Output** 



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## **Ordering Information**

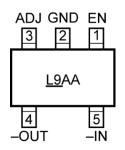
Part Number <sup>(1)</sup>	Marking <sup>(2)</sup>	Voltage	Junction Temperature Range	Package
MIC5271YM5	<u>L9</u> AA	Adjustable	-40°C to +125°C	SOT-23-5
MIC5271-3.0YM5	<u>L9</u> 30	–3.0V	-40°C to +125°C	SOT-23-5
MIC5271-5.0YM5	<u>L9</u> 50	–5.0V	-40°C to +125°C	SOT-23-5

Note:

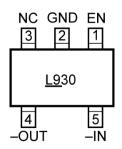
1. Other voltages are available. Contact Micrel for details.

2. Underbar (\_) symbol may not be to scale.

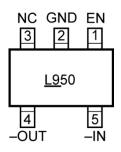
## **Pin Configurations**



MIC5271YM5 Adjustable Output Voltage (Top View)



MIC5271-3.0YM5 Fixed Output Voltage (Top View)



MIC5271-5.0YM5 Fixed Output Voltage (Top View)

### **Pin Description**

Pin Number	MIC5271YM5	MIC5271-3.0YM5	MIC5271-5.0YM5	Pin Function		
Pili Nulliber	Pin Name	Pin Name	Pin Name			
1	EN	EN	EN	Enable Input. TTL logic-compatible enable input. Logic HIGH = ON, Logic LOW or open = OFF.		
2	GND	GND	GND	Ground		
3	ADJ	_	_	Adjustable (Input): Adjustable feedback output connects to resistor voltage divider.		
	_	NC	NC	No Connect. Leave unconnected.		
4	–OUT	-OUT	-OUT	Negative Regulator Output.		
5	-IN	–IN	-IN	Negative Supply Input.		

## Absolute Maximum Ratings<sup>(3)</sup>

Input Voltage (V <sub>-IN</sub> )	20V to +0.3V
Enable Voltage (V <sub>EN</sub> )	20V to +20V
Power Dissipation (P <sub>D</sub> )	Internally Limited
Junction Temperature (T <sub>J</sub> )	40°C to +125°C
Lead Temperature (soldering, 10s)	260°C
Storage Temperature (T <sub>S</sub> )	–65°C to +150°C
ESD Rating	Note 6

## **Operating Ratings**<sup>(4)</sup>

Input Voltage (V-IN)	–16V to –3.3V
Enable Voltage (V <sub>EN</sub> )	–16V to +16V
	–40°C to +125°C
Thermal Resistance $(\theta_{JA})^{(5)}$ .	235°C/W

## Electrical Characteristics<sup>(7)</sup>

 $V_{\text{-IN}} = V_{\text{-OUT}} - 1.0V; C_{\text{OUT}} = 4.7 \mu F, I_{\text{OUT}} = 100 \mu A; T_{\text{J}} = +25^{\circ}C, \text{ bold } \text{values indicate } -40^{\circ}C \leq T_{\text{J}} \leq +125^{\circ}C; \text{ unless otherwise noted.}$ 

Symbol	Parameter	Condition	Min.	Тур.	Max.	Units
V <sub>-OUT</sub>	Output Voltage Accuracy	Variation from nominal V <sub>-OUT</sub>	-2		+2	%
			-3		+3	
$\Delta V_{\text{-OUT}} / \Delta T$	Output Voltage Temperature Coefficient	Note 8		100		ppm/°C
$\Delta V_{\text{-OUT}}/V_{\text{-OUT}}$	Line Regulation	$V_{-IN} = V_{-OUT} - 1V$ to $-16V$		0.04	0.15	%/V
					0.2	
$\Delta V_{\text{-OUT}}/V_{\text{-OUT}}$	Load Regulation	I <sub>OUT</sub> = -100μA to -100mA, Note 9		0.4	1.8	%
					2.0	
V-in — V-out	Dropout Voltage, Note 10	I <sub>OUT</sub> = -100μA		-55		mV
		I <sub>OUT</sub> = -50mA		-360	-500	
		I <sub>OUT</sub> = -100mA		-500	-700	
					-900	
I <sub>GND</sub>		I <sub>OUT</sub> = -100μA		-25	-100	μA
	Ground Current, Note 11	I <sub>OUT</sub> = -50mA		-0.9		mA
		I <sub>OUT</sub> = -100mA		-2.0	-3.0	
I <sub>GND_SD</sub>	Ground Current in Shutdown	$V_{EN} = \pm 0.6 V$	-1.0	0.1	+1.0	μA
PSRR	Ripple Rejection	f = 120Hz		50		dB
I <sub>LIMIT</sub>	Current Limit	$V_{-OUT} = 0V$		235	350	mA

Notes:

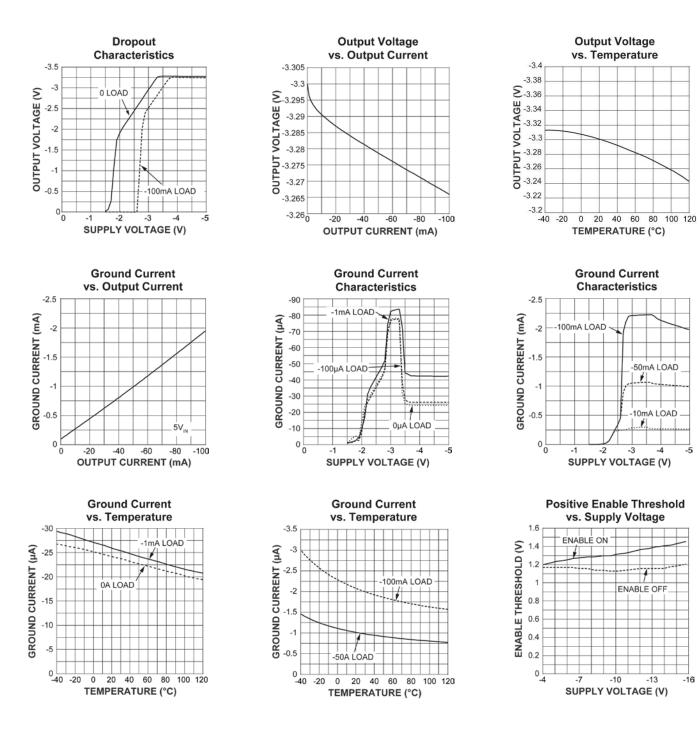
- 3. Exceeding the absolute maximum ratings may damage the device.
- 4. The device is not guaranteed to function outside its operating ratings.
- 5. The maximum allowable power dissipation is a function of the maximum junction temperature,  $T_{J(max)}$  the junction-to-ambient thermal resistance,  $\theta_{JA}$ , and the ambient temperature,  $T_A$ . The maximum allowable power dissipation at any ambient temperature is calculated using:  $P_{D(max)} = (T_{J(max)} T_A) \div \theta_{JA}$ , where  $\theta_{JA}$  is 235°C/W. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown. See the "Thermal Considerations" sub-section in the *Application Information* for details.
- 6. Devices are ESD sensitive. Handling precautions are recommended.
- 7. Specification for packaged product only.
- 8. Output voltage temperature coefficient is defined as the worst case voltage change divided by the total temperature range.
- Regulation is measured at constant junction temperature using low duty cycle pulse testing. Parts are tested for load regulation in the load range from 100μA to 100mA. Changes in output voltage due to heating effects are covered by the thermal regulation specification.
- 10. Dropout voltage is defined as the input to output differential at which the output voltage drops 2% below its nominal value measured at 1V differential.
- 11. Ground pin current is the regulator quiescent current plus pass transistor base current. The total current drawn from the supply is the sum of the load current plus the ground pin current.

## Electrical Characteristics<sup>(7)</sup> (Continued)

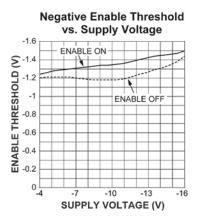
 $V_{-IN} = V_{-OUT} - 1.0V; C_{OUT} = 4.7 \mu F, I_{OUT} = 100 \mu A; T_J = +25^{\circ}C, \text{ bold } values indicate - 40^{\circ}C \leq T_J \leq +125^{\circ}C; unless otherwise noted.$ 

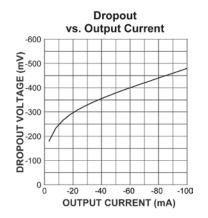
Symbol	Parameter	Condition	Min.	Тур.	Max.	Units	
T <sub>ON</sub>	Turn-On Time	Time to $V_{OUT} = 90\%$ (nominal)		60		μs	
V <sub>EN</sub>	Input Low Voltage	Regulator OFF			±0.6	V	
	Input high Voltage	Regulator ON	±2.0				
I <sub>EN</sub>	Enable Input Current	$V_{EN} = \pm 0.6V$ and $-2.0V$			0.1		
		V <sub>EN</sub> = +2.0V		5.6	10.0	μΑ	

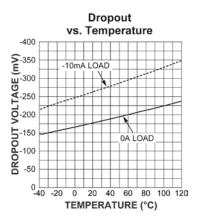
## **Typical Characteristics**

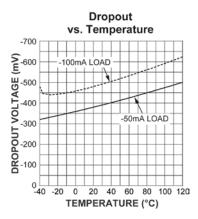


## **Typical Characteristics (Continued)**

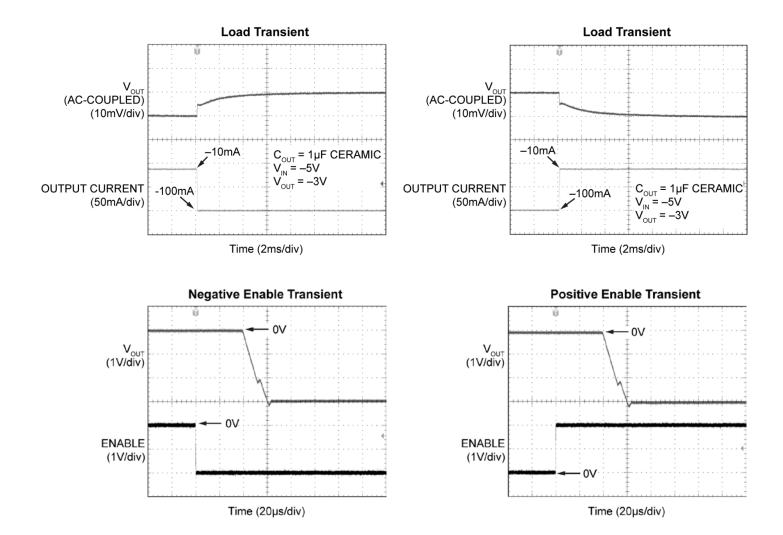








## **Functional Characteristics**



## **Functional Diagrams**

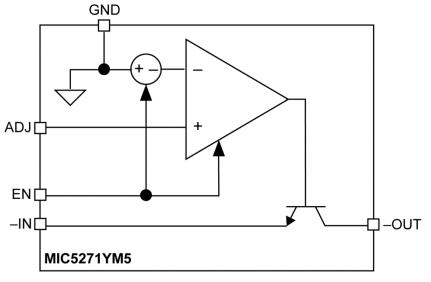
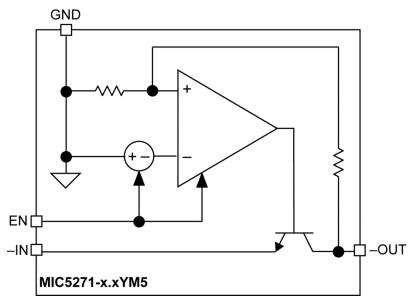
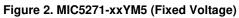


Figure 1. MIC5271YM5 (Adjustable Voltage)





## **Application Information**

The MIC5271 is a general-purpose negative voltage regulator that can be used in a system that requires a clean negative voltage. This includes the post regulation of DC-to-DC converters (transformer or charge pump based voltage converters). These negative voltages typically require a negative low dropout voltage regulator to provide a clean output from noisy input power.

#### Input Capacitor

A  $1\mu$ F input capacitor should be placed from –IN to GND if there is more than two inches of wire or trace between the input and the AC filter capacitor or if a battery is used as the input.

#### **Output Capacitor**

The MIC5271 requires an output capacitor for stable operation. A minimum of  $1\mu$ F of output capacitance is required. The output capacitor can be increased without limitation to improve transient response. The output does not require ESR to maintain stability; therefore a ceramic capacitor can be used. High-ESR capacitors may cause instability. Capacitors with an ESR of  $3\Omega$  or greater at 100kHz can cause a high-frequency oscillation.

Low-ESR tantalums are recommended due to the tight capacitance tolerance over temperature. The Z5U dielectric can change capacitance value by as much 50% over temperature, and the Y5V dielectric can change capacitance value by as much as 60% over temperature. To use a ceramic chip capacitor with the Y5V dielectric, the value must be much higher than a tantalum to ensure the same minimum capacitor value over temperature.

#### **No-Load Stability**

The MIC5271 does not require a load for stability.

#### Enable Input

The MIC5271 comes with an enable pin that allows the regulator to be disabled. Forcing the enable pin higher than the negative threshold and lower than the positive threshold disables the regulator and sends it into a "zero" off-mode current state. In this state, current consumed by the regulator goes nearly to zero. The MIC5271 will be in the "on" mode when the voltage applied to the enable pin is either greater than the positive threshold or less than the negative threshold.

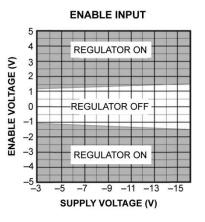


Figure 3. Positive and Negative Enable Voltage vs. Supply Voltage

#### Thermal Considerations

Absolute values will be used for thermal calculations to clarify the meaning of power dissipation and voltage drops across the part.

Proper thermal design for the MIC5271-5.0YM5 can be accomplished with some basic design criteria and some simple equations. The following information must be known to implement your regulator design:

- V<sub>IN</sub> = Input voltage
- V<sub>OUT</sub> = Output voltage
- I<sub>OUT</sub> = Output current
- T<sub>A</sub> = Ambient operating temperature
- I<sub>GND</sub> = Ground current

Maximum power dissipation can be determined by knowing the ambient temperature  $(T_A)$ , the maximum junction temperature (+125°C), and the thermal resistance (junction-to-ambient). The thermal resistance for this part, assuming a minimum footprint board layout, is +235°C/W. The maximum power dissipation at an ambient temperature of +25°C can be determined with Equations 1 and 2:

$$P_{D(MAX)} = \frac{T_{J(MAX)} - T_A}{\theta_{JA}} \qquad \qquad \text{Eq. 1}$$

$$P_{D(MAX)} = \frac{125^{\circ}C - 25^{\circ}C}{235^{\circ}C/W}$$
 Eq. 2

Where  $P_{D(MAX)} = +425mW$ .

The actual power dissipation of the regulator circuit can be determined using Equation 3:

$$P_{D} = (V_{IN} - V_{OUT}) \times I_{OUT} + (V_{IN} \times I_{GND})$$
 Eq. 3

Substituting  $P_{D(MAX)}$ , determined above, for  $P_D$  and solving for the operating conditions that are critical to the application will give the maximum operating conditions for the regulator circuit. The maximum power dissipation number cannot be exceeded for proper operation of the device. The maximum input voltage can be determined using the output voltage of 5.0V and an output current of 100mA. Ground current, of 2mA for 100mA of output current, can be taken from the *Electrical Characteristics*.

- $425mW = (V_{IN} 5.0V)100mA + V_{IN} \times 2mA$
- $425mW = (100mA \times V_{IN} + 2mA \times V_{IN}) -500mW$
- $925mW = 102mA \times V_{IN}$
- V<sub>IN</sub> = 9.07V (maximum)

Therefore, a -5.0V application at -100mA of output current can accept a maximum input voltage of -9.07V in a SOT-23 package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to "Regulator Thermals" section of Micrel's <u>Designing with</u> <u>Low Dropout Voltage Regulators</u> handbook.

#### Adjustable Regulator Application

The MIC5271YM5 can be adjusted from -1.20V to -14V by using two external resistors (Figure 4). The resistors set the output voltage based on Equation 4.

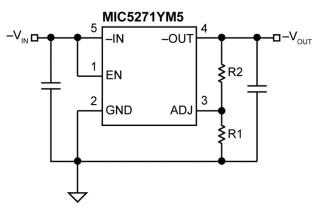
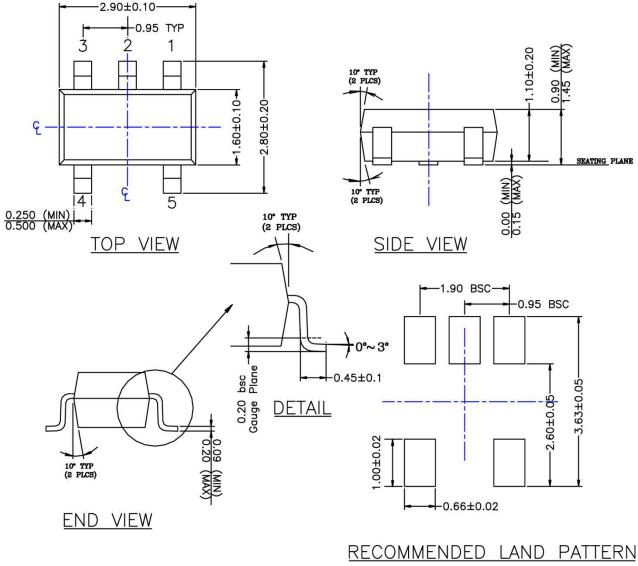


Figure 4. Adjustable Voltage Application

$$\left|V_{OUT}\right| = V_{REF}\left(1 + \frac{R2}{R1}\right)$$
 Eq. 4

Where  $V_{REF} = 1.20V$ .

## Package Information and Recommended Landing Pattern<sup>(12)</sup>



#### NOTE:

- 1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR. 2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.

- 3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982. 4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
- 5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.
- 6. ALL DIMENSIONS ARE IN MILLIMETERS.

#### 5-Pin SOT-23 (M5)

#### Note:

12. Package information is correct as of the publication date. For updates and most current information, go to www.micrel.com.

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