



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China





# MIC94310

## 200mA LDO with Ripple Blocker™ Technology



### General Description

The MIC94310 Ripple Blocker™ is a monolithic integrated circuit that provides low-frequency ripple attenuation (switching noise rejection) to a regulated output voltage. This is important for applications where a DC/DC switching converter is required to lower or raise a battery voltage but where switching noise cannot be tolerated by sensitive downstream circuits such as in RF applications. The MIC94310 maintains high power supply ripple rejection (PSRR) with input voltages operating near the output voltage level to improve overall system efficiency. A low-voltage logic enable pin facilitates ON/OFF control at typical GPIO voltage levels.

The MIC94310 operates from an input voltage of 1.8V to 3.6V.

Packaged in a 0.88mm × 0.88mm 4-ball WLCSP, a 4-pin 1.2mm × 1.6mm Thin DFN, or a 5-pin SOT-23, the MIC94310 has a junction operating temperature range of -40°C to +125°C.

Datasheets and support documentation are available on Micrel's web site at: [www.micrel.com](http://www.micrel.com).

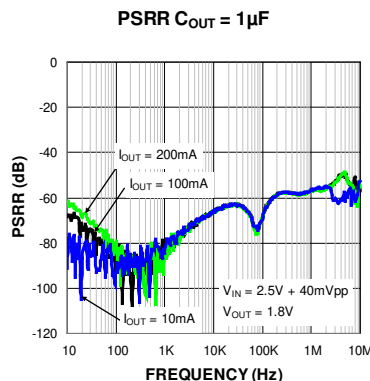
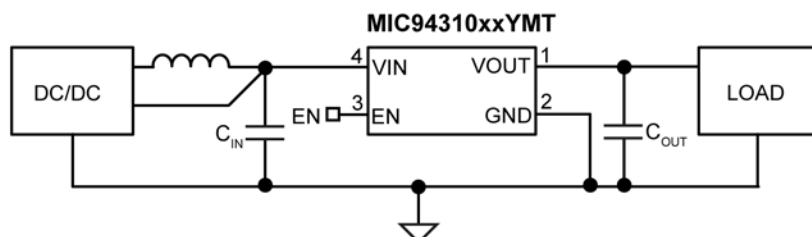
### Features

- 1.8V to 3.6V input voltage range
- Active noise rejection over a wide frequency band
  - >50dB from 10Hz to 10MHz at 200mA load
- Rated to 200mA output current
- Fixed output voltages
- Current-limit and thermal-limit protected
- Ultra-small 0.88mm × 0.88mm 4-ball WLCSP
- 1.2mm × 1.6mm 4-pin Thin DFN
- 5-pin SOT-23
- Logic-controlled enable pin
  - -40°C to +125°C junction temperature range

### Applications

- Smartphones/Smart books
- Tablet PC/notebooks and webcams
- Digital still and video cameras
- Global positioning systems
- Mobile computing
- Automotive and industrial applications

### Typical Application



Ripple Blocker is a trademark of Micrel, Inc.

Micrel Inc. • 2180 Fortune Drive • San Jose, CA 95131 • USA • tel +1 (408) 944-0800 • fax + 1 (408) 474-1000 • <http://www.micrel.com>

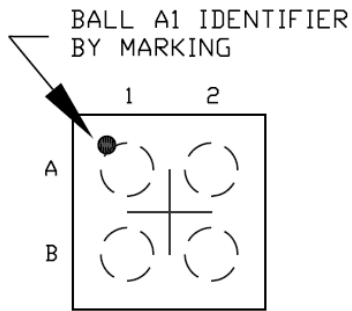
## Ordering Information

Part Number	Marking Code	Output Voltage	Package <sup>(1, 2)</sup>	Lead Finish
MIC94310-4YCS	1Z	1.2V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-FYCS	2Z	1.5V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-GYCS	Z9	1.8V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-DYCS	Z8	1.85V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-JYCS	Z7	2.5V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-LYCS	3Z	2.7V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-MYCS	Z6	2.8V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-NYCS	Z5	2.85V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-PYCS	Z4	3.0V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-SYCS	Z3	3.3V	0.88mm × 0.88mm WLCSP	Pb-Free
MIC94310-4YMT	31T	1.2V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-FYMT	32T	1.5V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-GYMT	31G	1.8V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-DYMT	31D	1.85V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-JYMT	31J	2.5V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-LYMT	31L	2.7V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-MYMT	31M	2.8V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-NYMT	31N	2.85V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-PYMT	31P	3.0V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-SYMT	31S	3.3V	1.2mm × 1.6mm Thin DFN	Pb-Free
MIC94310-4YM5	V31	1.2V	5-Pin SOT-23	Pb-Free
MIC94310-GYM5	W31	1.8V	5-Pin SOT-23	Pb-Free
MIC94310-MYM5	Z31	2.8V	5-Pin SOT-23	Pb-Free
MIC94310-SYM5	X31	3.3V	5-Pin SOT-23	Pb-Free

**Note:**

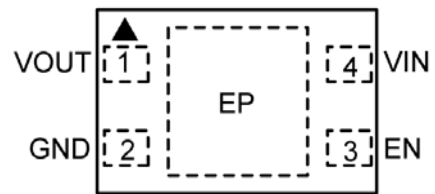
- Thin DFN ▲ = Pin 1 identifier.
- Thin DFN is a GREEN RoHS-compliant package. Lead finish is NiPdAu. Mold compound is Halogen Free.

## Pin Configuration



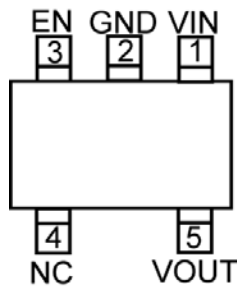
0.88mm x 0.88mm 4-Ball CSP (CS)

Top View



1.2mm x 1.6mm 4-Pin TDFN (MT)

Top View



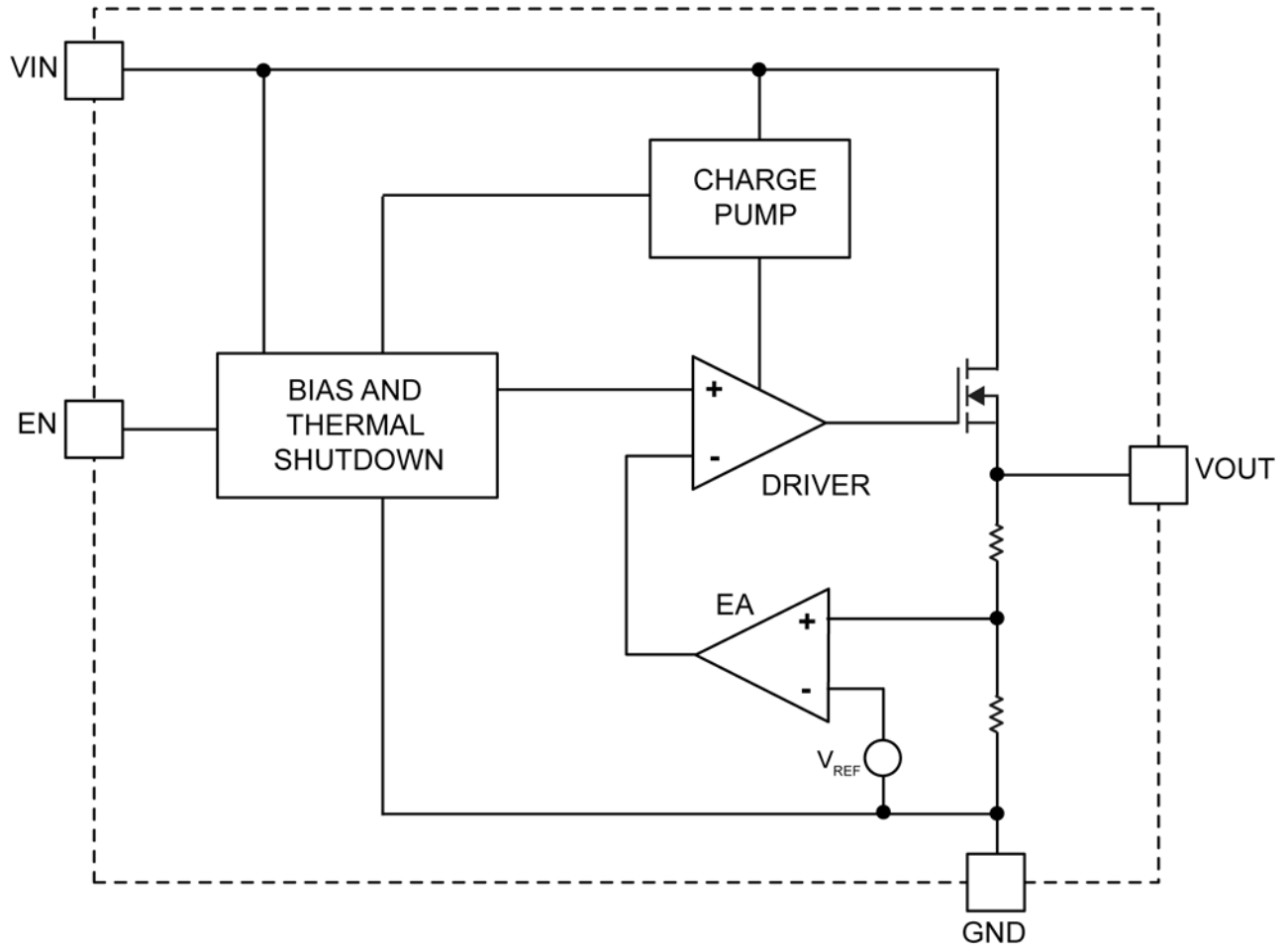
5-Pin SOT-23 (M5)

Top View

## Pin Description

Pin Number (TDFN)	Pin Number (SOT-23)	Ball Number (WLCSP)	Pin Name	Pin Function
1	5	A2	VOUT	Power switch output.
2	2	B2	GND	Ground.
3	3	B1	EN	Enable Input. A logic HIGH signal on this pin enables the part. Logic LOW disables the part. Do not leave floating.
4	1	A1	VIN	Power switch input and chip supply.
–	4	–	NC	No Connect. Not internally connected.
EP	–	–	ePad	Exposed Heatsink Pad. Connect to ground for best thermal performance.

### Functional Diagram



**Absolute Maximum Ratings<sup>(3)</sup>**

Input Voltage ( $V_{IN}$ )	-0.3 to +4.0V
Output Voltage ( $V_{OUT}$ )	-0.3 to $V_{IN}+0.3V$ or +4.0V
Enable Voltage ( $V_{EN}$ )	-0.3 to $V_{IN}+0.3V$ or +4.0V
Lead Temperature (soldering, 10s)	260°C
Storage Temperature ( $T_s$ )	-65°C to +150°C
ESD Rating <sup>(5)</sup>	3kV

**Operating Ratings<sup>(4)</sup>**

Supply Voltage ( $V_{IN}$ )	+1.8V to +3.6V
Enable Voltage ( $V_{EN}$ )	0V to $V_{IN}$
Junction Temperature ( $T_J$ )	-40°C to +125°C
Junction Thermal Resistance	
TDFN ( $\theta_{JA}$ )	173°C/W
WLCSP ( $\theta_{JA}$ )	250°C/W
SOT-23 ( $\theta_{JA}$ )	120°C/W

**Electrical Characteristics<sup>(6)</sup>**

$V_{IN} = V_{EN} = V_{OUT} + 500mV$  ( $V_{IN} = V_{EN} = 3.6V$  for  $V_{OUT} \geq 3.1V$ );  $I_{OUT} = 1mA$ ;  $C_{OUT} = 1\mu F$  (YCS, YMT),  $C_{OUT} = 10\mu F$  (YM5);  $T_A = 25^\circ C$ , **bold** values indicate  $-40^\circ C \leq T_J \leq +125^\circ C$ , unless noted.

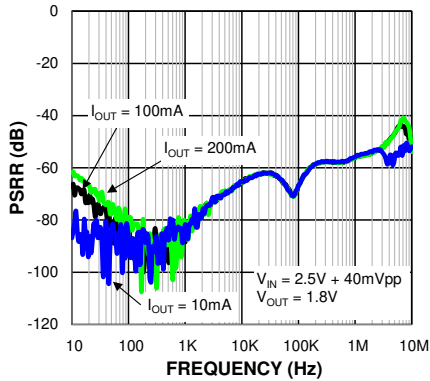
Parameter	Condition	Min.	Typ.	Max.	Units
Input Voltage		<b>1.8</b>		<b>3.6</b>	V
Output Voltage Accuracy	Variation from nominal $V_{OUT}$	<b>-3</b>	$\pm 1$	<b>+3</b>	%
Dropout Voltage	$V_{IN}$ to $V_{OUT}$ dropout at 100mA output current		20	<b>50</b>	mV
	$V_{IN}$ to $V_{OUT}$ dropout at 200mA output current		40	<b>100</b>	mV
Load Regulation	1mA to 100mA		4		mV
Line Regulation	$V_{IN} = V_{OUT} + 500mV$ to 3.6V		0.01	<b>0.5</b>	%
Ground Current	No load to full load		170	250	$\mu A$
Shutdown Current	$V_{EN} = 0V$		0.2	5	$\mu A$
$V_{IN}$ Ripple Rejection	$f = 100Hz$ , $I_{OUT} = 100mA$		85		dB
	$f = 100kHz$ , $I_{OUT} = 100mA$		68		dB
	$f = 1MHz$ , $I_{OUT} = 100mA$		57		dB
	$f = 10MHz$ , $I_{OUT} = 100mA$		50		dB
Current Limit	$V_{OUT} = 0V$	<b>250</b>	400	700	mA
Total Output Noise	10Hz to 100kHz		83		$\mu V_{RMS}$
Turn-on Time			70		$\mu s$
<b>Enable</b>					
Input Logic Level				<b>0.4</b>	V
Input Logic High		<b>1.0</b>			V
Input Current			0.01	1	$\mu A$

**Notes:**

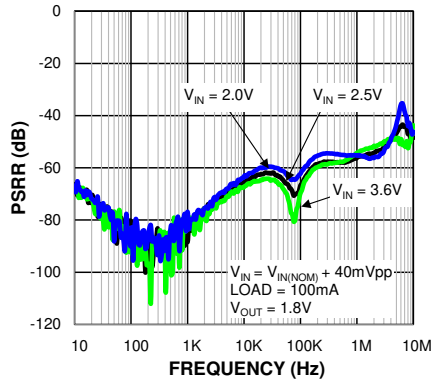
- Exceeding the absolute maximum ratings may damage the device.
- The device is not guaranteed to function outside its operating ratings.
- Devices are ESD sensitive. Handling precautions are recommended. Human body model, 1.5k $\Omega$  in series with 100pF.
- Specification for packaged product only.

# Typical Characteristics

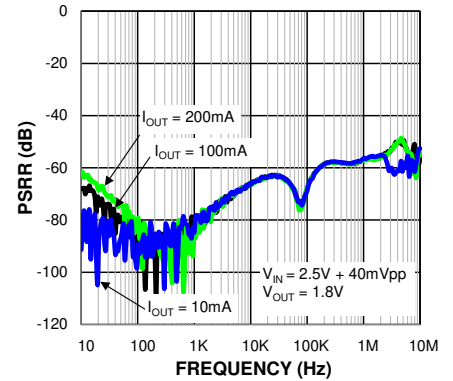
PSRR  $C_{OUT} = 0.47\mu F$



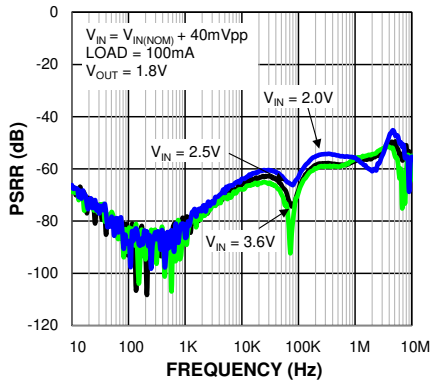
PSRR  $C_{OUT} = 0.47\mu F$



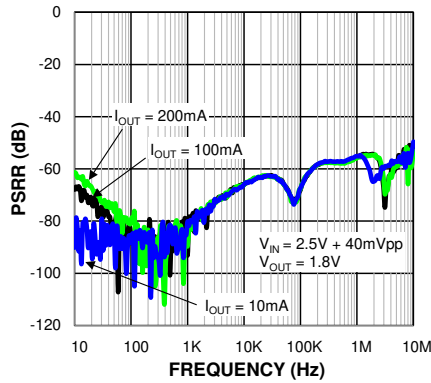
PSRR  $C_{OUT} = 1\mu F$



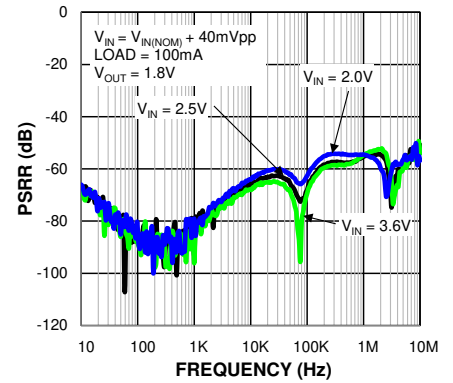
PSRR  $C_{OUT} = 1\mu F$



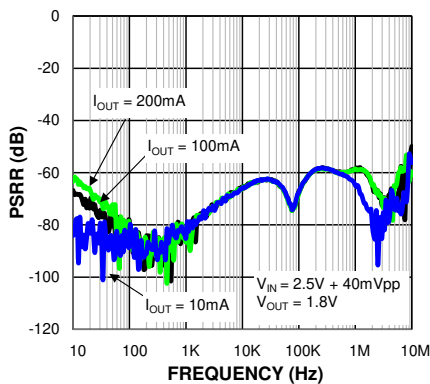
PSRR  $C_{OUT} = 2.2\mu F$



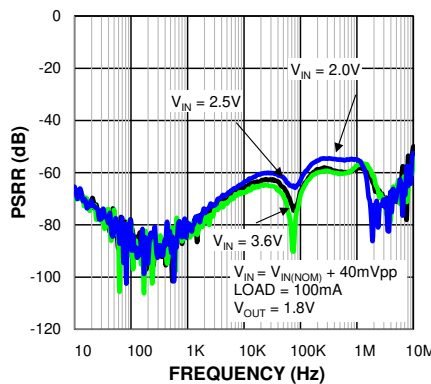
PSRR  $C_{OUT} = 2.2\mu F$



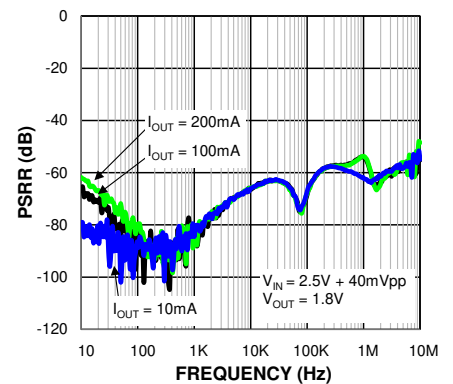
PSRR  $C_{OUT} = 4.7\mu F$



PSRR  $C_{OUT} = 4.7\mu F$

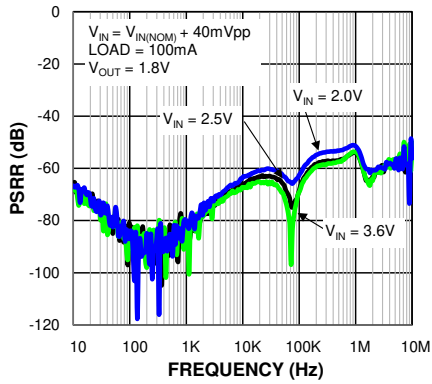


PSRR  $C_{OUT} = 10\mu F$

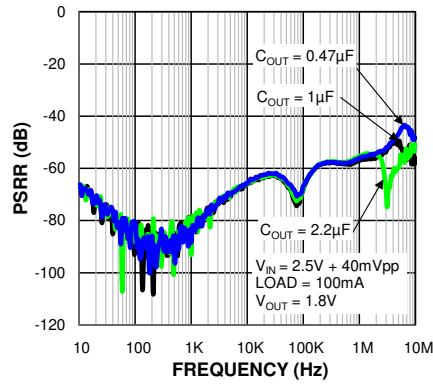


## Typical Characteristics (Continued)

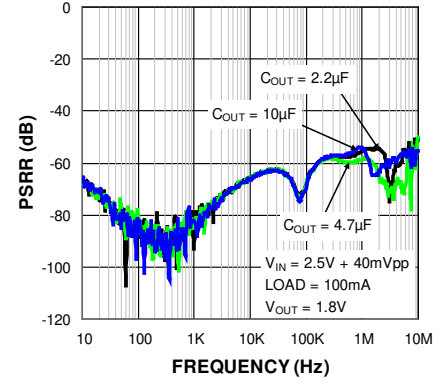
**PSRR  $C_{OUT} = 10\mu F$**



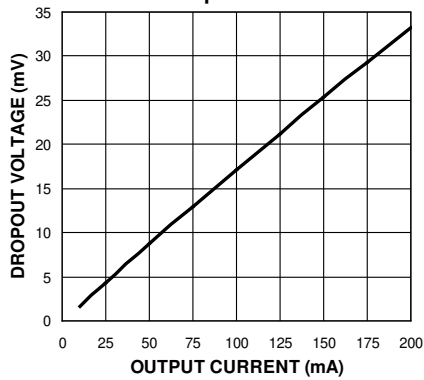
**PSRR (Varying  $C_{OUT}$ )**



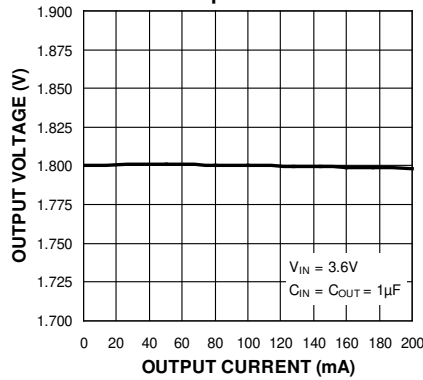
**PSRR (Varying  $C_{OUT}$ )**



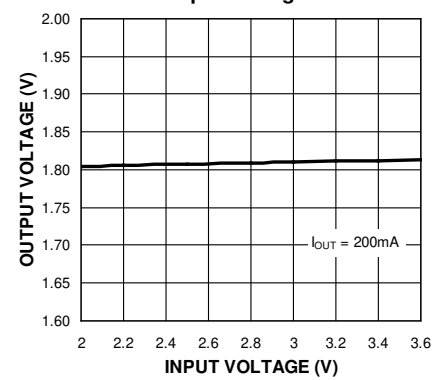
**Dropout Voltage vs. Output Current**



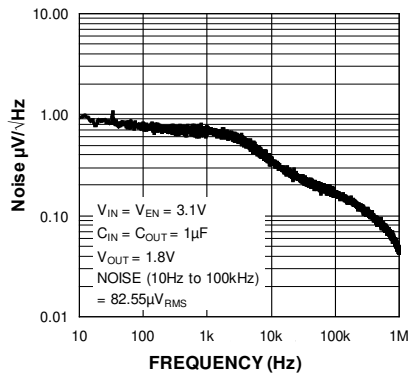
**Output Voltage vs. Output Current**



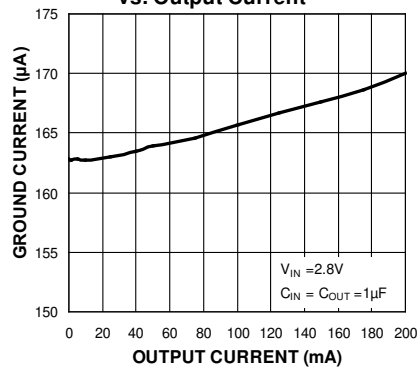
**Output Voltage vs. Input Voltage**



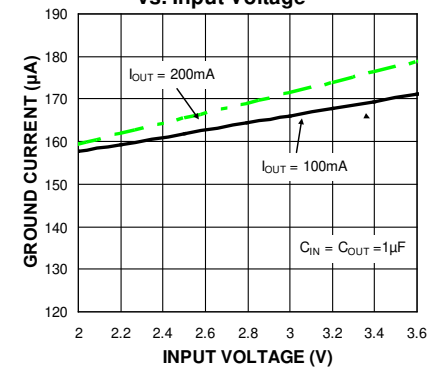
**Output Noise Spectral Density**



**Ground Current vs. Output Current**



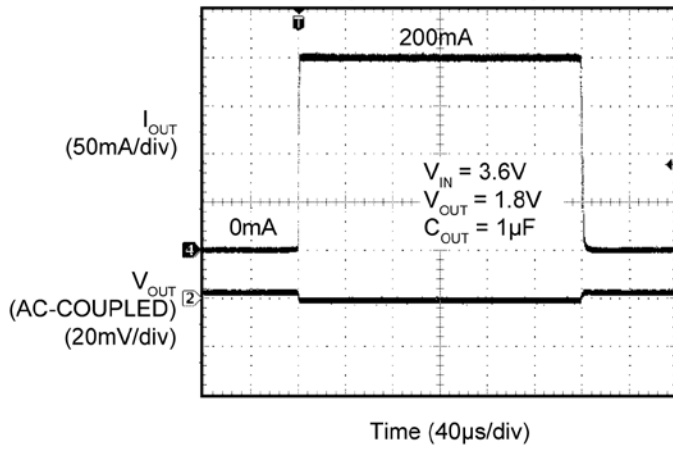
**Ground Current vs. Input Voltage**



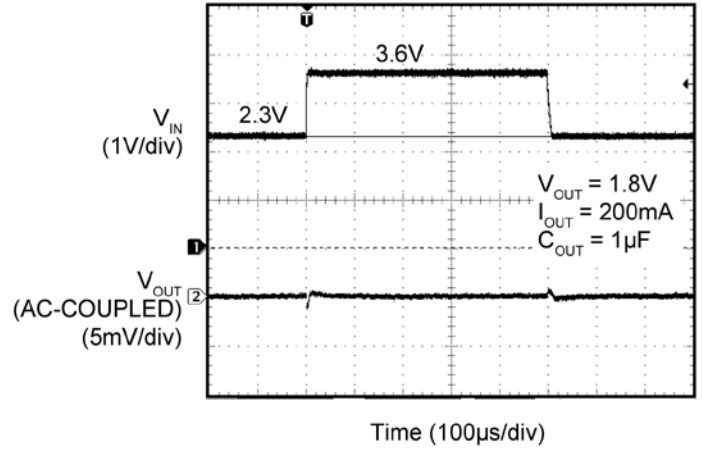


## Functional Characteristics

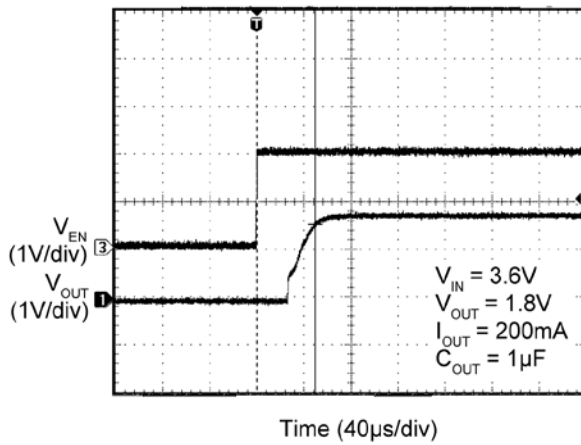
**Load Transient  
(0mA to 200mA)**



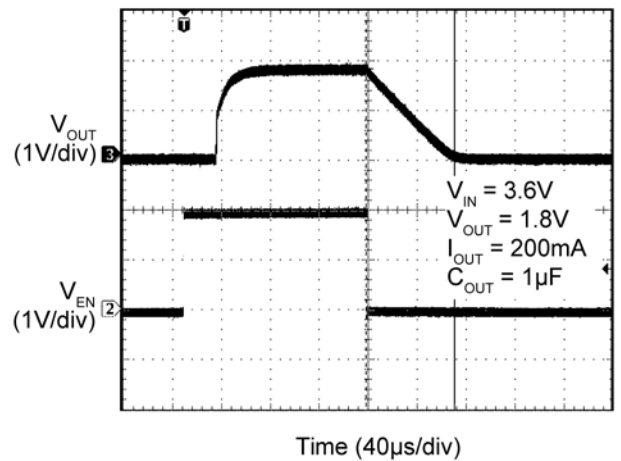
**Line Transient  
(2.6V to 3.6V)**



**Enable Turn-On**



**Enable Turn-Off**



## Application Information

The MIC94310 is a very-high PSRR, fixed-output, 200mA LDO utilizing Ripple Blocker technology. The MIC94310 is fully protected from damage due to fault conditions, offering linear current limiting and thermal shutdown.

### Input Capacitor

The MIC94310 is a high-performance, high-bandwidth device. An input capacitor of 0.47 $\mu$ F is required from the input to ground to provide stability. Low-ESR ceramic capacitors provide optimal performance at a minimum of space. Additional high-frequency capacitors, such as small-valued NPO dielectric-type capacitors, help filter out high-frequency noise and are good practice in any RF-based circuit. X5R or X7R dielectrics are recommended for the input capacitor. Y5V dielectrics lose most of their capacitance over temperature and are therefore, not recommended.

### Output Capacitance

In order to maintain stability, the MIC94310 requires an output capacitor of 0.47 $\mu$ F or greater for the Thin DFN and WLCSP packages and 10 $\mu$ F or greater for the SOT-23 package. For optimal ripple rejection performance a 1 $\mu$ F capacitor is recommended for the CSP and Thin DFN packages, while a 10 $\mu$ F capacitor is recommended for the SOT-23 package. The design is optimized for use with low-ESR ceramic chip capacitors. High-ESR capacitors are not recommended because they may cause high-frequency oscillation. The output capacitor can be increased, but performance has been optimized for a 1 $\mu$ F ceramic output capacitor and does not improve significantly with larger capacitance.

X7R/X5R dielectric type ceramic capacitors are recommended because of their temperature performance. X7R type capacitors change capacitance by 15% over their operating temperature range and are the most stable type of ceramic capacitors. Z5U and Y5V dielectric capacitors change their value by as much as 50% and 60%, respectively, over their operating temperature ranges. To use a ceramic chip capacitor with the Y5V dielectric, the value must be much higher than an X7R ceramic capacitor to ensure the same minimum capacitance over the equivalent operating temperature range.

### No Load Stability

The MIC94310 will remain stable and in regulation with no load. This is especially important in CMOS RAM keep-alive applications.

### Enable/Shutdown

Forcing the enable (EN) pin low disables the MIC94310 and sends it into a “zero” off mode current state. In this state, current consumed by the MIC94310 goes nearly to zero. Forcing EN high enables the output voltage. The EN pin uses CMOS technology and cannot be left floating as it could cause an indeterminate state on the output.

### Thermal Considerations

The MIC94310 is designed to provide 200mA of continuous current in a very-small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. For example if the input voltage is 2.5V, the output voltage is 1.8V, and the output current = 200mA. The actual power dissipation of the Ripple Blocker™ can be determined using Equation 1:

$$P_D = (V_{IN} - V_{OUT1}) I_{OUT} + V_{IN} I_{GND} \quad \text{Eq. 1}$$

Because this device is CMOS and the ground current is typically <170 $\mu$ A over the load range, the power dissipation contributed by the ground current is <1% and can be ignored for this calculation.

$$P_D = (2.5V - 1.8V) \times 200mA$$

$$P_D = 0.14W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the Equation 2:

$$P_{D(MAX)} = \left( \frac{T_{J(max)} - T_A}{\theta_{JA}} \right) \quad \text{Eq. 2}$$

$T_{J(MAX)} = 125^\circ\text{C}$ , the maximum junction temperature of the die,  $\theta_{JA}$  thermal resistance = 173 $^\circ\text{C/W}$  for the Thin DFN package.

Substituting  $P_D$  for  $P_{D(MAX)}$  and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit.

For proper operation, the maximum power dissipation must not be exceeded.

For example, when operating the MIC94310-GYMT at an input voltage of 2.5V and 200mA load with a minimum footprint layout, the maximum ambient operating temperature ( $T_A$ ) can be determined as follows:

$$0.14W = (125^{\circ}\text{C} - T_A)/(173^{\circ}\text{C/W})$$

$$T_A = 101^{\circ}\text{C}$$

Therefore, the maximum ambient operating temperature allowed in a 1.2mm × 1.6mm Thin DFN package is 101°C. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the “Regulator Thermals” section of Micrel’s *Designing with Low-Dropout Voltage Regulators* handbook. This information can be found on Micrel’s website at:

[http://www.micrel.com/PDF/other/LDOBk\\_ds.pdf](http://www.micrel.com/PDF/other/LDOBk_ds.pdf)

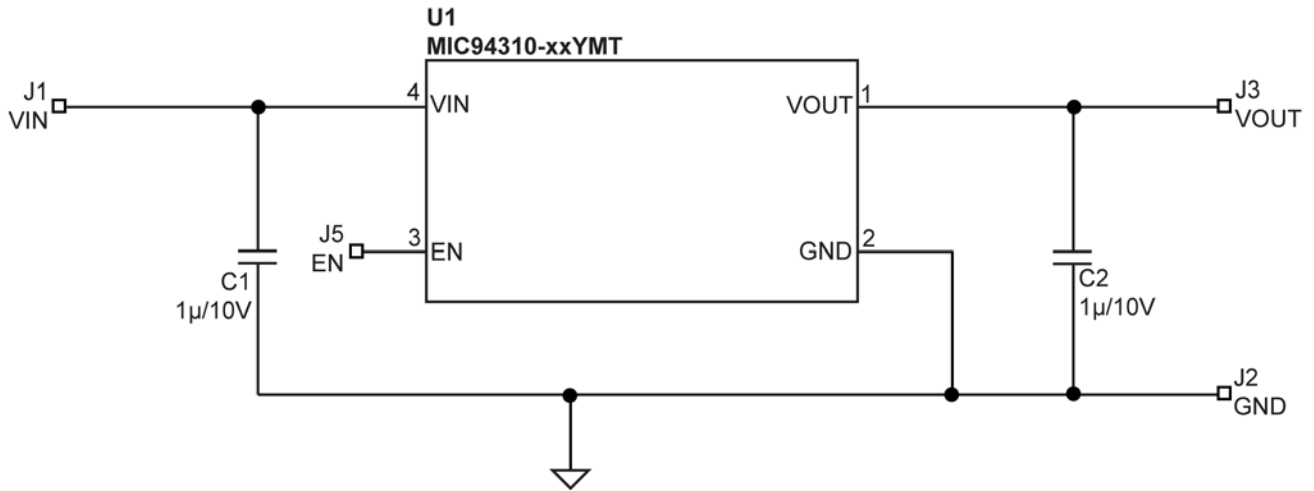
For more information about Micrel’s Ripple Blocker products, please visit:

<http://www.micrel.com/index.php/en/products/power-management-ics/ldos/linear-power-filters.html>

<http://www.micrel.com/index.php/en/products/power-management-ics/ldos/linear-power-filters/article/1-mic94300.html>

<http://www.micrel.com/index.php/en/products/power-management-ics/ldos/linear-power-filters/article/3-mic94310.html>

## Evaluation Board Schematic



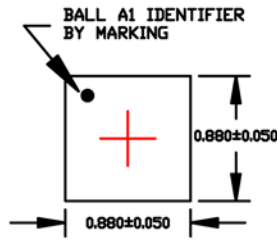
## Bill of Materials

Item	Part Number	Manufacturer	Description	Qty.
C1, C2	GRM155R61A105KE15D	Murata <sup>(7)</sup>	Capacitor, 1µF Ceramic, 10V, X7R, Size 0402	2
U1	MIC94310-xxYMT	Micrel, Inc. <sup>(8)</sup>	200mA LDO with Ripple Blocker Technology	1

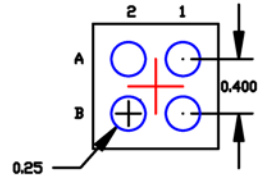
**Notes:**

- 7. Murata: [www.murata.com](http://www.murata.com).
- 8. Micrel, Inc.: [www.micrel.com](http://www.micrel.com).

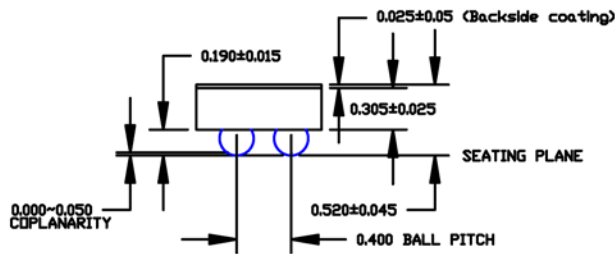
**Package Information and Recommended Landing Pattern<sup>(9)</sup>**



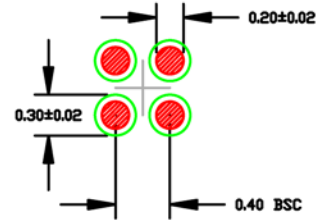
TOP VIEW  
NOTE: 1,2



BOTTOM VIEW  
NOTE: 1,2



SIDE VIEW  
NOTE: 1,2



RECOMMENDED LAND PATTERN  
NOTE: 3,4

NOTE:

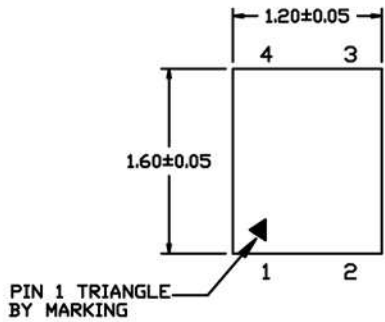
1. MAX PACKAGE WARPAGE IS 0.05 MM
2. MAX ALLOWABLE BURR IS 0.076MM IN ALL DIRECTIONS
3. NON-SOLDERMASK DEFINED PADS ARE RECOMMENDED FOR BOARD LAYOUT
4. SHADED RED CIRCLES REPRESENT CONTACT PAD AREA, GREEN CIRCLES REPRESENT SOLDER MASK OPENING

**4-Ball 0.88mm × 0.88mm WLCSP (CS)**

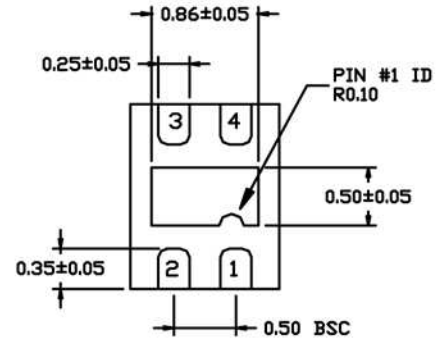
**Note:**

9. Package information is correct as of the publication date. For updates and most current information, go to [www.micrel.com](http://www.micrel.com).

Package Information and Recommended Landing Pattern<sup>(9)</sup> (Continued)



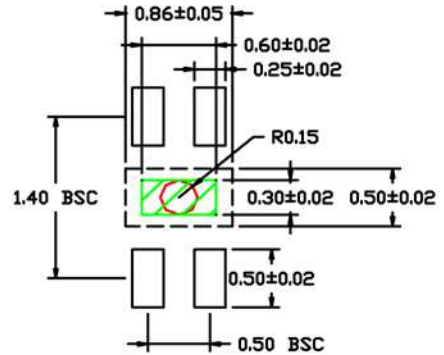
**TOP VIEW**  
NOTE: 1, 2, 3



**BOTTOM VIEW**  
NOTE: 1, 2, 3



**SIDE VIEW**  
NOTE: 1, 2, 3



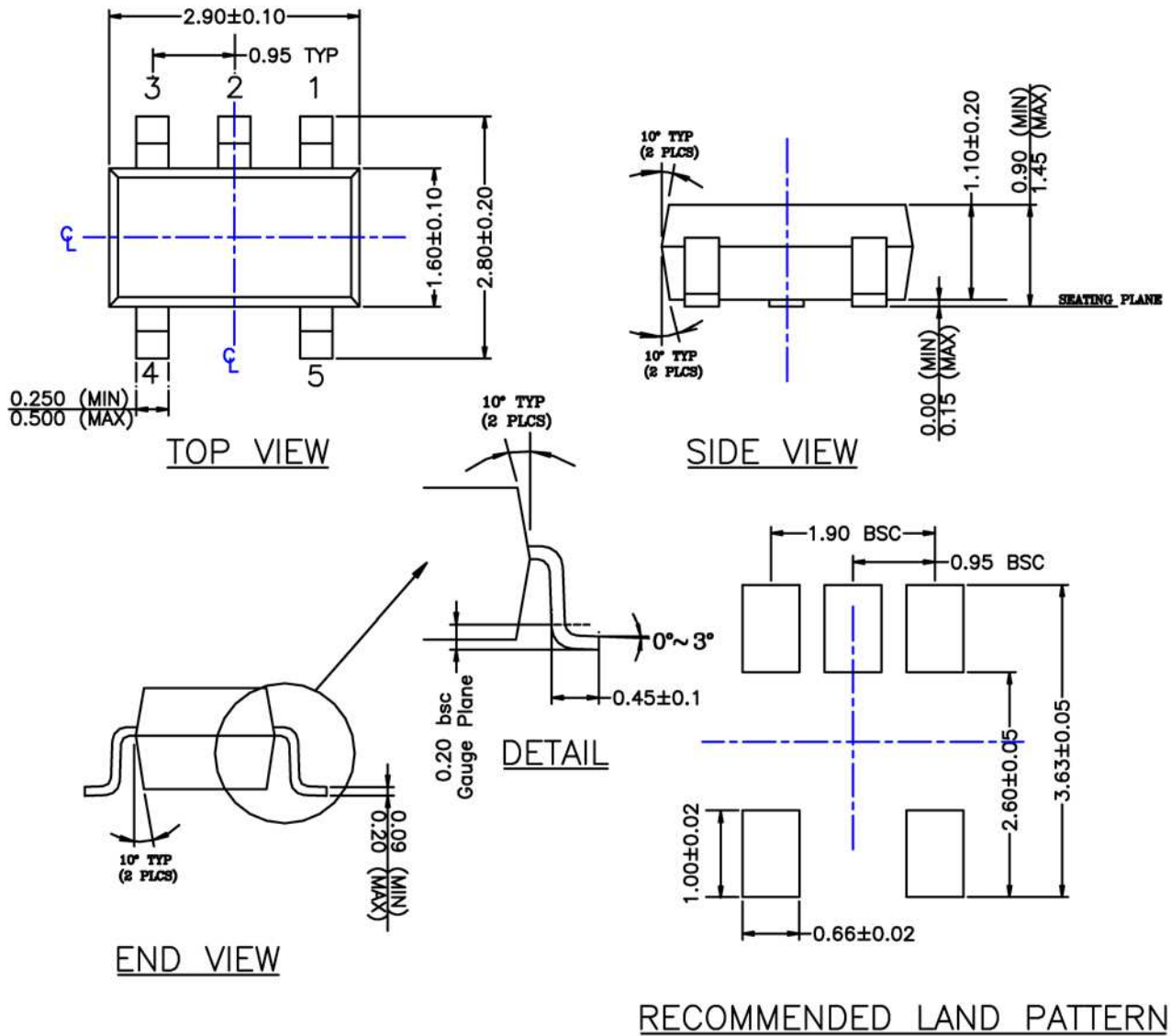
**RECOMMENDED LAND PATTERN**  
NOTE: 4, 5

NOTE:

1. MAX PACKAGE WARPAGE IS 0.05mm.
2. MAX ALLOWABLE BURR IS 0.076mm IN ALL DIRECTIONS.
3. PIN #1 IS ON TOP WILL BE LASER MARKED.
4. GREEN SHADED AREA INDICATES SOLDER STENCIL OPENING (OPTIONAL) FOR IMPROVED THERMAL PERFORMANCE. RECOMMENDED SIZE IS 0.60mm × 0.30mm.
5. RED CIRCLE REPRESENTS THERMAL VIA & SHOULD BE CONNECTED TO GND FOR MAX PERFORMANCE. RECOMMENDED DIAMETER IS 0.30mm - 0.35mm.

4-Pin 1.2mm × 1.6mm Thin DFN (MT)

Package Information and Recommended Landing Pattern<sup>(9)</sup> (Continued)



- NOTE:
1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR.
  2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.
  3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982.
  4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
  5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM.
  6. ALL DIMENSIONS ARE IN MILLIMETERS.

5-Pin SOT-23 (M5)

---

**MICREL, INC. 2180 FORTUNE DRIVE SAN JOSE, CA 95131 USA**  
TEL +1 (408) 944-0800 FAX +1 (408) 474-1000 WEB <http://www.micrel.com>

Micrel, Inc. is a leading global manufacturer of IC solutions for the worldwide high performance linear and power, LAN, and timing & communications markets. The Company's products include advanced mixed-signal, analog & power semiconductors; high-performance communication, clock management, MEMs-based clock oscillators & crystal-less clock generators, Ethernet switches, and physical layer transceiver ICs. Company customers include leading manufacturers of enterprise, consumer, industrial, mobile, telecommunications, automotive, and computer products. Corporation headquarters and state-of-the-art wafer fabrication facilities are located in San Jose, CA, with regional sales and support offices and advanced technology design centers situated throughout the Americas, Europe, and Asia. Additionally, the Company maintains an extensive network of distributors and reps worldwide.

Micrel makes no representations or warranties with respect to the accuracy or completeness of the information furnished in this datasheet. This information is not intended as a warranty and Micrel does not assume responsibility for its use. Micrel reserves the right to change circuitry, specifications and descriptions at any time without notice. No license, whether express, implied, arising by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Micrel's terms and conditions of sale for such products, Micrel assumes no liability whatsoever, and Micrel disclaims any express or implied warranty relating to the sale and/or use of Micrel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright, or other intellectual property right.

Micrel Products are not designed or authorized for use as components in life support appliances, devices or systems where malfunction of a product can reasonably be expected to result in personal injury. Life support devices or systems are devices or systems that (a) are intended for surgical implant into the body or (b) support or sustain life, and whose failure to perform can be reasonably expected to result in a significant injury to the user. A Purchaser's use or sale of Micrel Products for use in life support appliances, devices or systems is a Purchaser's own risk and Purchaser agrees to fully indemnify Micrel for any damages resulting from such use or sale.

© 2012 Micrel, Incorporated.