



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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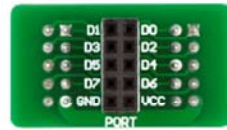
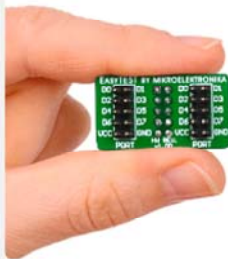
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EasyTEST Board

This board helps you to double the number of pins of single IDC10 PORT header, which allows you to monitor signals using oscilloscope probes while the PORT is in operation. Board is also useful when you need to connect two boards to the same PORT at once.



Technical Specifications

Applications

The EasyTEST Board is an ideal solution for connecting your prototype or development board to oscilloscope or other monitoring devices.

Key features

- ▶ Board features one female and two male IDC10 connectors, connected in parallel.
- ▶ Female IDC10 connector is compatible with mikroE development boards

Operating Voltage

Board can be used with both 5V and 3.3V systems.

Key Benefits

- ▶ Board is clearly marked, which makes it easier to connect with external electronics
- ▶ Flexible solution suitable for all MCU architectures