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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









Winstar Display Co., LTD 華凌光電股份有限公司

住址: 407 台中市中清路 163 號 No.163 Chung Ching RD., Taichune, Taiwan, R.O.C

WEB: http://www.winstar.com.tw E-mail: sales@winstar.com.tw Tel:886-4-24262208 Fax: 886-4-24262207



SPECIFICATION

CUST	OM	ER:									
MOD	ULE	NO.: _	WI	WDG0151-TMI-V#N00							
APPR	OVI	ED BY:									
(FOR CUS	ГОМЕБ	R USE ONLY	PCB	VER	SION:	DATA:					
SALES I	BY	APPROV	ED BY	(CHECKED BY	PREPARED BY					
						l					
VERSION			REVIS		su	MMARY					
0	200	9/11/10			First issue						



MODLE NO :		

REC	ORDS OF REV	'ISION	DO	OC. FIRST ISSUE
VERSION	DATE	REVISED PAGE NO.	SUMN	MARY
0	2009/11/10		First	t issue

Contents

- 1. Module classification information
- 2. Precautions in Use of LCM
- 3. General Specification
- 4. Absolute Maximum Ratings
- 5. Electrical Characteristics
- 6. Optical Characteristics
- 7.Interface Pin Function
- 8. Counter Drawing & Block Diagram
- 9. Timing Characteristics
- 10.Display Control Instruction
- 11.Detailed Explanation
- 12.Reliability
- 13.Backlight Information
- 14.Inspection specification
- 15.Material List of Components for RoHs

1. Module Classification Information

Brand: WINSTAR DISPLAY CORPORATION

© Custom: D

Model serials no.0000 - ZZZZ

Backlight Type: $N \rightarrow Without backlight$ $P \rightarrow LED$, Blue

 $B \rightarrow EL$, Blue green $A \rightarrow LED$, Amber

 $D \rightarrow EL$, Green $R \rightarrow LED$, Red

 $W \rightarrow EL$, White $O \rightarrow LED$, Orange

 $F \rightarrow CCFL$, White $G \rightarrow LED$, Green

 $Y \rightarrow LED$, Yellow Green $T \rightarrow LED$, White

6 LCD Mode: B→ TN Positive, Gray T→ FSTN Negative

N→ TN Negative,

G→ STN Positive, Gray

Y→ STN Positive, Yellow Green

M→ STN Negative, Blue

F→ FSTN Positive

② LCD Polarizer A→ Reflective, N.T, 6:00 H→ Transflective, W.T,6:00

Type/ D→ Reflective, N.T, 12:00 K→ Transflective, W.T,12:00

Temperature G→ Reflective, W. T, 6:00 C→ Transmissive, N.T,6:00

range/ View J→ Reflective, W. T, 12:00 F→ Transmissive, N.T,12:00

direction B→ Transflective, N.T,6:00 I→ Transmissive, W. T, 6:00

E→ Transflective, N.T.12:00 L→ Transmissive, W.T,12:00

Special Code
V: Build in Negative Voltage
N: IC NT7107, NT7108C

#: Fit in with the ROHS Directions and regulations;

0:Sales code 0:Version

2.Precautions in Use of LCD Module

- (1) Avoid applying excessive shocks to the module or making any alterations or modifications to it.
- (2)Don't make extra holes on the printed circuit board, modify its shape or change the components of LCD Module.
- (3)Don't disassemble the LCM.
- (4)Don't operate it above the absolute maximum rating.
- (5)Don't drop, bend or twist LCM.
- (6) Soldering: only to the I/O terminals.
- (7)Storage: please storage in anti-static electricity container and clean environment.
- (8). Winstar have the right to change the passive components
- (9). Winstar have the right to change the PCB Rev.

3.General Specification

ITEM	STANDARD VALUE	UNIT				
Number of dots	128 ×64	dots				
Outline dimension	78.0 (W) ×70.0 (H) ×14.3 (T)	mm				
View area	62.0(W) ×44.0(H)	mm				
Active area	56.3(W) ×38.38(H)	mm				
Dot size	0.42(W) ×0.58(H)	mm				
Dot pitch	0.44(W) ×0.60(H)	mm				
LCD type	STN Negative, Blue, Transmissive (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)					
View direction	6 o'clock					
Backlight	LED, White					

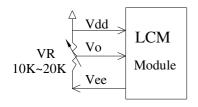
4.Absolute Maximum Ratings

ITEM	SYMBOL	MIN.	TYP.	MAX.	UNIT
Operating Temperature	T_{OP}	-20	-	+70	$^{\circ}$ C
Storage Temperature	T_{ST}	-30	-	+80	°C
Input Voltage	V _I	0	-	V_{DD}	V
Supply Voltage For Logic	$V_{DD}V_{SS}$	0	-	6.5	V
Supply Voltage For LCD	V_{DD} - V_{LCD}	0	-	17.0	V

5.Electrical Characteristics

ITEM	SYMBOL	CONDITION	MIN.	TYP.	MAX.	UNIT
Logic Voltage	V_{DD} - V_{SS}	-	4.5	5.0	5.5	V
Supply Voltage For		Ta=-20°C	-	-	-	V
LCD *Note	V_{DD} - V_{O}	Ta=25°C	7.62	8.51	9.26	V
11000		Ta=+70°C	-	-	-	V
Input High Volt.	V_{IH}	-	2.0	-	V_{DD}	V
Input Low Volt.	V_{IL}	-	0	ı	0.8	V
Output High Volt.	V_{OH}	-	2.4	1	V_{DD}	V
Output Low Volt.	V_{OL}	-	0	-	0.4	V
Supply Current	I_{OP}	5.0	2.8	3.1	3.4	mA

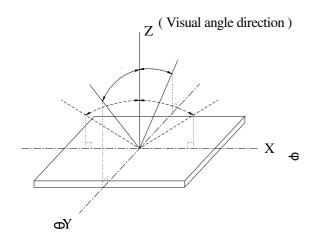
^{*} Note: Please design the VOP adjustment circuit on customer's main board

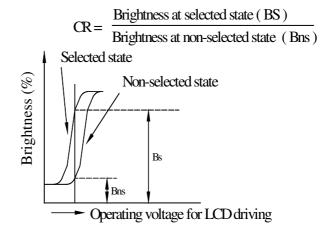


6.Optical Characteristics

ITEM	SYMBAL	CONDITION	MIN	TYP	MAX	UNIT
***	(V) θ	CR≧ 2	20	-	40	deg.
View Angle	(Н)ф	CR≧ 2	-30	-	30	deg.
Contrast Ratio	CR	-	-	3	-	-
, T:	T rise	-	-	200	300	ms
Response Time	T fall	-	-	150	200	ms

6.1 Definitions

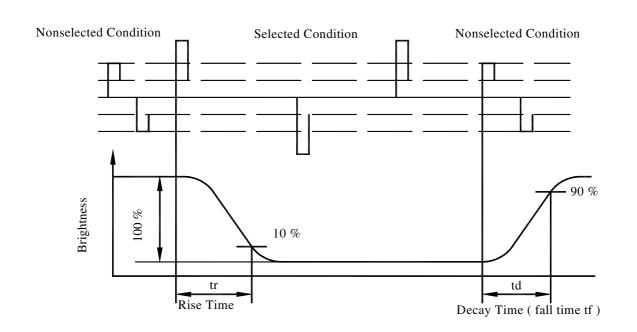




■ View Angles

■ Contrast Ratio

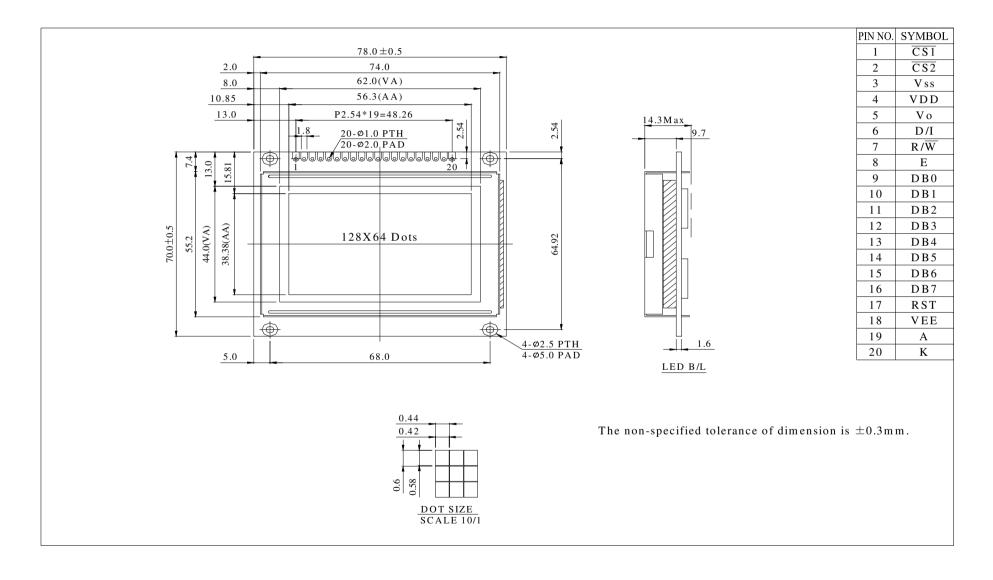
■ Response time

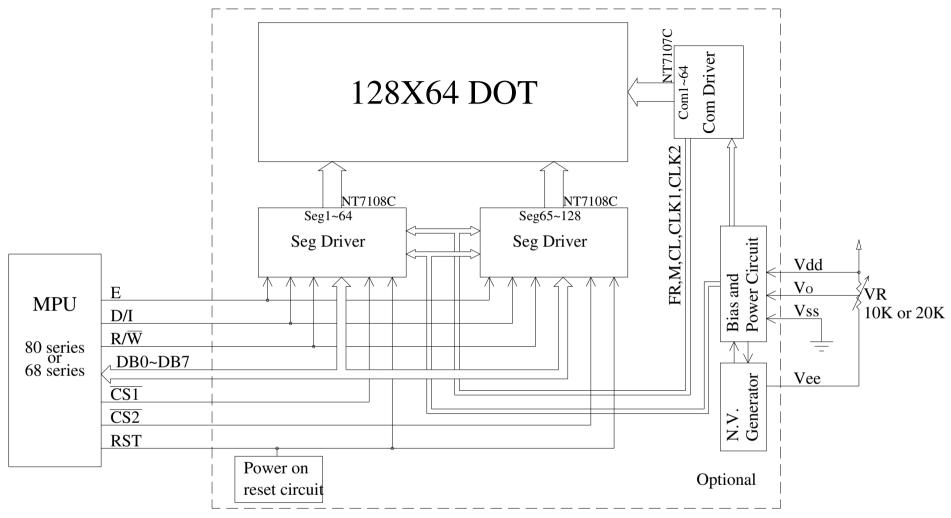


7.Interface Pin Function

Pin No.	Symbol	Level	Description
1	CS1	L	Select Segment 1 ~ Segment 64
2	CS2	L	Select Segment 65 ~ Segment 128
3	Vss	0V	Ground
4	V_{DD}	5.0V	Supply voltage for logic
5	V_{O}	(Variable)	Operating voltage for LCD
6	D/I	H/L	H: Data , L: Instruction
7	R/W	H/L	H: Read(MPU Module), L:Write(MPU→ Module)
8	E	Н	Enable signal
9	DB0	H/L	Data bus line
10	DB1	H/L	Data bus line
11	DB2	H/L	Data bus line
12	DB3	H/L	Data bus line
13	DB4	H/L	Data bus line
14	DB5	H/L	Data bus line
15	DB6	H/L	Data bus line
16	DB7	H/L	Data bus line
17	RST	L	Reset the LCM
18	VEE		Negative Voltage Output
19	A		Power supply for B/L(+)
20	K		Power supply for B/L(-)

8.Counter Drawing & Block diagram





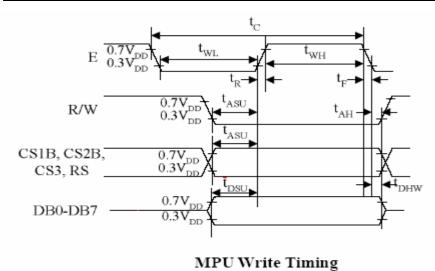
External contrast adjustment.

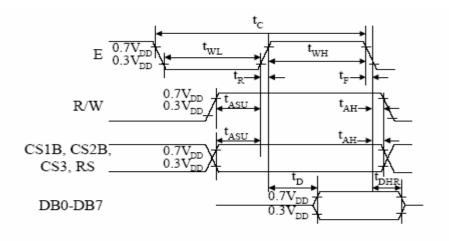
9.Timing Characteristics

MPU Interface

(T=25°C, VDD=+5.0V±0.5)

Characteristic	Symbol	Min	Тур	Max	Unit
E cycle	tcyc	1000	-	-	ns
E high level width	twhE	450	-	-	ns
E low level width	twlE	450	-	-	ns
E rise time	tr	-	-	25	ns
E tall time	tf	-	-	25	ns
Address set-up time	tas	140	-	-	ns
Address hold time	tah	10	-	-	ns
Data set-up time	tdsw	140	-	-	ns
Data delay time	tddr	-	-	320	ns
Data hold time (write)	tdhw	10	-	-	ns
Data hold time (read)	tdhr	20	-	-	ns





MPU Read Timing

10.Display Control Instruction

The display control instructions control the internal state of the NT7108. Instruction is received from MPU to NT7108 for the display control. The following table shows various instructions.

Instruction	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Function
Display on/off	L	L	L	L	Н	Н	Н	Н	Н	L/H	Controls the display on or off. Internal status and display RAM data is not affected. L:OFF, H:ON
Set address (Y address)	L	L	L	Н		Y	addres	ss (0-6	33)		Sets the Y address in the Y address counter.
Set page (X address)	L	L	Н	L	Н	Н	Н	Page (0-7)			Sets the X address at the X address register.
Display Start line (Z address)	L	L	Н	Н	Display start line (0-63)					Indicates the display data RAM displayed at the top of the screen.	
Status read	L	Н	Busy	L	On/ Off	Reset	L	L	L	L	Read status. BUSY L: Ready H: In operation ON/OFF L: Display ON H: Display OFF RESET L: Normal H: Reset
Write display data	Н	L				Write data					Writes data (DB0: 7) into display data RAM. After writing instruction, Y address is increased by 1 automatically.
Read display data	Н	Н				Read	data				Reads data (DB0: 7) from display data RAM to the data bus.

11.Detailed Explanation

	RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
Г	0	0	0	0	1	1	1	1	1	D

The display data appears when D is 1 and disappears when D is 0. Though the data is not on the screen with D=0, it remains in the display data RAM. Therefore, you can make it appear by changing D=0 into D=1.

SET ADDRESS (Y ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	0	1	AC5	AC4	AC3	AC2	AC1	AC0

Y address (AC0-AC5) of the display data RAM is set in the Y address counter. An address is set by instruction and increased by 1 automatically by read or write operations of display data.

SET PAGE (X ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	0	1	1	1	AC2	AC1	AC0

X address (AC0-AC2) of the display data RAM is set in the X address register. Writing or reading to or from MPU is executed in this specified page until the next page is set.

DISPLAY START LINE (Z ADDRESS)

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	0	1	1	AC5	AC4	AC3	AC2	AC1	AC0

Z address (AC0-AC5) of the display data RAM is set in the display start line register and displayed at the top of the screen. When the display duty cycle is 1/64 or others (1/32-1/64), the data of total line number of LCD screen, from the line specified by display start line instruction, is displayed.

STATUS READ

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
0	1	BUSY	0	ON/OFF	RESET	0	0	0	0

BUSY

When BUSY is 1, the Chip is executing internal operation and no instructions are accepted.

When BUSY is 0, the Chip is ready to accept any instructions.

ON/OFF

When ON/OFF is 1, the display is OFF.

When ON/OFF is 0, the display is ON.

RESET

When RESET is 1, the system is being initialized.

In this condition, no instructions except status read can be accepted.

When RESET is 0, initializing has finished and the system is in usual operation condition.

WRITE DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	0	D7	D6	D5	D4	D3	D2	D1	D0

Writes data (D0-D7) into the display data RAM. After writing instruction, Y address is increased by 1automatically.

READ DISPLAY DATA

RS	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0
1	1	D7	D6	D5	D4	D3	D2	D1	D0

Reads data (D0-D7) from the display data RAM. After reading instruction, Y address is increased by 1 automatically.

12.RELIABILITY

Content of Reliability Test (wide temperature, -20°C~70°C)

	Environmental Test		
Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	80°C 200hrs	2
Low Temperature storage	Endurance test applying the high storage temperature for a long time.	-30°C 200hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (Voltage & Current) and the thermal stress to the element for a long time.	70°C 200hrs	
Low Temperature Operation	Endurance test applying the electric stress under low temperature for a long time.	-20°C 200hrs	1
High Temperature/ Humidity Operation	The module should be allowed to stand at 60 °C,90%RH max For 96hrs under no-load condition excluding the polarizer, Then taking it out and drying it at normal temperature.	60°C,90%RH 96hrs	1,2
Thermal shock resistance	The sample should be allowed stand the following 10 cycles of operation -20°C 25°C 70°C 30min 5min 30min 1 cycle	-20°C/70°C 10 cycles	
Vibration test	Endurance test applying the vibration during transportation and using.	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 directions of X,Y,Z for Each 15 minutes	3
Static electricity test	Endurance test applying the electric stress to the terminal.	VS=800V,RS=1.5k CS=100pF 1 time	

Note1: No dew condensation to be observed.

Note2: The function test shall be conducted after 4 hours storage at the normal Temperature and humidity after remove from the test chamber.

Note3: Vibration test will be conducted t	o the product itself without putting it in a container.
	Page 17 of 30

13.Backlight Information

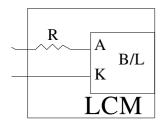
PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION
Supply Current	ILED	57.6	64	100	mA	V=3.5V
Supply Voltage	V	3.4	3.5	3.6	V	
Reverse Voltage	VR	-	-	5	V	
Luminous Intensity	IV	282	344	-	CD/M ²	ILED=64mA
LED Life Time (For Reference only)		-	50K	-	Hr.	ILED 64mA 25℃,50-60%RH, (Note 1)
Color				White		

Note: The LED of B/L is drive by current only, drive voltage is for reference only.

drive voltage can make driving current under safety area (current between minimum and maximum).

Note1:50K hours is only a estimate for reference.

.Drive from pin19,pin20

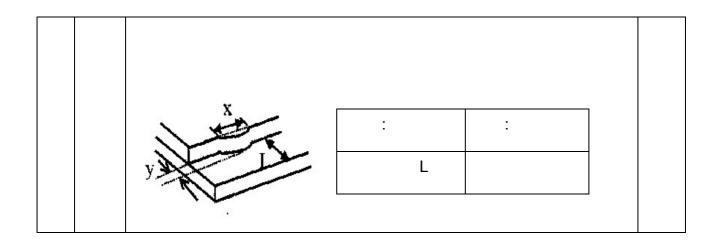


14. Inspection specification

04	Polarizer bubbles	If bubbles are visible, judge using black spot specifications, not easy to find, must check in specify direction.	Size Total Q TY	Acceptable Q TY Accept no dense 3 2 0 3	2.5
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Item	Criterion					
Scratches	Follow NO.3 LCD black spots, white spots, contamination					
	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels:					
	: C : C					
Chipped glass		\rfloor 2.5				
_	C XXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXXX					
	: C : C					
	Chipped	Symbols Define: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length: 6.1 General glass chip: 6.1.1 Chip on panel surface and crack between panels: C : C : C				

NO	Item	Criterion	AQL
		: x: Chip length y: Chip width z: Chip thickness k: Seal width t: Glass thickness a: LCD side length L: Electrode pad length 6.2 Protrusion over terminal: 6.2.1 Chip on electrode pad:	
06	Glass cra ck	$\begin{array}{c c} : C & : C \\ \hline \end{array}$	2.5
		: C : C : C	



NO	Item	Criterion			
07	Cracked glass	LCD	2.5		
08	Backlight elements	LCD			
09	Bezel				
			0.65		
10	PCB COB	C C The height of the COB should not exceed the height indicated in the assembly diagram. 10.4 There may not be more than 2mm of sealant outside the seal area on the PCB. And there should be no more than three places. 10.5 No oxidation or contamination PCB terminals. 10.6 Parts on PCB must be the same as on the production characteristic chart. There should be no wrong parts, missing parts or excess parts. 10.7 The jumper on the PCB should conform to the product characteristic chart. 10.8 If solder gets on bezel tab pads, LED pad, zebra pad or screw hold pad, make sure it is smoothed down. 10.9 The Scraping testing standard for Copper Coating of PCB X * Y <= 2mm²	2.5 2.5 0.65 2.5 2.5 0.65 2.5 2.5 2.5		
11	Soldering	- C	2.5 2.5 2.5 0.65		

NO	Item	Criterion					
			L	С		2.5	
	General			L	С	0.65	
						2.5	
						2.5	
		С	С			2.5	
12						2.5	
	appearance					2.5	
						0.65	
						0.65	
		LCD				0.65	
		LOD				0.65	
						0.03	