imall

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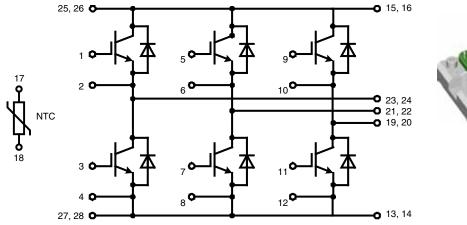
MIXA30W1200TED

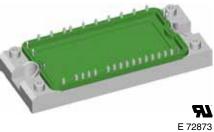
Six-Pack **XPT IGBT**

= 1200 V V_{CES} I_{C25} 43 A = V_{CE(sat)} = 1.8 V

Part name (Marking on product)

MIXA30W1200TED





Pin configuration see outlines.

Features:

- Easy paralleling due to the positive temperature coefficient of the on-state voltage
- Rugged XPT design
- (Xtreme light Punch Through) results in: - short circuit rated for 10 µsec.
- very low gate charge
- square RBSOA @ 3x Ic
- low EMI
- Thin wafer technology combined with the XPT design results in a competitive $\begin{array}{l} \text{low } V_{\text{CE(sat)}} \\ \bullet \ SONIC^{\text{TM}} \ diode \end{array}$
- fast and soft reverse recovery
- low operating forward voltage

Application:

- AC motor drives
- Solar inverter
- Medical equipment
- Uninterruptible power supply
- Air-conditioning systems
- Welding equipment
- · Switched-mode and
- resonant-mode power supplies

Package:

- "E2-Pack" standard outline
- Insulated copper base plate
- Soldering pins for PCB mounting
- Temperature sense included

IXYS reserves the right to change limits, test conditions and dimensions.



Ouput Inverter T1 - T6

					Ratir	Ratings		
Symbol	Definitions	Conditions		min.	typ.	max.	Unit	
V _{CES}	collector emitter voltage		$T_{VJ} = 25^{\circ}C$			1200	V	
V _{GES}	max. DC gate voltage	continuous				±20	V	
V _{GEM}	max. transient collector gate voltage	transient				±30	V	
I _{C25}	collector current		$T_c = 25^{\circ}C$			43	Α	
I _{C80}			$T_c = 80^{\circ}C$			30	A	
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			150	W	
V _{CE(sat)}	collector emitter saturation voltage	$I_{\rm C}$ = 25 A; $V_{\rm GE}$ = 15 V	$T_{vJ} = 25^{\circ}C$		1.8	2.1	V	
			$T_{VJ} = 125^{\circ}C$		2.1		V	
$V_{GE(th)}$	gate emitter threshold voltage	$I_{C} = 1 \text{ mA}; V_{GE} = V_{CE}$	$T_{VJ} = 25^{\circ}C$	5.4	6.0	6.5	V	
I _{CES}	collector emitter leakage current	$V_{CE} = V_{CES}; V_{GE} = 0 V$	$T_{vJ} = 25^{\circ}C$		0.01	2.1	mA	
			$T_{VJ} = 125^{\circ}C$		0.2		mA	
I _{GES}	gate emitter leakage current	$V_{GE} = \pm 20 \text{ V}$				500	nA	
Q _{G(on)}	total gate charge	$V_{ce} = 600 \text{ V}; V_{ge} = 15 \text{ V}; I_c = 2000 \text{ V}$	25 A		76		nC	
t _{d(on)}	turn-on delay time				70		ns	
t,	current rise time	inductive load	T _{v1} = 125°C		40		ns	
t _{d(off)}	turn-off delay time	$V_{CF} = 600 \text{ V}; I_{C} = 25 \text{ A}$	T _{VJ} = 120 O		250		ns	
t _r	current fall time	$V_{GE} = \pm 15 \text{ V}; \text{ R}_{G} = 39 \Omega$			100		ns	
E _{on}	turn-on energy per pulse	$v_{GE} = \pm 10 v_1 + t_G = 00 22$			2.5		mJ	
E _{off}	turn-off energy per pulse				3.0		mJ	
RBSOA	reverse bias safe operating area	$V_{GE} = \pm 15 \text{ V}; \text{ R}_{G} = 39 \Omega;$	T _{vJ} = 125°C					
			V _{CEK} = 1200 V			75	Α	
SCSOA	short circuit safe operating area							
t _{sc}	short circuit duration	$V_{CE} = 900 \text{ V}; V_{GE} = \pm 15 \text{ V};$	T _{vJ} = 125°C			10	μs	
I _{sc}	short circuit current	$R_{G} = 39 \Omega$; non-repetitive	·		100		Â	
R _{thJC}	thermal resistance junction to case	(per IGBT)				0.84	K/W	

Output Inverter D1 - D6

				Ratings			
Symbol	Definitions	Conditions		min.	typ.	max.	Unit
V _{RRM}	max. repetitve reverse voltage		$T_{VJ} = 25^{\circ}C$			1200	V
_{F25} _{F80}	forward current		$T_c = 25^{\circ}C$ $T_c = 80^{\circ}C$			44 29	A
V _F	forward voltage	$I_{F} = 30 \text{ A}; V_{GE} = 0 \text{ V}$	$T_{VJ} = 25^{\circ}C$ $T_{VJ} = 125^{\circ}C$		1.95 1.95	2.2	V V
Q _{rr} I _{RM} t _{rr} E _{rec}	reverse recovery charge max. reverse recovery current reverse recovery time reverse recovery energy	$ \left. \begin{array}{c} V_{\text{R}} = 600 \text{ V} \\ di_{\text{F}}/dt = -600 \text{ A}/\mu\text{s} \\ I_{\text{F}} = 30 \text{ A}; \text{ V}_{\text{GE}} = 0 \text{ V} \end{array} \right. $	T _{vJ} = 125°C		3.5 30 350 0.9		μC A ns mJ
R _{thJC}	thermal resistance junction to case	(per diode)				1.2	K/W

 $T_c = 25^{\circ}C$ unless otherwise stated



Temperature Sensor NTC

				Ratings			
Symbol	Definitions	Conditions		min.	typ.	max.	Unit
R ₂₅	resistance		$T_c = 25^{\circ}C$	4.75	5.0	5.25	kΩ
B _{25/50}					3375		K

Module

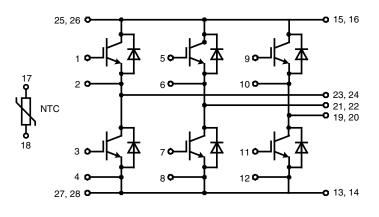
			Ratings				
Symbol	Definitions	Conditions	min.	typ.	max.	Unit	
T _{vj}	operating temperature		-40		125	°C	
T _{VJM}	max. virtual junction temperature				150	°C	
T _{stg}	storage temperature		-40		125	°C	
VISOL	isolation voltage	$I_{ISOL} \le 1 \text{ mA}; 50/60 \text{ Hz}$			2500	٧~	
СТІ	comparative tracking index				-		
M _d	mounting torque (M5)		3		6	Nm	
ds	creep distance on surface		10			mm	
d _A	strike distance through air		7.5			mm	
$\mathbf{R}_{pin-chip}$	resistance pin to chip			2.5		mΩ	
R _{thCH}	thermal resistance case to heatsink	with heatsink compound		0.02		K/W	
Weight				180		g	

Equivalent Circuits for Simulation							
	 R_0						
V ₀	0				Ratir	ngs	
Symbol	Definitions	Conditions		min.	typ.	max.	Unit
V _o	IGBT	T1 - T6	T _{vJ} = 150°C			1.1	V
\mathbf{R}_{0}						55	mΩ
V _o	free wheeling diode	D1 - D6	T _{vJ} = 150°C			1.2	V
R ₀						27	mΩ

 $T_{\rm C}$ = 25°C unless otherwise stated

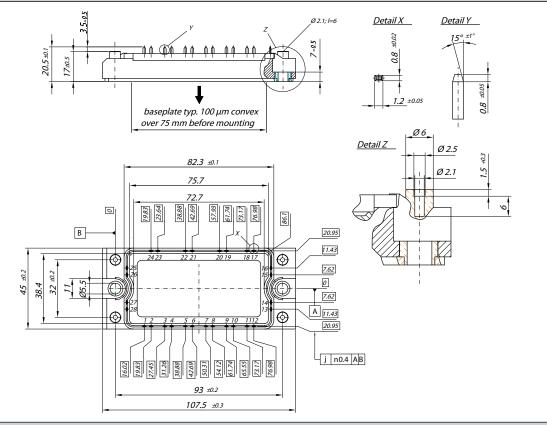
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Circuit Diagram

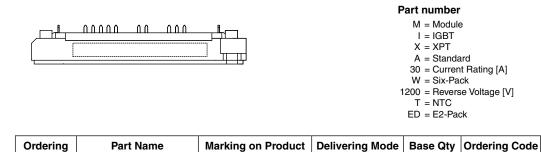


Outline Drawing

Dimensions in mm (1 mm = 0.0394")



Product Marking



MIXA30W1200TED

Box

6

508635

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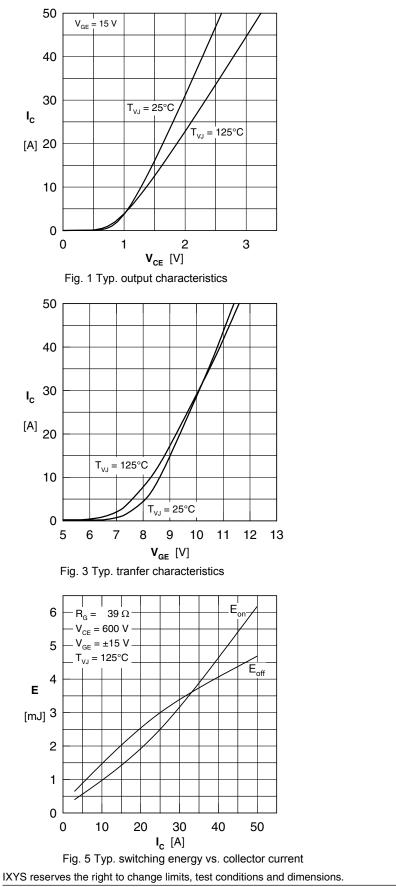
MIXA30W1200 TED

Standard

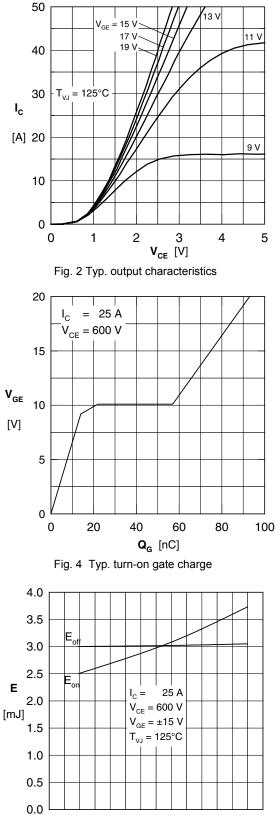
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MIXA30W1200TED

Inverter T1 - T6



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 $\label{eq:rescaled} \begin{array}{c} \textbf{R}_{\textbf{G}} ~ [\Omega] \\ \text{Fig. 6 Typ. switching energy vs. gate resistance} \\ & 20100622b \end{array}$

80 100 120 140 160

40

20

60



Inverter D1 - D6

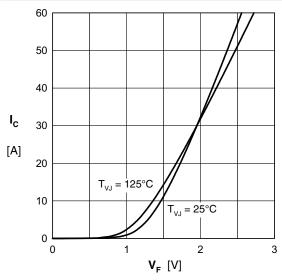
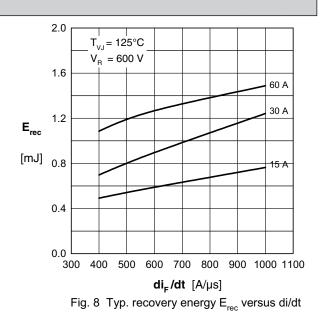
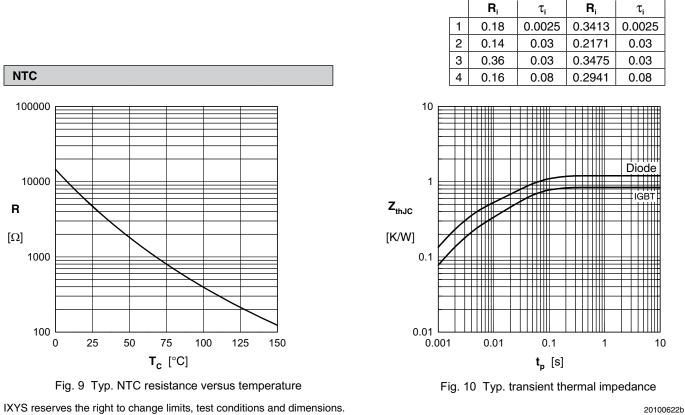


Fig. 7 Typ. forward characteristic



IGBT

FRD



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