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MLX90632 FIR sensor

Datasheet

Features and Benefits

- Small size
- Easy to integrate
- Factory calibrated
- External ambient and object temperature calculation
- Measurement resolution of 0.02°C
- Supply voltage of 3.3V, supply current 1mA (sleep current less than 2.5uA)
- I²C compatible digital interface
- Software definable I²C address with 1 LSB bit external address pin
- Field of View of 50°
- Default refresh rate 0.5s, configurable between 16ms and 2s
- Integrated post-calibration option

Application Examples

- Non-contact temperature measurements
- Temperature sensing element for residential, commercial and industrial building air conditioning
- Industrial temperature control of moving parts
- Home appliances with temperature control



Figure 1: Image of MLX90632



MLX90632 FIR sensor

Datasheet

Description

The MLX90632 is a non-contact infrared temperature sensor in a small SMD SFN package. The device is factory calibrated with calibration constants stored in the EEPROM memory. The ambient and object temperature can be calculated based on these calibration constants and the measurement data.

The MLX90632 is factory calibrated in the ambient temperature range from -20 to 85°C and from -20 to 200°C for the object temperature range. The measured value is the average temperature of all objects in the Field Of View of the sensor.

It is very important for the application designer to understand that these accuracies are guaranteed and achievable when the sensor is in thermal equilibrium and under isothermal conditions (no temperature differences across the sensor package).

The accuracy of the thermometer can be influenced by temperature differences in the package induced by causes like (among others): Hot electronics behind the sensor, heaters/coolers behind or beside the sensor or by a hot/cold object very close to the sensor that not only heats the sensing element in the thermometer but also the thermometer package.

A major strength of the MLX90632 is that the measured effect of these temperature differences around the sensor package is reduced to a minimum by the internal measurement algorithm.

In the same way, localized thermal variations -like turbulence in the air- will not generate thermal noise in the output signal of the thermopile.

However, some extreme cases will influence the sensor.

The typical supply voltage of the MLX90632 is 3.3V. The communication with the chip is done by I^2C in fast mode plus (FM+). Through I^2C the external microcontroller has access to the following blocks:

- RAM memory used for measurement data
- EEPROM used to store the trimming values, calibration constants and device/measurement settings
- Register to control the sensor

Based on this data, the external microcontroller can calculate the object temperature and if needed the sensor temperature.

An optical filter (long-wave pass) that cuts off the visible and near infrared radiant flux is integrated in the sensor to provide ambient light immunity. The wavelength pass band of this optical filter is from 2 till $14\mu m$.





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2. Ordering Information

Product	Temperature Code	Package	Option Code	Packing Form
MLX90632	S	LD	BCB-000	RE/SP

Table 1 : Ordering codes for MLX90632

Legend:

Temperature Code:	S: from -20°C to 85°C sensor temperature		
Package Code:	"LD" for SFN 3x3 package		
Option Code:	XYZ-123		
	X: Accuracy		
	 B: standard accuracy 		
	Y: Pixel type		
	 C: high stability version 		
	Z: Field Of View		
	B: 50 degrees		
	1: I ² C level		
	• 0: 3V3		
	2-3:		
	 00: Standard configuration 		
	 xx: Reserved 		
Packing Form:	"RE" for Reel		
	"SP" for sampling quantities in tubes		
Ordering Example:	"MLX90632SLD-BCB-000-RE" For a non-contact thermometer in SFN 3x3 package with standard accuracy and a Field Of View of 50 degrees, delivered in Reel.		

Table 2: Coding legend



3. Glossary of Terms

POR	Power On Reset
IR	InfraRed
I ² C	Inter-Integrated Circuit
SDA	Serial DAta – I^2C compatible communication pins
SCL	Serial CLock – I ² C compatible communication pins
ACK / NACK	Acknowledge / Not Acknowledge
SOC	Start Of Conversion
EOC	End Of Conversion
FOV	Field Of View
Та	Ambient Temperature measured from the chip – (the package temperature)
То	Object Temperature, 'seen' from IR sensor
SFN	Single Flat pack No-lead
TBD	To Be Defined
LSB	Least Significant Bit
MSB	Most Significant Bit
EMC	Electro-Magnetic Compatibility
ESD	Electro-Static Discharge
HBM	Human Body Model
CDM	Charged Device Model

Table 3: List of abbreviations



4. Absolute Maximum ratings

Parameter	Symbol	Min.	Max.	Unit
Supply Voltage, (over voltage)	V _{DD}		5	V
Supply Voltage, (operating)	V_{DD}		3.6	V
Reverse Voltage	V _R		-1.5	V
Address-pin Voltage	V _{ADDR}		V _{DD} +0.6	V
Operating Temperature Range	T _A	-20	+85	°C
Storage Temperature Range	Ts	-40	+105	°C
ESD Sensitivity (AEC Q100 002)				
- HBM			2	kV
- CDM			750	V
- Air discharge			+4	kV
- Contact discharge			+2	kV
DC current into SCL			10	μΑ
DC sink current, SDA pin		20		mA
DC clamp current, SDA pin			25	mA
DC clamp current, SCL pin			25	mA

Table 4: Absolute maximum ratings

Exceeding the absolute maximum ratings may cause permanent damage. Exposure to absolute maximum-rated conditions for extended periods may affect device reliability.



5. Pin definitions and descriptions



Figure 2: MLX90632 pinout, TOP view

Pin #	Name	Direction	Description
1	SDA	In/Out	I ² C Data line
2	VDD	POWER	Supply
3	GND	GND	Ground
4	SCL	In	I ² C Clock line
5	ADDR	In	LSB of I ² C address

Table 5: Pin definition



6. Electrical characteristics

All parameters are valid for $T_A = 25$ °C, $V_{DD} = 3.3V$ (unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typical	Max	Units
		Supplies				
External supply	V _{DD}		3	3.3	3.6	V
Supply current	I _{DD}	No load	0.5	1	1.4	mA
Sleep current	I _{DDpr}	No load, erase/write EEPROM operations		1.5	2.5	μΑ
		Power On Reset				
POR level	$V_{\text{POR up}}$	Power-up (full temp range)	1.3		2.4	V
POR level	$V_{POR\ down}$	Power-down (full temp range)	1.1		2.1	V
POR hysteresis	$V_{\text{POR hys}}$	Full temperature range	200		500	mV
V _{DD} rise time (10% to 90% of specified supply voltage)	T _{POR}	Ensure POR signal			20	ms
Output valid (result in RAM)	Tvalid	After POR	64			ms
		I ² C compatible 2-wire interface				
I ² C Voltage	V _{I2C}		3	V _{DD}	3.6	V
Input high voltage	V _{IH}	Over temperature and supply	$0.7^{*}V_{I2C}$		V _{I2C} +0.5	V
Input low voltage	V _{IL}	Over temperature and supply	-0.5		0.3*V _{I2C}	V
Output low voltage	V _{OL}	Over temperature and supply	0		0.4	V
Address pin voltage ("1")	V _{ADDR,HI}		2	V _{DD}	V _{DD} +0.5	V
Address pin voltage ("0")	V _{ADDR,LO}		0		0.5	V
ADDR leakage	I _{ADDR, leak}				1	μA
SCL leakage	I _{SCL, leak}	V _{SCL} =3.6V, Ta=+85°C			1	μΑ
SDA leakage	I _{SDA, leak}	V _{SDA} =3.6V, Ta=+85°C			1	μΑ
SCL capacitance	C _{SCL}				10	pF
SDA capacitance	C _{SDA}				10	pF
Slave address	SA	Factory default, ADDR-pin grounded		3A		hex

Table 6: Electrical characteristics



7. Product Description

7.1. Block diagram



Figure 3: Block diagram

7.2. Description

The MLX90632 is a far infrared, non-contact temperature sensor which is factory calibrated to a high accuracy. Internally, electrical and thermal precautions are taken to compensate for thermally harsh external conditions. The thermopile sensing element voltage signal is amplified and digitized. After digital filtering, the raw measurement result is stored in the RAM memory. Furthermore, the MLX90632 contains a sensor element to measure the temperature of the sensor itself. The raw information of this sensor is also stored in RAM after processing. All above functions are controlled by a state machine. The result of each measurement conversion is accessible via l²C.

The communication to the chip is done by I^2C in fast mode plus (FM+). The requirement of the standard is to run at frequencies up to 1MHz. Through I^2C the external unit can have access to the following blocks:

- Control registers of internal state machines
- RAM (96bit x 16bit) for pixel and auxiliary measurement data, in this document mainly referred to as 'storage memory'.
- EEPROM (256bit x 16bit) used to store the trimming values, calibration constants and various device/measurement settings.

From the measurement data and the calibration data the external unit can calculate both the sensor temperature and the object temperature. The calculation allows the customer to adjust the calibration for his own application in case an optical window or obstructions are present.



8. Memory map

Access (Write key required)	Address	Name	Description
			EEPROM
Read-only	0x240B	EE_VERSION	EEPROM version
Read-only	0x240C	EE_P_R [15:0]	P_R calibration constant (16-bit, Least Significant Word)
Read-only	0x240D	EE_P_R [31:16]	P_R calibration constant (16-bit, Most Significant Word)
Read-only	0x240E	EE_P_G [15:0]	P_G calibration constant (16-bit, Least Significant Word)
Read-only	0x240F	EE_P_G [31:16]	P_G calibration constant (16-bit, Most Significant Word)
Read-only	0x2410	EE_P_T [15:0]	P_T calibration constant (16-bit, Least Significant Word)
Read-only	0x2411	EE_P_T [31:16]	P_T calibration constant (16-bit, Most Significant Word)
Read-only	0x2412	EE_P_O [15:0]	P_O calibration constant (16-bit, Least Significant Word)
Read-only	0x2413	EE_P_O [31:16]	P_O calibration constant (16-bit, Most Significant Word)
Read-only	0x2414	EE_Aa [15:0]	Aa calibration constant (16-bit, Least Significant Word)
Read-only	0x2415	EE_Aa [31:16]	Aa calibration constant (16-bit, Most Significant Word)
Read-only	0x2416	EE_Ab [15:0]	Ab calibration constant (16-bit, Least Significant Word)
Read-only	0x2417	EE_Ab [31:16]	Ab calibration constant (16-bit, Most Significant Word)
Read-only	0x2418	EE_Ba [15:0]	Ba calibration constant (16-bit, Least Significant Word)
Read-only	0x2419	EE_Ba [31:16]	Ba calibration constant (16-bit, Most Significant Word)
Read-only	0x241A	EE_Bb [15:0]	Bb calibration constant (16-bit, Least Significant Word)
Read-only	0x241B	EE_Bb [31:16]	Bb calibration constant (16-bit, Most Significant Word)
Read-only	0x241C	EE_Ca [15:0]	Ca calibration constant (16-bit, Least Significant Word)
Read-only	0x241D	EE_Ca [31:16]	Ca calibration constant (16-bit, Most Significant Word)
Read-only	0x241E	EE_Cb [15:0]	Cb calibration constant (16-bit, Least Significant Word)
Read-only	0x241F	EE_Cb [31:16]	Cb calibration constant (16-bit, Most Significant Word)
Read-only	0x2420	EE_Da [15:0]	Da calibration constant (16-bit, Least Significant Word)
Read-only	0x2421	EE_Da [31:16]	Da calibration constant (16-bit, Most Significant Word)
Read-only	0x2422	EE_Db [15:0]	Db calibration constant (16-bit, Least Significant Word)
Read-only	0x2423	EE_Db [31:16]	Db calibration constant (16-bit, Most Significant Word)
Read-only	0x2424	EE_Ea [15:0]	Ea calibration constant (16-bit, Least Significant Word)
Read-only	0x2425	EE_Ea [31:16]	Ea calibration constant (16-bit, Most Significant Word)
Read-only	0x2426	EE_Eb [15:0]	Eb calibration constant (16-bit, Least Significant Word)
Read-only	0x2427	EE_Eb [31:16]	Eb calibration constant (16-bit, Most Significant Word)
Read-only	0x2428	EE_Fa [15:0]	Fa calibration constant (16-bit, Least Significant Word)



Read-only	0x2429	EE_Fa [31:16]	Fa calibration constant (16-bit, Most Significant Word)
Read-only	0x242A	EE_Fb [15:0]	Fb calibration constant (16-bit, Least Significant Word)
Read-only	0x242B	EE_Fb [31:16]	Fb calibration constant (16-bit, Most Significant Word)
Read-only	0x242C	EE_Ga [15:0]	Ga calibration constant (16-bit, Least Significant Word)
Read-only	0x242D	EE_Ga [31:16]	Ga calibration constant (16-bit, Most Significant Word)
Read-only	0x242E	EE_Gb [15:0]	Gb calibration constant (16-bit)
Read-only	0x242F	EE_Ka [15:0]	Ka calibration constant (16-bit)
Read-only	0x2430	EE_Kb [15:0]	Kb calibration constant (16-bit)
-	-	Melexis reserved	
R/W	0x2481	EE_Ha [15:0]	Ha Customer calibration constant (16 bit)
R/W	0x2482	EE_Hb [15:0]	Hb Customer calibration constant (16 bit)
-	-	Melexis reserved	
R/W	0x24D4	EE_CONTROL	EEPROM Control register, measurement control
R/W	0x24D5	EE_I2C_ADDRESS	I ² C slave address >> 1 <u>Example:</u> standard address (= 0x003A) >> 1 = 0x001D
			DECISTED
			REGISTER
R/W	0x3000	REG_I2C_ADDRESS	I ² C slave address >> 1
R/W R/W	0x3000 0x3001	REG_I2C_ADDRESS REG_CONTROL	I ² C slave address >> 1 Control register, measurement mode
R/W R/W	0x3000 0x3001 -	REG_I2C_ADDRESS REG_CONTROL Melexis reserved	I ² C slave address >> 1 Control register, measurement mode
R/W R/W - R/W	0x3000 0x3001 - 0x3FFF	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS	I ² C slave address >> 1 Control register, measurement mode Status register: data available
R/W R/W - R/W	0x3000 0x3001 - 0x3FFF	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM
R/W R/W - R/W Read-only	0x3000 0x3001 - 0x3FFF 0x4000	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1
R/W R/W - R/W Read-only Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4000	REG_I2C_ADDRESSREG_CONTROLMelexis reservedREG_STATUSRAM_1RAM_2	REGISTER I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2
R/W R/W - R/W Read-only Read-only Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4000	REG_I2C_ADDRESSREG_CONTROLMelexis reservedREG_STATUSRAM_1RAM_2RAM_3	REGISTER I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3
R/W R/W - R/W Read-only Read-only Read-only Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4000 0x4001 0x4002	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1 RAM_1 RAM_2 RAM_3 RAM_4	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3 Raw data 4
R/W R/W - R/W Raad-only Read-only Read-only Read-only Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4000 0x4001 0x4002 0x4003 0x4004	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1 RAM_2 RAM_3 RAM_4 RAM_5	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3 Raw data 4 Raw data 5
R/W R/W - R/W Read-only Read-only Read-only Read-only Read-only Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4001 0x4002 0x4003 0x4003 0x4004	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1 RAM_2 RAM_2 RAM_3 RAM_4 RAM_5 RAM_6	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3 Raw data 4 Raw data 5 Raw data 6
R/W R/W R/W R/W R/W R/W R/W R/W Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4001 0x4002 0x4003 0x4004 0x4005 0x4005	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1 RAM_2 RAM_2 RAM_3 RAM_4 RAM_5 RAM_5 RAM_6 RAM_7	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3 Raw data 4 Raw data 5 Raw data 6 Raw data 7
R/W R/W R/W R/W R/W R/W R/W Read-only	0x3000 0x3001 - 0x3FFF 0x4000 0x4001 0x4002 0x4003 0x4003 0x4005 0x4006 0x4006	REG_I2C_ADDRESS REG_CONTROL Melexis reserved REG_STATUS RAM_1 RAM_2 RAM_3 RAM_5 RAM_7 RAM_8	I ² C slave address >> 1 Control register, measurement mode Status register: data available RAM Raw data 1 Raw data 2 Raw data 3 Raw data 4 Raw data 5 Raw data 6 Raw data 7 Raw data 8

Table 7: Memory table

Important! The width of the EEPROM is 16 bit.

Some calibration parameters are 32 bit and split up into two 16 bit numbers in EEPROM. The least significant 16 bits of the parameter starts on the address shown in the Memory table. <u>Example</u>: To retrieve value EE_Aa (32bit) = EE_Aa_MS (at 0x2415) << 16 | EE_Aa_LS (at 0x2414)



9. Control and configuration registers

Several registers are available to control and configure the measurements:

9.1. REG_CONTROL

REG_CONTROL controls the measurement handling and data storage.

Bits	Parameter	Description	See section
15:12	MLX internal		
10	MLX internal		
9	MLX internal		
8:4	MLX internal		
3	SOC	starts a measurement when being in (sleeping) step mode	11
2:1	mode[1:0]	defines the operating mode (step mode or continuous mode)	11
0	MLX internal		

Table 8: REG_CONTROL register

Note that this register is initialized during POR by the EEPROM word EE_CONTROL.

Several measurement modes exist. These modes are controlled by bits mode[1:0] in register REG_CONTROL. In continuous mode the measurements are constantly running while in step mode the state machine will execute only one measurement which is initiated by soc bit. After finishing the measurement it will go in wait state until the next measurement is initiated by soc. The measurements are following the measurement sequence as defined in the measurement table.

The different possible measurement modes are:

- mode[1:0] = 01: Enables the <u>sleeping step mode</u>. In this mode the device will be by default in sleep.
 On request (soc bit), the device will power-on, the state machine will do one measurement, will go into sleep and will wait for next command.
- mode[1:0] = 10: Enables the <u>step mode</u>. In this mode the state machine will do one measurement upon request (soc bit) and will wait for next command. The device remains powered all time in this mode.
- mode[1:0] = 11: Device is in <u>continuous mode</u>. Measurements are executed continuously. The device remains powered all time in this mode.

Switching between the step modes and continuous mode has only effect after the current measurement has finished (not waiting till end of measurement table was reached).



9.2. REG_STATUS

REG_STATUS allows checking in which state the device is and indicates when measurements are finished.

Bits	Parameter	Description	See section
10	device_busy	Read-only Flag indicating that a measurement is being executed (1: measurement ongoing) In sleep mode, this flag is always low. In continuous mode, this flag is always high. In step mode, this flag is high during one measurement.	
9	eeprom_busy	Read-only Flag indicating that the eeprom is busy (0: not busy) Eeprom being busy is defined as follows: - at start-up, the eeprom is busy and remains busy till initialization phase (eeprom copy) has finished - during eeprom write/erase, the eeprom is busy	
8	brown_out	Bit is set to 0 Customer should set bit to 1 When device is reset, the bit is set to 0 and reset can be detected	
6:2	cycle_pos	Read-only Cycle_pos returns the current position of the measurement defined by the measurement table (number from 0 to 31)	
0	new_data	Customer should set bit to 0 When a measurement is done, the bit is set to 1 Customer can readout the data and reset the bit to 0	<u>11</u>

Table 9: REG_STATUS register



10. I²C commands

This device is based on I^2C specification Rev.5 – October 9th 2012. I^2C FM+ mode is supported.

The sensor implements following I²C features:

- Slave mode only
- 7-bits addressing
- Modes: Standard-mode, Fast-mode, Fast-mode Plus
- Incremental addressing allowing a block of addresses to be accessed inside one I²C sequence

As a standard, the device responds to the 7-bit slave address: 0x3A.

The least significant bit of the address is determined by the status of the ADDR-pin (either connected to ground or supply) and is taken in after power-up or reset command if the change is made in EEPROM.



Important! The device will not respond if the I²C address is changed to 0 (and ADDR pin is low). The only way to get the device to respond is to pull the ADDR pin high.

The slave address will be changed to 1 and communication is possible.

The following I²C commands are implemented:

- Read/write access to internal memories and registers
 - □ Addressed write
 - Addressed read
- Global reset / addressed reset
- EEPROM unlock for CUST access
- **Important!** The device shall not execute measurements when performing EEPROM memory operations (I²C read/write instructions in EEPROM address range)! Hence, the device shall be put in halt mode or in a stepping mode before doing EEPROM read/write operations.



10.1. Addressed read

The addressed read command allows doing an incremental read-out, starting from any given address within the memory space.





Important! An addressed read is only valid when combining directly an addressed write and a direct read through a repeated START condition. In case the read and write part are separated by a STOP condition, or in case the read is not directly following the write, or in case the slave address is not identical for both, the command will not be seen as an addressed read. As a result, the second read will in practice act as a direct read.

As soon as incremental addressing leaves the address space, the slave will respond with all 8'hFF.

10.2. Addressed write

The addressed write command allows doing an incremental write, starting from any given address within the memory space.



Figure 5: Addressed write

Important! The slave is sending ACK/NACK based on the fact whether it was able to write data (timing, end of register space, access rights).

The slave will automatically increment the address of the write byte, independent if it gave an ACK or a NACK to the master. It is up to the master to re-write the byte afterwards.

Before writing to EEPROM it is necessary to erase the specific address location in EEPROM. This is done by first writing 0x0000. Then the new data can be written.

When the device is busy with the write operation to EEPROM, new write commands will be ignored. A read operation will return invalid data. The fact that the device is busy is indicated via the bit device_busy in REG_STATUS.



10.3. Global reset



This command resets all devices on the I²C bus (based on the general call address 0x00).



10.4. Addressed reset

This command resets the addressed device only (based on the I²C address).



Figure 7: Addressed reset

10.5. EEPROM unlock for customer access

This command unlocks the EEPROM allowing only one write operation to an EEPROM word in the customer part of the EEPROM.

After the EEPROM write, the EEPROM access goes back to the "NoKey" access mode.



Figure 8: EEPROM unlock



10.6. Direct read

The direct read command allows an incremental read out at a default start address.

This default start address is fixed to the register location REG_STATUS (0x3FFF).

According to the I²C specification, the master will keep sending an acknowledge (A) until it want to stop. This is indicated by sending a NACK. As a result, the slave will stop driving the SDA-bus as soon as a NACK is received by the master.

As soon as the incremental addressing leaves the address space, the slave will respond with all 8'hFF.

SCL				
-----	--	--	--	--

SDA						MSByte of DEF. ADDR	LSByte of DEF. ADDR)_(M	//SByte of DEF. ADDR + 1	LSByte	of DEF. ADDR + 1		MSByte of DEF. ADDR + x	MSByte of DEF. ADDR + x	
	S 0 1	1 1	0	1 0	RA	A A	A	A	А		А	A		A	N P A
	S	lave ad	ldress												к

Figure 9: Direct read



11. Operating Modes

The device has two states of operation: **sleep state** and **active state**.

Sleep state

In this state, most of the circuitry is disabled to limit the current consumption to a few μ A.

Active state

In this state, the sensor is active.

Several measurement modes exist. These modes are controlled by bits mode[1:0] in register REG_CONTROL[2:1]. In continuous mode the measurements are constantly running while in step mode the state machine will execute only one measurement which is initiated by soc bit. After finishing the measurement it will go in wait state until the next measurement is initiated by soc. The measurements are following the measurement sequence as defined in the measurement table.

The different possible measurement modes are:

mode[1:0] = 01: Enables the <u>sleeping step mode</u>.

The device will be by default in sleep mode. On request (soc bit), the device will power-on, the state machine will do one measurement, will go into sleep and will wait for next command.

mode[1:0] = 10: Enables the <u>step mode</u>.

The state machine will do one measurement upon request (soc bit) and will wait for next command. The device remains powered all time in this mode.

mode[1:0] = 11: Device is in <u>continuous mode</u>.

Measurements are executed continuously. The device remains powered all time in this mode.

Switching between the step modes and continuous mode has only effect after the current measurement has finished (not waiting till end of measurement table was reached).



12. Temperature calculation

To calculate the ambient and object temperature, a set of 2 measurements is required:

- Measurement 1: RAM_4, RAM_5, RAM_6;
- Measurement 2: RAM_7, RAM_8, RAM_9;

One should notice this requires double the measurement time than specified (= 2 * 512ms). However, this is only valid for the very first calculation. After the first calculation, TA and TO should be calculated with the next measurement.

Example:

t0:	Measurement 1 (cycle_pos = 1)	=>	no calculation of TA or TO possible because not all parameters are known		
t1:	Measurement 2 (cycle_pos = 2)	=>	calculate TA (RAM_6, RAM_9) calculate TO (RAM_7, RAM_8, RAM_6, RAM_9)	=>	1 s.
t2:	Measurement 3 (= 1) (cycle_pos = 1)	=>	calculate TA (RAM_6, RAM_9) calculate TO (RAM_4, RAM_5, RAM_6, RAM_9)	=>	0.512 s.
t3:	Measurement 4 (= 2) (cycle_pos = 2)	=>	calculate TA (RAM_6, RAM_9) calculate TO (RAM_7, RAM_8, RAM_6, RAM_9)	=>	0.512 s.

t4: ...

To calculate the new ambient and object temperature RAM_6 and RAM_9 have to be used. The choice between [RAM_4 and RAM_5] or [RAM_7 and RAM_8] depends on the current measurement. REG_STATUS[6:2] (= "cycle_pos") returns the current position of the measurement defined in the measurement table.

Using the current and the data from measurement (x-1), TA and TO can be calculated every 512ms. The complete measurement sequence can be automated by using the new_data bit in combination with cycle_pos bits.

The sequence should look like the following:

- Write new_data = 0
- Check when new_data = 1
- Read cycle_pos to get measurement pointer

□ If cycle_pos = 1

Calculate TA and TO based on RAM_4, RAM_5, RAM_6, RAM_9

□ If cycle_pos = 2

- Calculate TA and TO based on RAM_7, RAM_8, RAM_6, RAM_9
- Return to top

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12.1. Pre-calculations

12.1.1. Ambient

$$VR_{TA} = RAM_9 + Gb * \frac{RAM_6}{12}$$
$$Gb = EE_Gb * 2^{-10}$$

$$AMB = \left[\frac{RAM_{-}6}{12}\right] / VR_{TA} * 2^{19}$$

The parameter EE_Gb is a signed 16-bit number.

12.1.2. Object

$S = \frac{RAM_4 + RAM_5}{2}$
OR
$S = \frac{RAM_7 + RAM_8}{2}$
$VR_{TO} = RAM_9 + Ka * \frac{RAM_6}{12}$
$Ka = EE_Ka * 2^{-10}$
$S_{TO} = \left[\frac{S}{12}\right] / VR_{TO} * 2^{19}$

The parameter EE_Ka is a signed 16-bit number.



12.2. Ambient temperature

Ta (sensor temperature in °C) = $P_0 + \frac{AMB - P_R}{P_G} + P_T * (AMB - P_R)^2$

With:

Ta in degrees Celsius $P_R = EE_P_R * 2^{-8}$ $P_O = EE_P_O * 2^{-8}$ $P_G = EE_P_G * 2^{-20}$ $P_T = EE_P_T * 2^{-44}$

The parameters EE_P_R, EE_P_O, EE_P_G and EE_P_T are signed 32-bit numbers.

12.3. Object temperature

T0 (object temperature in °C) = $\sqrt[4]{\frac{S_{T0}}{\epsilon * Fa * Ha * (1 + Ga * (TO_{DUT} - TO_0) + Fb * (TA_{DUT} - TA_0))} + Ta_{[K]}^4}$ - 273.15 - Hb

With:

 $\begin{array}{lll} Fa & = EE_Fa * 2^{-46} \\ Fb & = EE_Fb * 2^{-36} \\ Ga & = EE_Ga * 2^{-36} \\ Ha & = EE_Ha * 2^{-14} \\ Hb & = EE_Hb * 2^{-14} \\ TO_0 & = 25^{\circ}C \\ TA_0 & = 25^{\circ}C \\ TA_Dutr & = \frac{(AMB - Eb)}{Ea} + 25 \\ Ea & = EE_Ea * 2^{-16} \\ Eb & = EE_Eb * 2^{-8} \\ Ta_{[K]} = TA_{DUT} + 273.15 \text{ in Kelvin} \\ TO_{DUT} & = Object temperature in 25^{\circ}C \\ \epsilon & = 1 = Object Emissivity parameter (not stored in EEPROM, but part of the 'app') \end{array}$

The parameters EE_Ea, EE_Eb, EE_Fa, EE_Fb, EE_Ga are signed 32-bit numbers. The parameters EE_Gb, EE_Ka, EE_Ha and EE_Hb are signed 16-bit numbers.

Note:

One can see that to compute "To (object temperature)", "To" already needs to be known. "To (object temperature)" is computed in an iterative manner. In the first iteration "To" is assumed to be 25°C. In the 2nd iteration the result of first iteration is used, and in the 3rd iteration the end result is obtained. (See example on next page).



12.4. Example Temperature Calculation

Assumed are the following calibration parameters read from EEPROM:

ADDR PARAM		DATA (hex)	hex to dec	Conversion to use in formula		
0x240C	EE_P_R [15:0]	0103		D D coordon * 2 ⁸ 22000 01		
0x240D	EE_P_R [31:16]	005D	$EE_P_R = 005D0103_{hex} = 6095107_{dec}$	P_R = 6095107 * 2 = 23809.01		
0x240E	EE_P_G [15:0]	FAE5		D C 05705247 * 2 ⁻²⁰ C1 04025		
0x240F	EE_P_G [31:16]	051C	$EE_P_G = 051CFAE5_{hex} = 85785317_{dec}$	r_u - 65/6531/ 2 = 81.81125		
0x2410	EE_P_T [15:0]	0000		D T 0 * 2 ⁻⁴⁴		
0x2411	EE_P_T [31:16]	0000	$EE_P_I = 0000000_{hex} = 0_{dec}$	P_I = 0 * 2 = 0		
0x2412	EE_P_O [15:0]	1900	FF D Q 00001000 C400	D O (400 * 2 ⁻⁸ D		
0x2413	EE_P_O [31:16]	0000	$EE_P_O = 00001900_{hex} = 6400_{dec}$	r_0 - 0400 · 2 = 23		
0x2424	EE_Ea [15:0]	CFAE		F- F2C4F02 * 2 ⁻¹⁵ 04 0442F		
0x2425	EE_Ea [31:16]	0051	$EE_Ed = OUSICFAE_{hex} = S301582_{dec}$	Ed = 23012957 · 2 = 81.81122		
0x2426	EE_Eb [15:0]	0103	$F_{1} = F_{1} = 00000000000000000000000000000000000$	$r_{h} = c_{00}c_{10}c_{7} * 2^{-8} = 22800.01$		
0x2427	EE_Eb [31:16]	005D	EE_ED = 003D0103hex = 0033107dec	ED = 6095107 + 2 = 23809.01		
0x2428	EE_Fa [15:0]	6351		E 2 - EEE000E2 * 2 ⁻⁴⁶ - 7 0E 07		
0x2429	EE_Fa [31:16]	0350	EE_Fd = 05500551 _{hex} = 5559995 _{dec}	Fa = 55599953 * 2 = 7.9E-07		
0x242A	EE_Fb [15:0]	71F1	EE Eh - EE2571E1 - 21100/21	Eb = $21100421 * 2^{-36} = 0.00045$		
0x242B	EE_Fb [31:16]	FE25	LL_FD = FL2371F1 _{hex} = -51100431 _{dec}	FD = -51100451 2 = -0.00045		
0x242C	EE_Ga [15:0]	A7A4		$c_{2} = 22577052 * 2^{-36} = 0.00040$		
0x242D	EE_Ga [31:16]	FDFF	$LL_da = rDrrA/AJ_{hex} = -55577052_{dec}$	Ga = -33577052 · 2 = -0.00049		
0x242E	EE_Gb [15:0]	2600	$EE_Gb = 2600_{hex} = 9728_{dec}$	Gb = 9728 * 2 ⁻¹⁰ = 9.5		
0x242F	EE_Ka [15:0]	2A00	$EE_Ka = 2A00_{hex} = 10752_{dec}$	Ka = 10752 * 2 ⁻¹⁰ = 10.5		
0x2481	EE_Ha [15:0]	4000	$EE_Ha = 4000_{hex} = 16384_{dec}$	Ha = $16384 * 2^{-14} = 1$		
0x2482	EE_Hb [15:0]	0000	$EE_Hb = 0000_{hex} = 0_{dec}$	$Hb = 0 * 2^{-14} = 0$		



The returned values from the RAM (0x4000 to 0x4008):

ADDR	PARAM	DATA (hex)	DATA (dec)
0x4003	RAM_4	FF9B	-101
0x4004	RAM_5	FF9D	-99
0x4005	RAM_6	57E4	22500
0x4006	RAM_7	FF97	-105
0x4007	RAM_8	FF99	-103
0x4008	RAM_9	59D8	23000

12.4.1. Ambient temperature calculation

$$VR_{TA} = RAM_9 + Gb * \frac{RAM_6}{12} = 23000 + 9.5 * \frac{22500}{12}$$

 $VR_{TA} = 40812.5$

AMB =
$$\left[\frac{\text{RAM}_6}{12}\right] / \text{VR}_{\text{TA}} * 2^{19} = \left[\frac{22500}{12}\right] / 40812.5 * 2^{19}$$

AMB = 24086.73813

Ta (sensor temperature in °C) = P_0 + $\frac{AMB - P_R}{P_G}$ + P_T * (AMB - P_R)² Ta = 25 + $\frac{24086.73813 - 23809.01}{81.81125}$ + 0 * (24086.73813 - 23809.01)² Ta = 28.395°C

 $Ta = 28.4^{\circ}C$

12.4.2. Object temperature calculation

$$S = \frac{RAM_4 + RAM_5}{2} = \frac{(-101) + (-99)}{2}$$
$$S = -100$$
$$OR$$
$$S = \frac{RAM_7 + RAM_8}{2} = \frac{(-105) + (-103)}{2}$$
$$S = -104$$

Assumed is that RAM_4 and RAM_5 are updated lastly by the device (cycle_pos = 1)

$$VR_{TO} = RAM_9 + Ka * \frac{RAM_6}{12} = 23000 + 10.5 * \frac{22500}{12}$$
$$VR_{TO} = 42687.5$$

$$S_{TO} = \left[\frac{S}{12}\right] / VR_{TO} * 2^{19} = \left[\frac{-100}{12}\right] / 42687.5 * 2^{19}$$

 $S_{TO} = -102.35$

$$TO_0 = 25^{\circ}C$$

$$TA_0 = 25^{\circ}C$$

 $TA_{DUT} = \frac{(AMB - Eb)}{Ea} + 25 = \frac{(24086.73813 - 23809.01)}{81.81125} + 25 = 28.3947$

