

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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MMBV409LT1

Preferred Device

Silicon Tuning Diode

This device is designed in the Surface Mount package for general frequency control and tuning applications. It provides solid–state reliability in replacement of mechanical tuning methods.

Features

- High Q with Guaranteed Minimum Values at VHF Frequencies
- Controlled and Uniform Tuning Ratio
- Available in Surface Mount Package
- Pb-Free Package is Available

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|---|------------------|-------------|-------------|
| Reverse Voltage | V _R | 20 | Vdc |
| Forward Current | I _F | 200 | mAdc |
| Forward Power Dissipation @ T _A = 25°C Derate above 25°C | P _D | 225 1.8 | mW mW/°C |
| Junction Temperature | TJ | +125 | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



ON Semiconductor®

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SOT-23 (TO-236) CASE 318 STYLE 8

MARKING DIAGRAM



X5 = Specific Device Code

M = Date Code*

= Pb-Free Package

(Note: Microdot may be in either location)

*Date Code orientation and/or overbar may vary depending upon manufacturing location.

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|-------------|---------------------|-----------------------|
| MMBV409LT1 | SOT-23 | 3,000 / Tape & Reel |
| MMBV409LT1G | SOT-23 (Pb-Free) | 3,000 / Tape & Reel |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

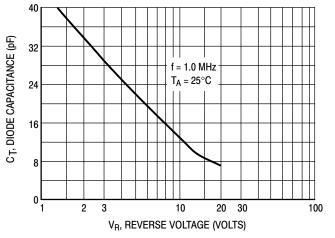
| Characteristic | Symbol | Min | Тур | Max | Unit |
|---|--------------------|-----|-----|-----|--------|
| Reverse Breakdown Voltage $(I_R = 10 \mu Adc)$ | V _{(BR)R} | 20 | _ | - | Vdc |
| Reverse Voltage Leakage Current (V _R = 15 Vdc) | I _R | - | - | 0.1 | μAdc |
| Diode Capacitance Temperature Coefficient (V _R = 3.0 Vdc, f = 1.0 MHz) | TC _C | - | 300 | - | ppm/°C |

| | C _t , Diode Capacitance V _R = 3.0 Vdc, f = 1.0 MHz pF | | Q, Figure of Merit V _R = 3.0 Vdc f = 50 MHz | C _R , Capacitance Ratio C ₃ /C ₈ f = 1.0 MHz (Note 1) | | |
|------------|---|-----|--|--|-----|-----|
| Device | Min | Nom | Max | Min | Min | Max |
| MMBV409LT1 | 26 | 29 | 32 | 200 | 1.5 | 1.9 |

^{1.} C_R is the ratio of C_t measured at 3 Vdc divided by C_t measured at 8 Vdc.

TYPICAL CHARACTERISTICS

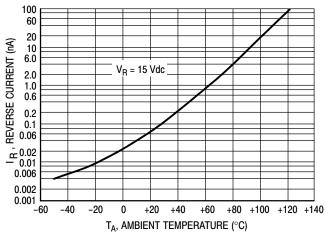
1000

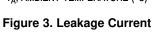


100 V_R = 3 Vdc T_A = 25°C T

Figure 1. Diode Capacitance

Figure 2. Figure of Merit





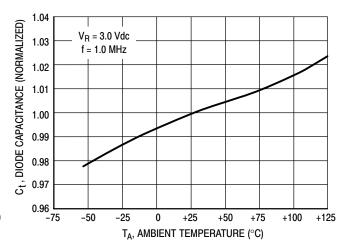
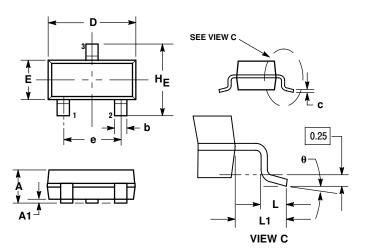


Figure 4. Diode Capacitance

MMBV409LT1

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 ISSUE AN



NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.
- 2. GONTHOLEING SIMILONS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.
- 4. 318-01 THRU -07 AND -09 OBSOLETE, NEW STANDARD 318-08.

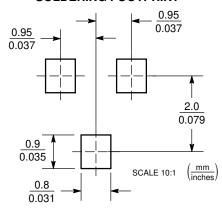
| | MILLIMETERS | | | INCHES | | | |
|-----|-------------|------|------|--------|-------|-------|--|
| DIM | MIN | NOM | MAX | MIN | NOM | MAX | |
| Α | 0.89 | 1.00 | 1.11 | 0.035 | 0.040 | 0.044 | |
| A1 | 0.01 | 0.06 | 0.10 | 0.001 | 0.002 | 0.004 | |
| b | 0.37 | 0.44 | 0.50 | 0.015 | 0.018 | 0.020 | |
| С | 0.09 | 0.13 | 0.18 | 0.003 | 0.005 | 0.007 | |
| D | 2.80 | 2.90 | 3.04 | 0.110 | 0.114 | 0.120 | |
| E | 1.20 | 1.30 | 1.40 | 0.047 | 0.051 | 0.055 | |
| е | 1.78 | 1.90 | 2.04 | 0.070 | 0.075 | 0.081 | |
| L | 0.10 | 0.20 | 0.30 | 0.004 | 0.008 | 0.012 | |
| L1 | 0.35 | 0.54 | 0.69 | 0.014 | 0.021 | 0.029 | |
| HE | 2 10 | 2 40 | 2 64 | 0.083 | 0.094 | 0 104 | |

STYLE 8:

PIN 1. ANODE

- 2. NO CONNECTION
- 3. CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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