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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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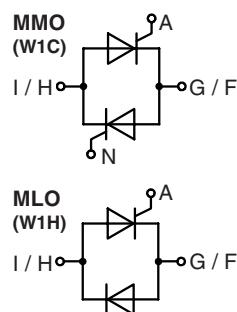
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AC Controller Modules

I_{RMS} = 112 A
V_{RRM} = 800-1400 V

Preliminary Data

| V _{RSM} | V _{RRM} | Type |
|------------------|------------------|---------------|
| V _{DSM} | V _{DRM} | |
| V | V | |
| 800 | 800 | MMO 110-08io7 |
| 1200 | 1200 | MMO 110-12io7 |
| 1400 | 1400 | MMO 110-14io7 |
| | | MLO 110-08io7 |
| | | MLO 110-12io7 |
| | | MLO 110-14io7 |



E 72873

| Symbol | Conditions | Maximum Ratings | | |
|-----------------------|--|---|-----------------------------|--------------------------------------|
| I _{RMS} | T _C = 85°C, 50 - 400 Hz, module | 112 | A | |
| I _{TRMS} | | 81 | A | |
| I _{TAVM} | T _C = 85°C; (180° sine) | 51 | A | |
| I _{TSM} | T _{VJ} = 45°C V _R = 0 | t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine | 1000 1070 | A A |
| | T _{VJ} = 125°C V _R = 0 | t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine | 870 930 | A A |
| I ² t | T _{VJ} = 45°C V _R = 0 | t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine | 5000 4810 | A ² s A ² s |
| | T _{VJ} = 125°C V _R = 0 | t = 10 ms (50 Hz), sine t = 8.3 ms (60 Hz), sine | 3780 3630 | A ² s A ² s |
| (di/dt) _{cr} | T _{VJ} = 125°C f = 50 Hz, t _p = 200 µs | repetitive, I _T = 50 A | 100 | A/µs |
| | V _D = 2/3 V _{DRM} I _G = 0.45 A di _G /dt = 0.45 A/µs | non repetitive, I _T = I _{TAVM} | 500 | A/µs |
| (dv/dt) _{cr} | T _{VJ} = 125°C; V _{DR} = 2/3 V _{DRM} R _{GR} = ∞; method 1 (linear voltage rise) | | 1000 | V/µs |
| P _{GM} | T _{VJ} = 125°C I _T = I _{TAVM} | t _p = 30 µs t _p = 300 µs | 10 5 | W W |
| P _{GAVM} | | | 0.5 | W |
| V _{RGM} | | | 10 | V |
| T _{VJ} | | | -40...+150 | °C |
| T _{VJM} | | | 150 | °C |
| T _{stg} | | | -40...+125 | °C |
| V _{ISOL} | 50/60 Hz, RMS I _{ISOL} ≤ 1 mA | t = 1 min t = 1 s | 2500 3000 | V~ V~ |
| M _d | Mounting torque (M4) | | 1.5...2.0/14...18 Nm/lb.in. | |
| Weight | typ. | | 18 | g |

Data according to IEC 60747 and to a single thyristor/diode unless otherwise stated.

IXYS reserve the right to change limits, conditions and dimensions.

| Symbol | Conditions | Characteristic Values | | | |
|------------|--|------------------------|------|-----------|----|
| I_D, I_R | $T_{VJ} = 125^\circ C; V_R = V_{RRM}; V_D = V_{DRM}$ | ≤ | 5 | mA | |
| V_T | $I_T = 150 A; T_{VJ} = 25^\circ C$ | ≤ | 1.57 | V | |
| V_{TO} | For power-loss calculations only | 0.85 | | V | |
| r_T | | 5.6 | | $m\Omega$ | |
| V_{GT} | $V_D = 6 V$ | $T_{VJ} = 25^\circ C$ | ≤ | 1.5 | V |
| | | $T_{VJ} = -40^\circ C$ | ≤ | 1.9 | V |
| I_{GT} | $V_D = 6 V$ | $T_{VJ} = 25^\circ C$ | ≤ | 100 | mA |
| | | $T_{VJ} = -40^\circ C$ | ≤ | 200 | mA |
| V_{GD} | $T_{VJ} = 125^\circ C; V_D = \frac{2}{3} V_{DRM}$ | ≤ | 0.2 | V | |
| I_{GD} | | ≤ | 1 | mA | |
| I_L | $T_{VJ} = 25^\circ C; t_p = 10 \mu s$ | ≤ | 200 | mA | |
| | $I_G = 0.45 A; di_G/dt = 0.45 A/\mu s$ | | | | |
| I_H | $T_{VJ} = 25^\circ C; V_D = 6 V; R_{GK} = \infty$ | ≤ | 100 | mA | |
| t_{gd} | $T_{VJ} = 25^\circ C; V_D = \frac{1}{2} V_{DRM}$ | ≤ | 2 | μs | |
| | $I_G = 0.45 A; di_G/dt = 0.45 A/\mu s$ | | | | |
| R_{thJC} | per thyristor; DC | 0.8 | | K/W | |
| | per module | 0.4 | | K/W | |
| R_{thCH} | per thyristor; sine 180° el | typ. | 0.12 | K/W | |
| | per module | typ. | 0.06 | K/W | |
| d_s | Creeping distance on surface | 11.2 | | mm | |
| d_a | Creepage distance in air | 17.0 | | mm | |
| a | Max. allowable acceleration | 50 | | m/s^2 | |

Dimensions in mm (1 mm = 0.0394")

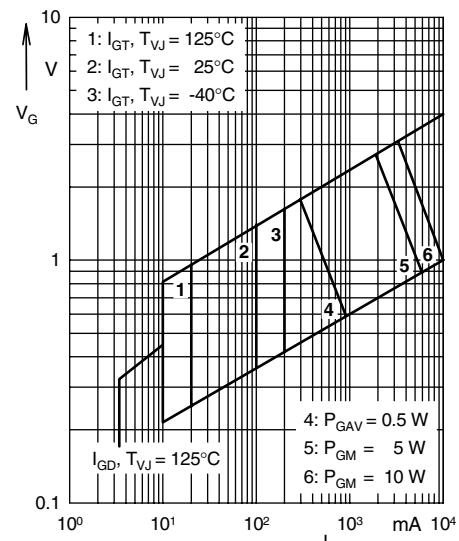
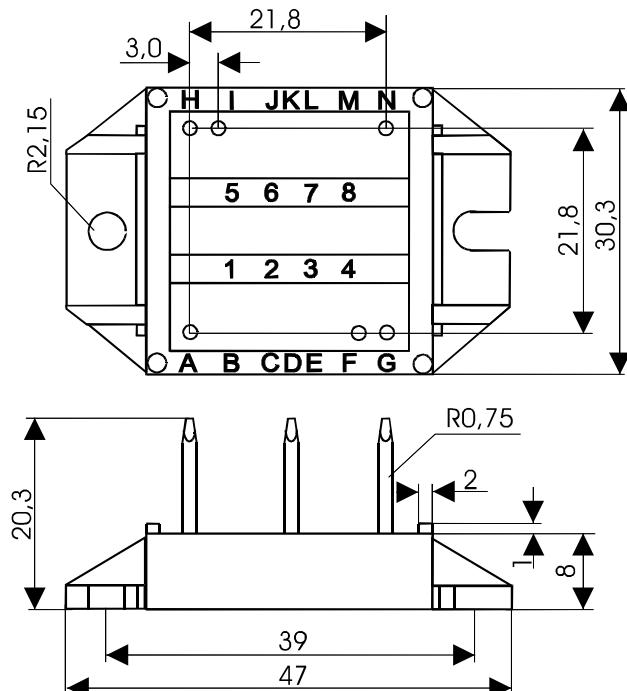


Fig. 1 Gate trigger characteristics

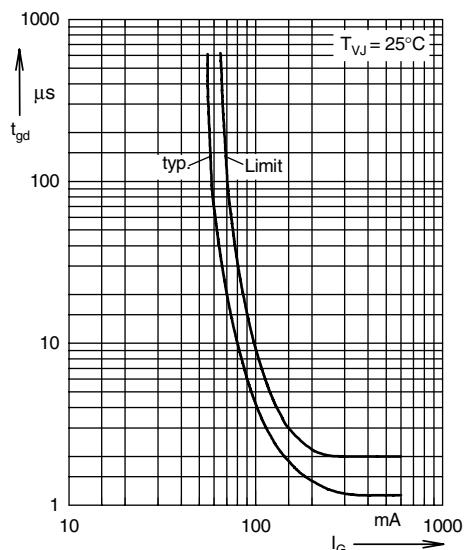


Fig. 2 Gate trigger delay time