imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

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Thyristor

MMO90-14io6

V_{RRM}	=	1400 V
I _{tav}	=	50 A
V _T		1,2 V

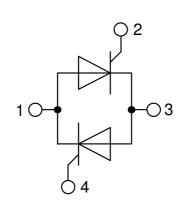
AC Controlling 1~ full-controlled

Part number MMO90-14io6



Backside: isolated **E**72873

20150827b



Features / Advantages:

- Thyristor for line frequency
- Planar passivated chip
- Long-term stability

Applications:

- Line rectifying 50/60 Hz
- Softstart AC motor control
- DC Motor control
- Power converter
- AC power control
- Lighting and temperature control

Package: SOT-227B (minibloc)

- Isolation Voltage: 3000 V~
- Industry standard outline
- RoHS compliant
- Epoxy meets UL 94V-0
- Base plate: Copper
- internally DCB isolated
- Advanced power cycling

Terms Conditions of usage:

The data contained in this product data sheet is exclusively intended for technically trained staff. The user will have to evaluate the suitability of the product for the intended application and the completeness of the product data with respect to his application. The specifications of our components may not be considered as an assurance of component characteristics. The information in the valid application- and assembly notes must be considered. Should you require product information in excess of the data given in this product data sheet or which concerns the specific application of your product, please contact the sales office, which is responsible for you. Due to technical requirements our product may contain dangerous substances. For information on the types in question please contact the sales office, which is responsible for you. Should you intend to use the product in aviation, in health or live endangering or life support applications, please notify. For any such application we urgently recommend

to perform joint risk and quality assessments;
the conclusion of quality agreements;

- to establish joint measures of an ongoing product survey, and that we may make delivery dependent on the realization of any such measures.

IXYS reserves the right to change limits, conditions and dimensions.

Data according to IEC 60747and per semiconductor unless otherwise specified

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MMO90-14io6

Thyristo				1	Ratings		į
Symbol	Definition	Conditions		min.	typ.	max.	Uni
V _{RSM/DSM}	max. non-repetitive reverse/forwar	d blocking voltage	$T_{VJ} = 25^{\circ}C$			1500	١
V _{RRM/DRM}	max. repetitive reverse/forward blo	cking voltage	$T_{VJ} = 25^{\circ}C$			1400	١
R/D	reverse current, drain current	$V_{R/D} = 1400 V$	$T_{vJ} = 25^{\circ}C$			100	μ/
		$V_{R/D} = 1400 V$	$T_{vJ} = 125^{\circ}C$			10	m/
V _T	forward voltage drop	$I_{T} = 50 \text{ A}$	$T_{VJ} = 25^{\circ}C$			1,27	١
		$I_{T} = 100 \text{ A}$				1,53	١
		$I_{T} = 50 \text{ A}$	$T_{VJ} = 150 ^{\circ}\text{C}$			1,20	١
		$I_{T} = 100 \text{ A}$				1,50	١
ITAV	average forward current	$T_c = 95^{\circ}C$	$T_{vJ} = 150^{\circ}C$			50	F
	RMS forward current per phase	180° sine				110	A
ν _{το}	threshold voltage		T _{vJ} = 150°C			0,88	\
r _T	slope resistance } for power los	ss calculation only				6	mΩ
R _{thJC}	thermal resistance junction to case					0,6	K/W
R _{thCH}	thermal resistance case to heatsin				0,10		K/W
P _{tot}	total power dissipation		$T_c = 25^{\circ}C$			210	W
I _{TSM}	max. forward surge current	t = 10 ms; (50 Hz), sine	$T_{v,i} = 45^{\circ}C$			800	A
- 1 5 M	Ū.	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$			865	A
		t = 10 ms; (50 Hz), sine	$T_{y,l} = 150^{\circ}C$			680	
		t = 8,3 ms; (60 Hz), sine	$V_{\rm N} = 0$ V			735	A
l²t	value for fusing	t = 0,0 ms; (00 Hz), sinc t = 10 ms; (50 Hz), sinc	$\frac{V_{\rm R}}{T_{\rm VJ}} = 45^{\circ}{\rm C}$			3,20	kA²s
	value for ruening	t = 8,3 ms; (60 Hz), sine	$V_{\rm R} = 0 V$			3,20	1
		t = 0.3 ms, (00 Hz), sine t = 10 ms; (50 Hz), sine	$T_{V,I} = 150^{\circ}C$			2,31	kA ² s
		t = 8,3 ms; (60 Hz), sine				2,31	i .
<u>^</u>	junction capacitance		$V_{R} = 0 V$ $T_{VJ} = 25^{\circ}C$		32	2,25	
C,		$V_{\rm R} = 400 \text{V}$ f = 1 MHz			32	10	pF W
P _{GM}	max. gate power dissipation	$t_{\rm P} = 30 \mu s$	$T_c = 150 \circ C$			10	
-		t _P = 300 μs				5	W W
P _{GAV}	average gate power dissipation	T (5000 (50))				0,5	W
(di/dt) _{cr}	critical rate of rise of current	$T_{VJ} = 150 ^{\circ}C; f = 50 \text{Hz}$ re	• • •			100	A/με
		$t_{P} = 200 \mu s; di_{G}/dt = 0.3 A/\mu s; -$					
			on-repet., $I_{T} = 50 \text{ A}$				A/μs
(dv/dt) _{cr}	critical rate of rise of voltage	$V = \frac{2}{3} V_{DRM}$	$T_{vJ} = 150^{\circ}C$			1000	V/με
		$R_{GK} = \infty$; method 1 (linear volta					
V _{GT}	gate trigger voltage	$V_{\rm D} = 6 \text{ V}$	$T_{vJ} = 25^{\circ}C$			1,5	١
			$T_{vJ} = -40 ^{\circ}\text{C}$			1,6	١
I _{GT}	gate trigger current	$V_{D} = 6 V$	$T_{vJ} = 25^{\circ}C$			100	mA
			$T_{vJ} = -40 ^{\circ}C$			200	mÆ
V _{gd}	gate non-trigger voltage	$V_{\rm D} = \frac{2}{3} V_{\rm DRM}$	$T_{vJ} = 150^{\circ}C$			0,2	\
I _{gd}	gate non-trigger current					5	mA
I.	latching current	t _p = 10 μs	$T_{vJ} = 25 \degree C$			250	mA
		$I_{G} = 0,3 \text{ A}; \text{ di}_{G}/\text{dt} = 0,3 \text{ A}/\mu s$	6				
I _H	holding current	$V_{\rm D} = 6 \text{ V} \text{R}_{\rm GK} = \infty$	$T_{vJ} = 25 ^{\circ}C$			100	mA
t _{gd}	gate controlled delay time	$V_{\rm D} = \frac{1}{2} V_{\rm DBM}$	$T_{vJ} = 25 ^{\circ}\text{C}$			2	μ
an	- ,	$I_{\rm G} = 0.3 \text{A}; \text{di}_{\rm G}/\text{dt} = 0.3 \text{A}/\mu\text{s}$				-	P**
+	turn-off time	$V_{\rm R} = 100 \text{ V}; \ I_{\rm T} = 50 \text{ A}; \ V = 3$			150		μ
t _q		$v_{R} = 100 v$, $t_{T} = 30 A$, $v = 7$ di/dt = 10 A/µs dv/dt = 15 V			150		μ

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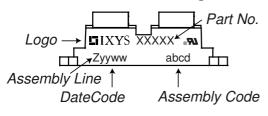
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MMO90-14io6

Package SOT-227B (minibloc)			Ratings					
Symbol	Definition	Conditions			min.	typ.	max.	Unit
I _{RMS}	RMS current	per terminal					150	Α
T _{vj}	virtual junction temperature)			-40		150	°C
T _{op}	operation temperature				-40		125	°C
T _{stg}	storage temperature				-40		150	°C
Weight						30		g
M _D	mounting torque				1,1		1,5	Nm
M _T	terminal torque				1,1		1,5	Nm
d _{Spp/App}	oroonago distanco on surfa	ce striking distance through air	terminal to terminal	10,5	3,2			mm
d _{Spb/Apb}	creepage ustance on suna	ice suiking distance unough an	terminal to backside	8,6	6,8			mm
V	isolation voltage	t = 1 second			3000			V
		t = 1 minute	50/60 Hz, RMS; liso∟ ≤ 1 mA		2500			V

Product Marking



Ordering	Ordering Number	Marking on Product	Delivery Mode	Quantity	Code No.
Standard	MMO90-14io6	MMO90-14io6	Tube	10	485144

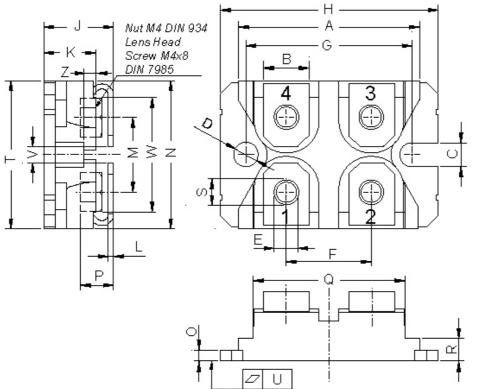
Equiva	alent Circuits for	Simulation	* on die level	$T_{vJ} = 150 \circ C$
)[Thyristor		
V _{0 max}	threshold voltage	0,88		V
$\mathbf{R}_{0 \text{ max}}$	slope resistance *	4,2		mΩ

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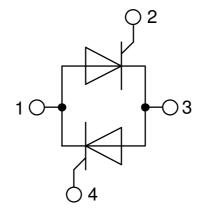
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Outlines SOT-227B (minibloc)



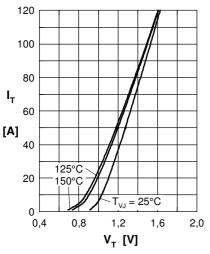
Dim	Millimeter		Inc	hes	
Dim.	min	max	min	max	
Α	31.50	31.88	1.240	1.255	
В	7.80	8.20	0.307	0.323	
С	4.09	4.29	0.161	0.169	
D	4.09	4.29	0.161	0.169	
Е	4.09	4.29	0.161	0.169	
F	14.91	15.11	0.587	0.595	
G	30.12	30.30	1.186	1.193	
Н	37.80	38.23	1.488	1.505	
J	11.68	12.22	0.460	0.481	
К	8.92	9.60	0.351	0.378	
L	0.74	0.84	0.029	0.033	
Μ	12.50	13.10	0.492	0.516	
Ν	25.15	25.42	0.990	1.001	
0	1.95	2.13	0.077	0.084	
Ρ	4.95	6.20	0.195	0.244	
Q	26.54	26.90	1.045	1.059	
R	3.94	4.42	0.155	0.167	
S	4.55	4.85	0.179	0.191	
Т	24.59	25.25	0.968	0.994	
U	-0.05	0.10	-0.002	0.004	
V	3.20	5.50	0.126	0.217	
W	19.81	21.08	0.780	0.830	
Ζ	2.50	2.70	0.098	0.106	



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700

600

500

400

300

0,01

I_{TSM}

[A]

Fig. 1 Forward characteristics

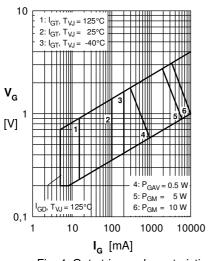


Fig. 4 Gate trigger characteristics

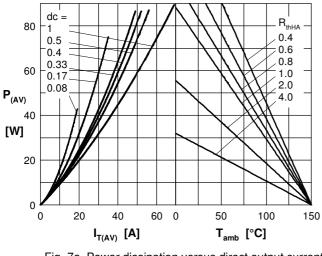
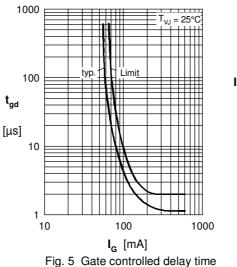


Fig. 7a Power dissipation versus direct output current Fig. 7b and ambient temperature



3500 $V_{R} = 0 V$ 3000 2500 l²t 2000 $T_{VJ} = 45^{\circ}C$ [A²s] 1500 = 125°C T_{VJ} 1000 500 2 3 4 5 6 7 8 9 1 0 1 1 t [ms]

Fig. 3 l^2 t versus time (1-10 ms)



50 Hz, 80% V_{BB}

= 45°C

0,1

t [s]

Fig. 2 Surge overload current

Γ_{vJ} = 125°

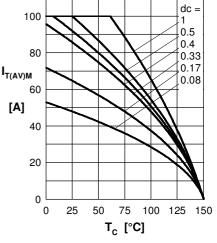
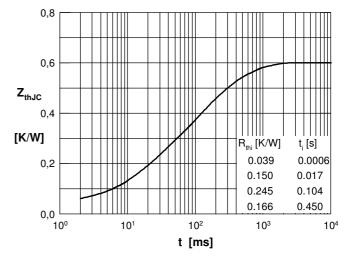


Fig. 6 Max. forward current at case temperature





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