



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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NPN SURFACE MOUNT DARLINGTON TRANSISTOR
Features

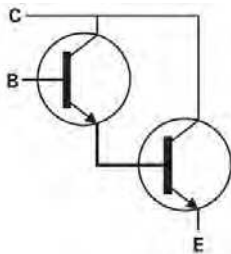
- 40V Darlington Transistor
- Epitaxial Planar Die Construction
- Ideal for Low Power Amplification and Switching
- High Current Gain
- Ultra-Small Surface Mount Package
- **Lead Free, RoHS Compliant (Note 1)**
- **Halogen and Antimony Free "Green" Device (Note 2)**

Mechanical Data

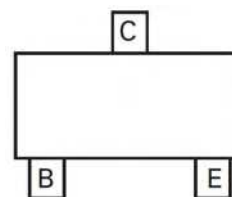
- Case: SOT-323
- Case Material: Molded Plastic, "Green" Molding Compound, Note 4. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Weight: 0.006 grams (approximate)



Top View



Device symbol

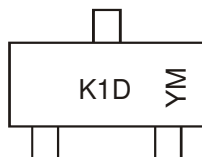


Pin-out Top view

Ordering Information (Note 3)

Product	Marking	Reel size (inches)	Tape width (mm)	Quantity per reel
MMST6427-7-F	K1D	7	8	3,000

- Notes:
1. No purposefully added lead.
 2. Diodes Inc.'s "Green" Policy can be found on our website at <http://www.diodes.com>
 3. For Packaging Details, go to our website at <http://www.diodes.com>.

Marking Information


K1D= Product Type Marking Code
 YM = Date Code Marking
 Y = Year ex: X = 2010
 M = Month ex: 9 = September

Date Code Key

Year	2010	2011	2012	2013	2014	2015
Code	X	Y	Z	A	B	C

Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CBO}	40	V
Collector-Emitter Voltage	V _{CEO}	40	V
Emitter-Base Voltage	V _{EBO}	12	V
Collector Current - Continuous (Note 4)	I _C	500	mA
Power Dissipation (Note 4)	P _d	200	mW
Thermal Resistance, Junction to Ambient (Note 4)	R _{θJA}	625	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

Electrical Characteristics @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Min	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 5)					
Collector-Base Breakdown Voltage	BV _{CBO}	40	—	V	I _C = 100μA, I _E = 0
Collector-Emitter Breakdown Voltage	BV _{CEO}	40	—	V	I _C = 10mA, I _B = 0
Emitter-Base Breakdown Voltage	BV _{EBO}	12	—	V	I _E = 10μA, I _C = 0
Collector Cutoff Current	I _{CBO}	—	50	nA	V _{CB} = 30V, I _E = 0
Collector Cutoff Current	I _{CEO}	—	1.0	μA	V _{CE} = 25V, I _B = 0
Emitter Cutoff Current	I _{EBO}	—	50	nA	V _{EB} = 10V, I _C = 0
ON CHARACTERISTICS (Note 5)					
DC Current Gain	h _{FE}	10,000 20,000 14,000	100,000 200,000 140,000	—	I _C = 10mA, V _{CE} = 5.0V I _C = 100mA, V _{CE} = 5.0V I _C = 500mA, V _{CE} = 5.0V
Collector-Emitter Saturation Voltage	V _{CE(sat)}	—	1.2 1.5	V	I _C = 50mA, I _B = 0.5mA I _C = 500mA, I _B = 0.5mA
Base-Emitter Saturation Voltage	V _{BE(sat)}	—	2.0	V	I _C = 500mA, I _B = 0.5mA
Base-Emitter On Voltage	V _{BE(on)}	—	1.75	V	I _C = 50mA, V _{CE} = 5.0V
SMALL SIGNAL CHARACTERISTICS					
Output Capacitance	C _{obo}	8.0 Typical		pF	V _{CB} = 10V, f = 1.0MHz, I _E = 0
Input Capacitance	C _{ibo}	15 Typical		pF	V _{EB} = 0.5V, f = 1.0MHz, I _C = 0

- Notes: 4. Device mounted on 25mm x 22 mm x 1.6mm FR4 PCB, 1oz copper, singled sided
5. Short duration pulse test used to minimize self-heating effect.

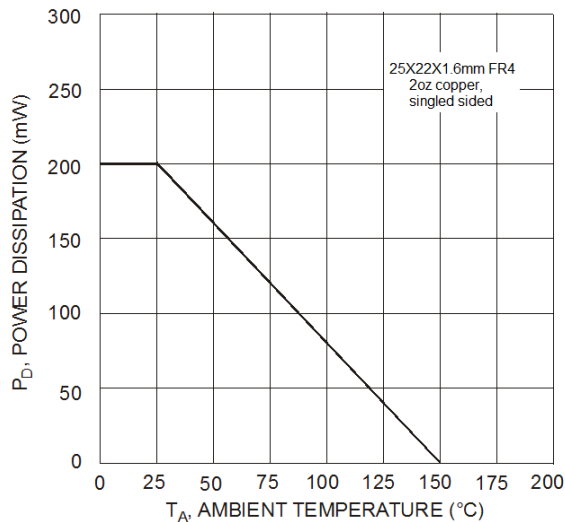


Fig. 1, Max Power Dissipation vs. Ambient Temperature

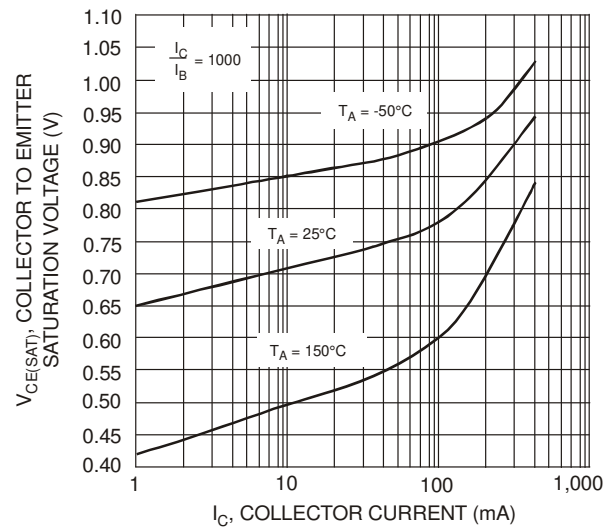


Fig. 2, Collector Emitter Saturation Voltage vs. Collector Current

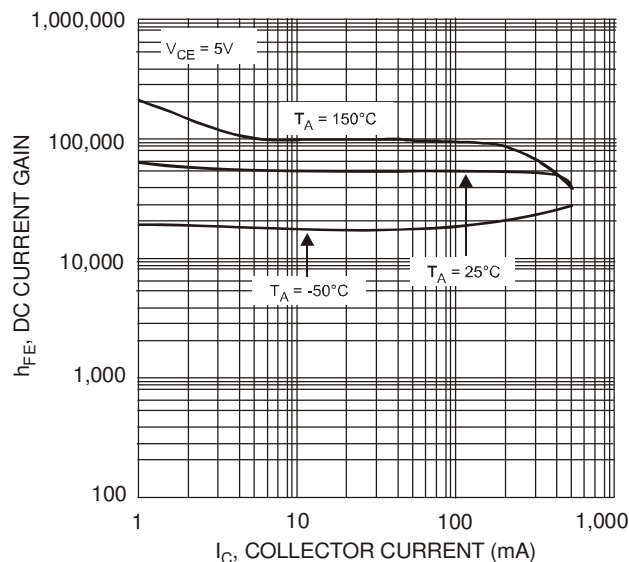


Fig. 3, DC Current Gain vs. Collector Current

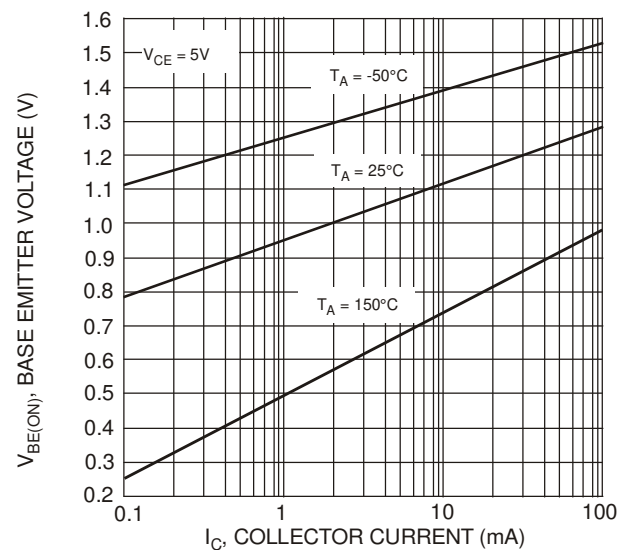


Fig. 4, Base Emitter Voltage vs. Collector Current

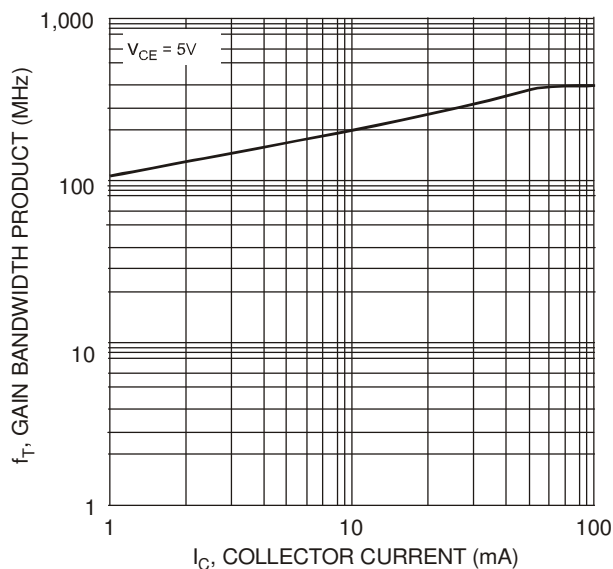
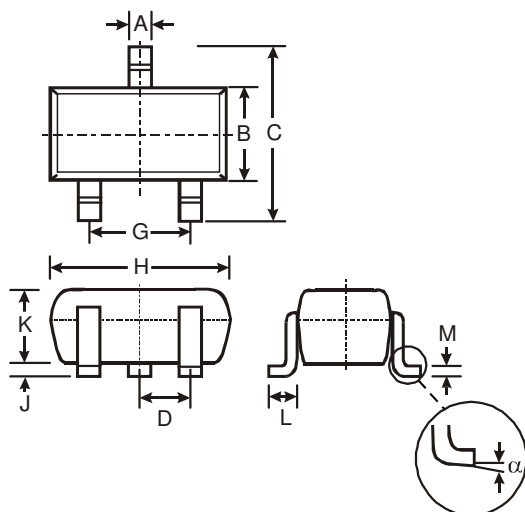


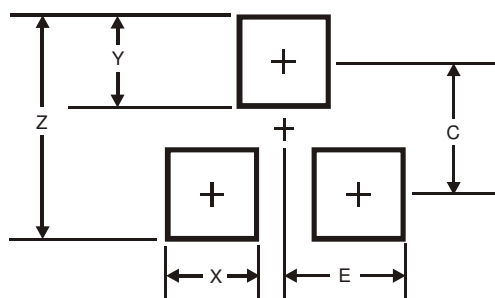
Fig. 5, Gain Bandwidth Product vs. Collector Current

Package Outline Dimensions



SOT323			
Dim	Min	Max	Typ
A	0.25	0.40	0.30
B	1.15	1.35	1.30
C	2.00	2.20	2.10
D	-	-	0.65
G	1.20	1.40	1.30
H	1.80	2.20	2.15
J	0.0	0.10	0.05
K	0.90	1.00	1.00
L	0.25	0.40	0.30
M	0.10	0.18	0.11
α	0°	8°	-
All Dimensions in mm			

Suggested Pad Layout



Dimensions	Value (in mm)
Z	2.8
X	0.7
Y	0.9
C	1.9
E	1.0

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