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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







# MMVL3102T1

**Preferred Device** 

# **Silicon Tuning Diode**

This device is designed in the Surface Mount package for general frequency control and tuning applications. It provides solid–state reliability in replacement of mechanical tuning methods.

#### **Features**

- High Q with Guaranteed Minimum Values at VHF Frequencies
- Controlled and Uniform Tuning Ratio
- Pb-Free Package is Available

#### **MAXIMUM RATINGS**

| Rating                     | Symbol         | Value | Unit |
|----------------------------|----------------|-------|------|
| Continuous Reverse Voltage | V <sub>R</sub> | 30    | Vdc  |
| Peak Forward Current       | IF             | 200   | mAdc |

#### THERMAL CHARACTERISTICS

| Characteristic  | Symbol                            | Max         | Unit        |
|---|-----------------------------------|-------------|-------------|
| Total Device Dissipation FR–5 Board,<br>T <sub>A</sub> = 25°C (Note 1)<br>Derate above 25°C | P <sub>D</sub>                    | 200<br>1.57 | mW<br>mW/°C |
| Thermal Resistance Junction-to-Ambient  | $R_{\theta JA}$                   | 635         | °C/W        |
| Junction and Storage Temperature  | T <sub>J</sub> , T <sub>stg</sub> | 150         | °C          |

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. FR-4 Minimum Pad



# ON Semiconductor®

http://onsemi.com

# 22 pF (Nominal) 30 VOLTS VOLTAGE VARIABLE CAPACITANCE DIODE





PLASTIC SOD-323 CASE 477 STYLE 1

# **MARKING DIAGRAM**



4C = Device Code M = Date Code\*

= Pb–Free Package

(Note: Microdot may be in either location) \*Date Code orientation may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

| Device      | Package              | Shipping <sup>†</sup> |
|-------------|----------------------|-----------------------|
| MMVL3102T1  | SOD-323              | 3000 / Tape & Reel    |
| MMVL3102T1G | SOD-323<br>(Pb-Free) | 3000 / Tape & Reel    |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

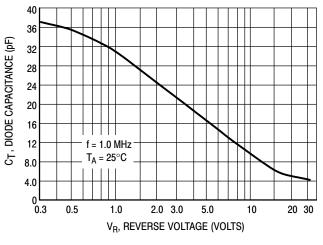
**Preferred** devices are recommended choices for future use and best overall value.

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

| Characteristic  |                    | Min | Тур | Max | Unit   |
|---|--------------------|-----|-----|-----|--------|
| Reverse Breakdown Voltage<br>(I <sub>R</sub> = 10 μAdc)                           | V <sub>(BR)R</sub> | 30  | -   | _   | Vdc    |
| Reverse Voltage Leakage Current (V <sub>R</sub> = 25 Vdc, T <sub>A</sub> = 25°C)  | I <sub>R</sub>     | _   | -   | 0.1 | μAdc   |
| Diode Capacitance Temperature Coefficient (V <sub>R</sub> = 4.0 Vdc, f = 1.0 MHz) | TC <sub>C</sub>    | -   | 300 | -   | ppm/°C |

|            | C <sub>t</sub> , Diode Capacitance<br>V <sub>R</sub> = 3.0 Vdc, f = 1.0 MHz<br>pF |    | Q, Figure of Merit<br>V <sub>R</sub> = 3.0 Vdc<br>f = 50 MHz | $C_R$ , Capacitance Ratio $C_3/C_{25}$ f = 1.0 MHz |     |     |
|------------|---|----|--|--|-----|-----|
| Device     | Min Nom Max   |    | Max  | Min  | Min | Тур |
| MMVL3102T1 | 20  | 22 | 25   | 200  | 4.5 | 4.8 |

### TYPICAL CHARACTERISTICS



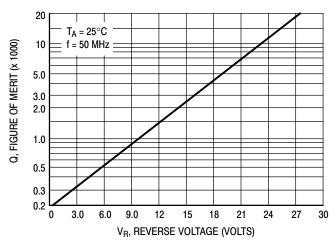
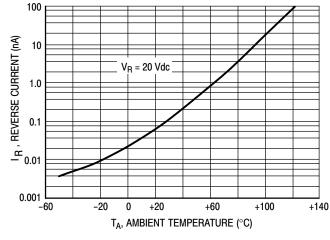


Figure 1. Diode Capacitance

Figure 2. Figure of Merit



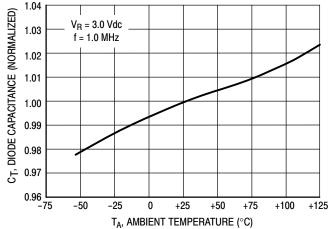


Figure 3. Leakage Current

Figure 4. Diode Capacitance

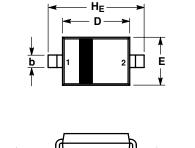
# NOTES ON TESTING AND SPECIFICATIONS

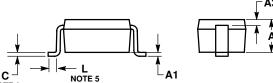
 $C_{R}$  is the ratio of  $C_{T}$  measured at 3.0 Vdc divided by  $C_{T}$  measured at 25 Vdc.

## MMVL3102T1

#### **PACKAGE DIMENSIONS**

SOD-323 CASE 477-02 ISSUE G





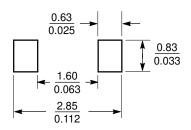
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
   V14 5M 1982
- Y14.5M, 1982.
  2. CONTROLLING DIMENSION: MILLIMETERS.
- LEAD THICKNESS SPECIFIED PER L/F DRAWING WITH SOLDER PLATING.
- 4. DIMENSIONS A AND B DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.
- DIMENSION L IS MEASURED FROM END OF BADIUS.

|     | MILLIMETERS            |          |       | INCHES    |       |       |  |
|-----|------------------------|----------|-------|-----------|-------|-------|--|
| DIM | MIN                    | NOM      | MAX   | MIN       | NOM   | MAX   |  |
| Α   | 0.80                   | 0.90     | 1.00  | 0.031     | 0.035 | 0.040 |  |
| A1  | 0.00 0.05              |          | 0.10  | 0.000     | 0.002 | 0.004 |  |
| A3  | (                      | ).15 REI | F     | 0.006 REF |       |       |  |
| ь   | 0.25 0.32              |          | 0.4   | 0.010     | 0.012 | 0.016 |  |
| C   | 0.089                  | 0.12     | 0.177 | 0.003     | 0.005 | 0.007 |  |
| D   | 1.60 1.70<br>1.15 1.25 |          | 1.80  | 0.062     | 0.066 | 0.070 |  |
| Е   |                        |          | 1.35  | 0.045     | 0.049 | 0.053 |  |
| Ĺ   | 0.08                   |          |       | 0.003     |       |       |  |
| HE  | 2.30                   | 2.50     | 2.70  | 0.090     | 0.098 | 0.105 |  |

STYLE 1: PIN 1. CATHODE 2. ANODE

#### **SOLDERING FOOTPRINT\***



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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