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MN13822S

CMOS IC for Voltage Detection

Overview

The MN13822S are elements that monitor the power supply voltage supplied to microcomputers and other LSI systems and issue reset signals for initializing the system after the power is first applied or for preventing runaway operation when the supply voltage fluctuates.

This is a inverted CMOS output, choose the ideal element for your application from 5 detection ranks.

There is other output types, CMOS output (MN1382S) and N-channel open drain output (MN13821S).

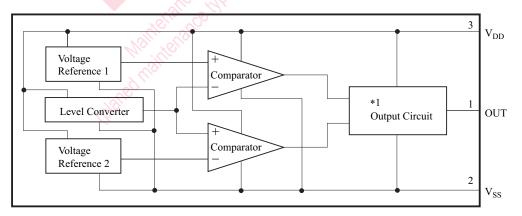
Features

- Three-pin element requiring no adjustment
- Highly precise detection voltage
- Detection voltage with hysteresis characteristic
- $\Delta VD = 50 \text{ mV}$ for ranks C
- $\Delta VD = 100 \text{ mV}$ for ranks L
- Low current consumption: $I_{DD} = 1 \ \mu A$ (typ.) for $V_{DD} = 5 \ V$
- Low fluctuation in detection voltage with tempera-ture (1 mV/°C (typ.))

Applications

- Battery checkers
- Power outage detectors
- Level discriminators
- Memory backup systems
- Microcomputer reset circuits
- Reset circuits for other electronic circuits

Block Diagram



Note) *1: Circuits vary slightly depending on the output type (CMOS output, N-channel open drain output, or inverted CMOSoutput)



- MINI-3DC
- Pin name
- 1: Out Reset signal output pin
- 2: V_{SS} Ground pin
- 3: V_{DD} Power supply pin



Note) Rank symbol will be marked on the package in the \Box area.

MN13822S

Detection Ranks (on Voltage)

Rank	Detection Voltage for Drop in	Power Supply Voltage V_{DL}	Unit	Detection Voltage H	Linit	
	Min	Max	Unit	Min	max	Unit
С	2.0	2.2		50		mV
G	2.4	2.6	V			
L	3.0	3.3			300	
R	4.0	4.3		100		
S	4.2	4.5				

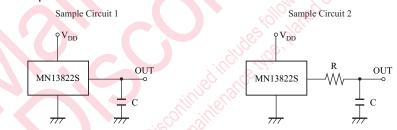
Absolute Maximum Ratings $V_{SS} = 0 V, T_a = 25^{\circ}C$

Parameter	Symbol	Rating	Unit	
Power supply voltage	V _{DD}	7.0	V	
Output voltage	Vo	-0.3 to $V_{DD}\!+\!0.3$	V	
Operating ambient temperature	T _{opr}	-20 to +70	°C	
Storage temperature	T _{stg}	-55 to +125	°C	

Recommended Operating Conditions $V_{SS} = 0 V, T_a = 25^{\circ}C$

Parameter	Symbol	Conditions	Min Typ	Max	Unit
Power supply voltage	V _{DD}	See Figures 1 and 4	1.5	6.0	V

Application Circuit Example



Note) Connect resistors, capacitors, and the like only to the output pin on the MN13822S element.

Note that connect-ing them to the power source pins changes V_{DH} , V_{DL} , and ΔVD . Select the values of R and C to match the application.

Electrical Characteristics

• DC Characteristics $V_{SS} = 0$ V, $T_a = -20^{\circ}$ C to $+70^{\circ}$ C

Parameter	Symbol	Symbol Conditions		Тур	Max	Unit
Power supply current	I _{DD}	$V_{DD} = 5 V^*$, Load resistor 10 k Ω		1	5	μΑ
Detection voltage for drop in power supply voltage	V _{DL}	$T_a = 25^{\circ}C$	For particulars, see the detection voltage rank table.			V
Detection voltage hysteresis width	ΔVD	See Figures 1 and 4				mV
High level output voitage	V _{OH}	$V_{\rm DD} = 1.8 \text{ V}, I_{\rm OH} = -0.5 \text{ mA}$	$0.8 \mathrm{V_{DD}}$		V _{DD}	V
Low level output voitage	V _{OL}	$V_{\rm DD} = 6.0$ V, $I_{\rm OH} = 0.3$ mA	V _{SS}		0.4	V

Note) *: This includes the output pin's leakage current.

• AC Characteristics $V_{SS} = 0 V$, $T_a = 25^{\circ}C$

Parameter	Symbol	Conditions		Allowable Value (typ)	Unit	
	Cymbol		Rank	Anowabie value (typ)	Unit	
			C	230.0		
			G	100.0	0.	
Reset release time	t _{OH}	See Figures 2 and 3	L	60.0	μs	
			R	30.0		
			S	30.0		
			C			
			G			
Reset time	t _{OL}	See Figures 2 and 3	L	3.0	μs	
			R	× 110°, 449°		
			S	0°, 0°,		

MN13822S

Panasonic

Reference Data

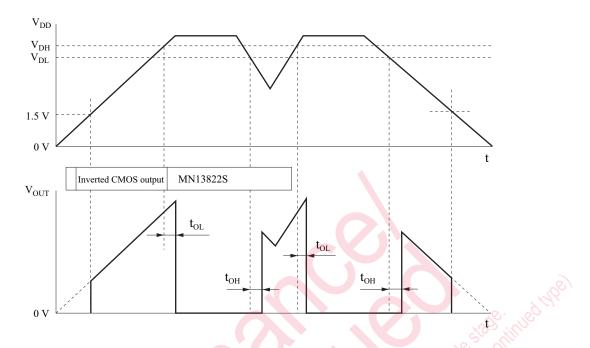


Figure 1. Description of Operation

Note) 1. Output cannot be specified for power supply voltages under 1.5 V because operation is not guaranteed for that range.

- 2. V_{DL} : Detection voltage for drop in power supply voltage
 - V_{DH} : Detection voltage for rise in power supply voltage
 - t_{OL} : Time lag between the time that the power supply voltage reaches the detection voltage (V_{DL} or V_{DH}) and the time that theoutput pin (OUT) goes to Low level.
 - t_{OH} : Time lag between the time that the power supply voltage reaches the detection voltage (V_{DL} or V_{DH}) and the time that theoutput pin (OUT) goes to High level.

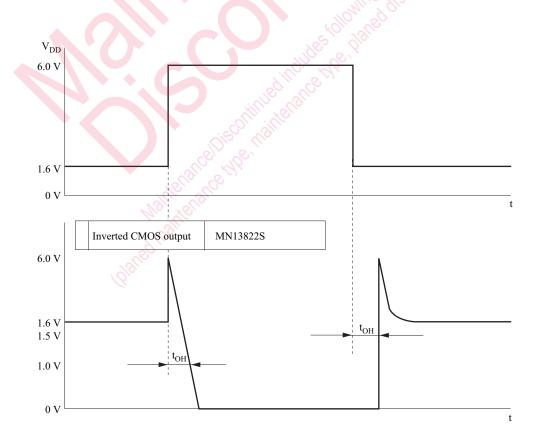


Figure 2. Description chart of Measuring the Output Characteristics

Reference Data (Continued)

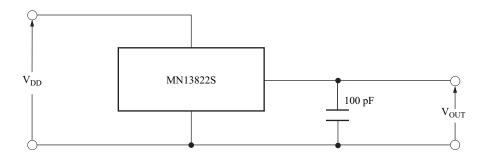


Figure 3. Circuit for Measuring the Output Characteristics

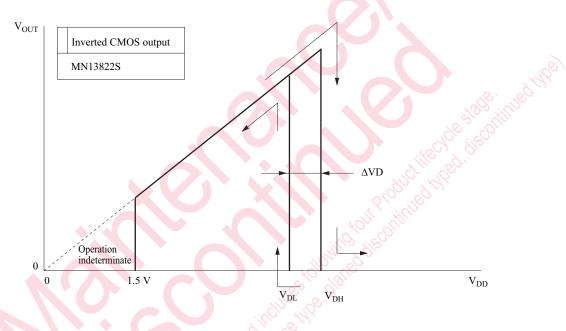
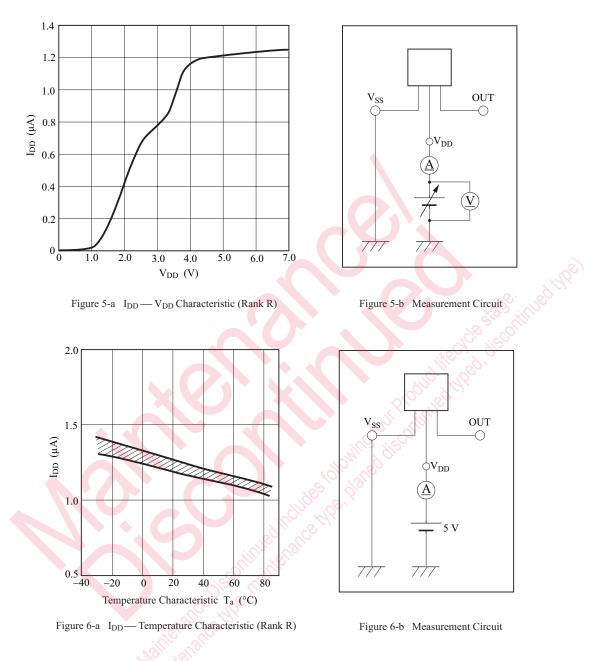


Figure 4. Description chart for Measuring the I/O Characteristics

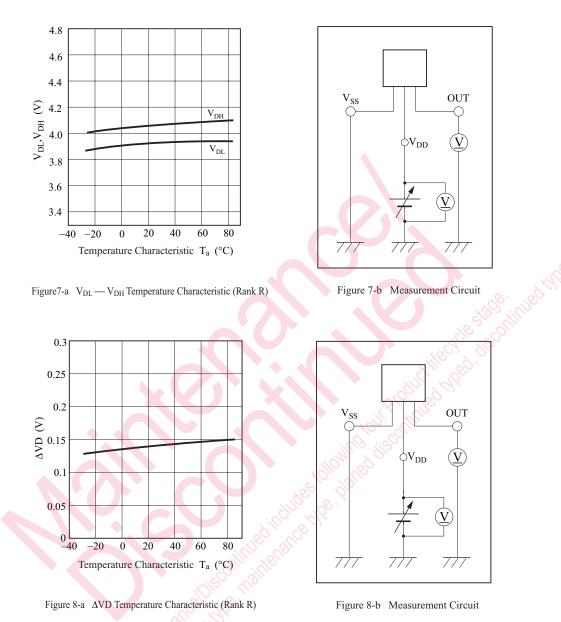
Note) 1. Output cannot be specified for power supply voltages under 1.5 V because operation is not guaranteed for that range.

- 2. V_{DL} : Detection voltage for drop in power supply voltage
 - $V_{\rm DH}$: Detection voltage for rise in power supply voltage \bigcirc

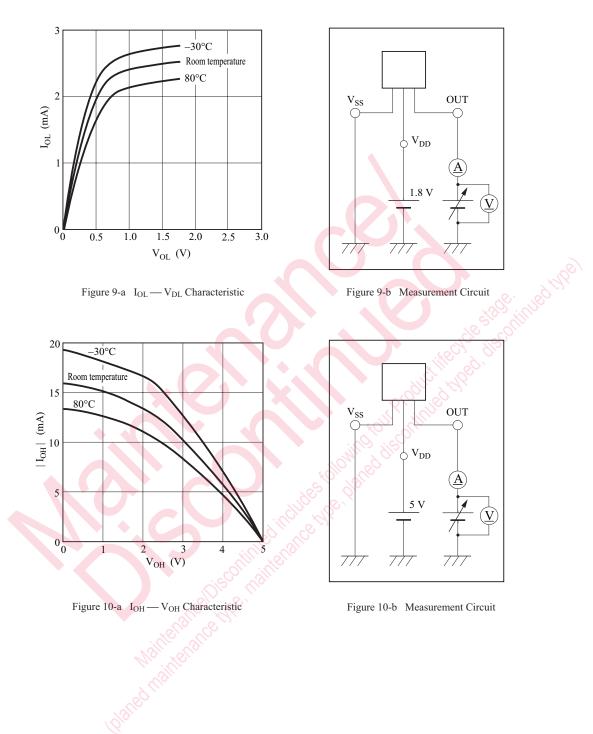
Reference Characteristics



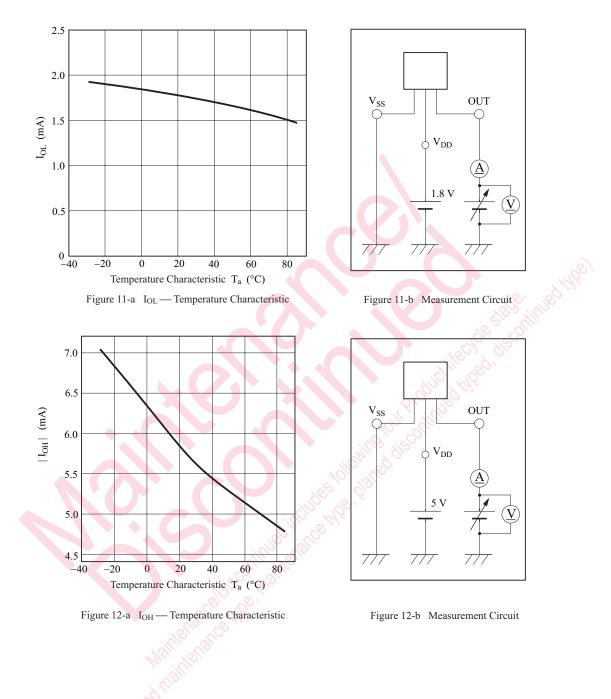
Reference Characteristics (Continued)



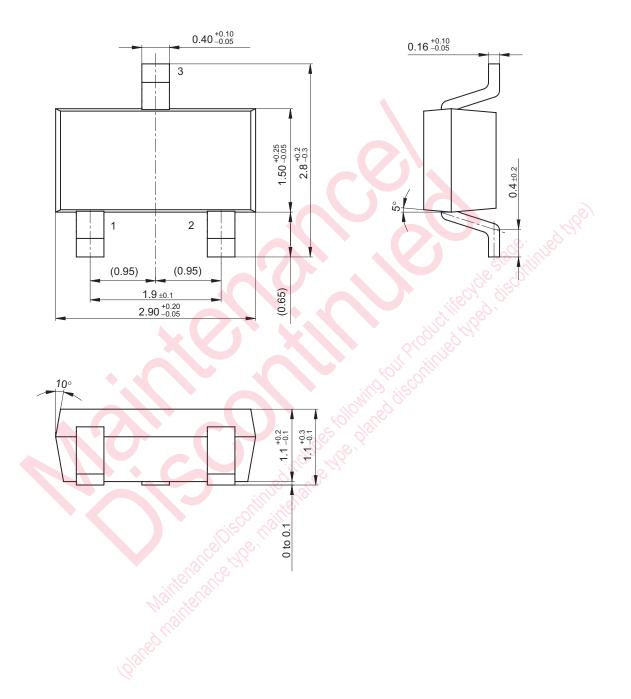
Reference Characteristics (Continued)



Reference Characteristics (Continued)



MINI-3DC



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