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## OPTICALLY COUPLED BILATERAL SWITCH NON-ZERO CROSSING TRIAC

## APPROVALS

- UL recognised, File No.E91231

Package Code " KK "

## 'X'SPECIFICATIONAPPROVALS

- VDE 0884 in 3 available lead form :-
- STD
- G form
- SMD approved to CECC 00802


## DESCRIPTION

The MOC3009,301_series are optically coupled isolators consisting of a Gallium Arsenide infrared emitting diode coupled with a light activated silicon bilateral switch performing the functions of a triac mounted in a standard 6 pin dual-in-line package.

## FEATURE

- Options :-

10 mm lead spread - add G after part no. Surface mount - add SM after part no. Tape\&reel - add SMT\&R after part no.

- High Isolation Voltage $\left(5.3 \mathrm{kV}_{\mathrm{RMS}}, 7.5 \mathrm{kV}_{\mathrm{PK}}\right)$
- 250 V Peak Blocking Voltage
- All electrical parameters $100 \%$ tested
- Custom electrical selections available


## APPLICATIONS

- CRTs
- PowerTriac Driver
- Motors
- Consumer appliances
- Printers




## ABSOLUTE MAXIMUM RATINGS

( $25{ }^{\circ} \mathrm{C}$ unless otherwise noted)

| Storage Temperature | $-55^{\circ} \mathrm{C}-+150^{\circ} \mathrm{C}$ |
| :--- | ---: |
| Operating Temperature | $-40^{\circ} \mathrm{C}-+100^{\circ} \mathrm{C}$ |
| Lead Soldering Temperature_ |  |
| (1.6mm from case for 10 seconds) |  |

## INPUTDIODE

$$
\begin{array}{ll}
\text { Forward Current } & 50 \mathrm{~mA} \\
\text { Reverse Voltage } & 6 \mathrm{~V} \\
\text { Power Dissipation } \\
\left(\text { derate linearly } 0.93 \mathrm{~mW} /{ }^{\circ} \mathrm{C} \text { above } 25^{\circ} \mathrm{C}\right)
\end{array} 70 \mathrm{~mW}
$$

## OUTPUT PHOTO TRIAC

Off-State Output Terminal Voltage ..... 250 V
Forward Current (Peak) ..... 1A
Power Dissipation ..... 300 mW(derate linearly $4.0 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $25^{\circ} \mathrm{C}$ )
POWERDISSIPATION
Total Power Dissipation330 mW330 mW(derate linearly $4.4 \mathrm{~mW} /{ }^{\circ} \mathrm{C}$ above $25^{\circ} \mathrm{C}$ )

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ELECTRICAL CHARACTERISTICS ( $\mathrm{T}_{\mathrm{A}}=25^{\circ} \mathrm{C}$ Unless otherwise noted )

| PARAMETER |  | MIN | TYP | MAX | UNITS | TEST CONDITION |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Input | Forward Voltage ( $\mathrm{V}_{\mathrm{F}}$ ) <br> Reverse Current $\left(\mathrm{I}_{\mathrm{R}}\right)$ |  | 1.2 | $\begin{aligned} & 1.5 \\ & 100 \end{aligned}$ | $\begin{aligned} & \mathrm{V} \\ & \mu \mathrm{~A} \end{aligned}$ | $\begin{aligned} & \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA} \\ & \mathrm{~V}_{\mathrm{R}}=6 \mathrm{~V} \end{aligned}$ |
| Output | Peak Off-state Current ( $\mathrm{I}_{\text {DRM }}$ ) <br> Peak Blocking Voltage ( $\mathrm{V}_{\text {DRM }}$ ) <br> On-state Voltage ( $\mathrm{V}_{\text {тM }}$ ) <br> Critical rate of rise of off-state <br> Voltage (dv/dt) ( note 1 ) <br> Critical rate of rise of commutating <br> Voltage (dv/dt) ( note 1 ) | $\begin{aligned} & 250 \\ & \\ & 0.1 \end{aligned}$ | $\begin{aligned} & 1.5 \\ & 10 \\ & 0.2 \end{aligned}$ | $\begin{aligned} & 100 \\ & 3.0 \end{aligned}$ | nA <br> V <br> V <br> V/ $\mu \mathrm{s}$ <br> $\mathrm{V} / \mu \mathrm{s}$ | $\begin{aligned} & \mathrm{V}_{\mathrm{DRM}}=250 \mathrm{~V}(\text { note } 1) \\ & \mathrm{I}_{\mathrm{DRM}}=100 \mathrm{nA} \\ & \mathrm{I}_{\mathrm{TM}}=100 \mathrm{~mA}(\text { peak }) \\ & \mathrm{I} \text { load }=15 \mathrm{~mA}, \\ & \mathrm{~V}_{\mathrm{IN}}=30 \mathrm{~V}(\text { fig } 1 .) \end{aligned}$ |
| Coupled | $\begin{gathered} \text { Input Current to Trigger }\left(\mathrm{I}_{\mathrm{FT}}\right)(\text { note } 2) \\ \text { MOC3009 } \\ \text { MOC3010 } \\ \text { MOC3011 } \\ \text { MOC3012 } \end{gathered}$ <br> Holding Current, either direction ( $\mathrm{I}_{\mathrm{H}}$ ) <br> Input to Output Isolation Voltage $\mathrm{V}_{\text {ISo }}$ | $\begin{aligned} & 5300 \\ & 7500 \end{aligned}$ | 100 | $\begin{aligned} & 30 \\ & 15 \\ & 10 \\ & 5 \end{aligned}$ | mA <br> mA <br> mA <br> mA <br> $\mu \mathrm{A}$ $\begin{aligned} & \mathrm{V}_{\mathrm{RMS}} \\ & \mathrm{~V}_{\mathrm{PK}} \end{aligned}$ | $\mathrm{V}_{\mathrm{D}}=3 \mathrm{~V}(\text { note } 2)$ <br> See note 3 <br> See note 3 |

Note 1. Test voltage must be applied within dv/dt rating.
Note 2. Guaranteed to trigger at an $\mathrm{I}_{\mathrm{F}}$ value less than or equal to max. $\mathrm{I}_{\mathrm{FT}}$, recommended $\mathrm{I}_{\mathrm{F}}$ lies between Rated $\mathrm{I}_{\mathrm{FT}}$ and absolute max. $\mathrm{I}_{\mathrm{FT}}$.
Note 3. Measured with input leads shorted together and output leads shorted together.

FIGURE 1




