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April 2015

MOC8021M, MOC8050M 6-Pin DIP Photodarlington Optocoupler (No Base Connection)

Features

- High BV_{CEO}:
 - Minimum 50 V (MOC8021M)
 - Minimum 80 V (MOC8050M)
- High Current Transfer Ratio:
 - Minimum 1000% (MOC8021M)
 - Minimum 500% (MOC8050M)
- No Base Connection for Improved Noise Immunity
- Safety and Regulatory Approvals:
 - UL1577, 4,170 VAC_{RMS} for 1 Minute
 - DIN-EN/IEC60747-5-5, 850 V Peak Working Insulation Voltage

Applications

- Appliances, Measuring Instruments
- I/O Interface for Computers
- Programmable Controllers
- Portable Electronics
- Interfacing and Coupling Systems of Different Potentials and Impedance
- Solid State Relays

Description

The MOC8021M and MOC8050M are photodarlingtontype optically coupled optocouplers. The devices have a gallium arsenide infrared emitting diode coupled with a silicon darlington phototransistor.

Schematic

ANODE 1 6 N/C CATHODE 2 N/C 3 4 EMITTER

Figure 1. Schematic

Package Outlines

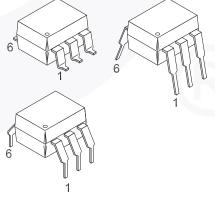


Figure 2. Package Outlines

Safety and Insulation Ratings

As per DIN EN/IEC 60747-5-5, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Parameter	Characteristics	
Installation Classifications per DIN VDE	< 150 V _{RMS}	I–IV
0110/1.89 Table 1, For Rated Mains Voltage	< 300 V _{RMS}	I–IV
Climatic Classification		55/100/21
Pollution Degree (DIN VDE 0110/1.89)		2
Comparative Tracking Index		175

Symbol	Parameter	Value	Unit
V	Input-to-Output Test Voltage, Method A, $V_{IORM} \times 1.6 = V_{PR}$, Type and Sample Test with $t_m = 10$ s, Partial Discharge < 5 pC	1360	V _{peak}
V_{PR}	Input-to-Output Test Voltage, Method B, $V_{IORM} \times 1.875 = V_{PR}$, 100% Production Test with $t_m = 1$ s, Partial Discharge < 5 pC	1594	V _{peak}
V _{IORM}	Maximum Working Insulation Voltage	850	V _{peak}
V _{IOTM}	Highest Allowable Over-Voltage	6000	V _{peak}
	External Creepage	≥ 7	mm
	External Clearance	≥ 7	mm
	External Clearance (for Option TV, 0.4" Lead Spacing)	≥ 10	mm
DTI	Distance Through Insulation (Insulation Thickness)	≥ 0.5	mm
T _S	Case Temperature ⁽¹⁾	175	°C
I _{S,INPUT}	Input Current ⁽¹⁾	350	mA
P _{S,OUTPUT}	Output Power ⁽¹⁾	800	mW
R _{IO}	Insulation Resistance at T _S , V _{IO} = 500 V ⁽¹⁾	> 10 ⁹	Ω

Note:

1. Safety limit values – maximum values allowed in the event of a failure.

Absolute Maximum Ratings

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit
TOTAL DEVIC	E		
T _{STG}	Storage Temperature	-40 to +125	°C
T _{OPR}	Operating Temperature	-40 to +100	°C
TJ	Junction Temperature	-40 to +125	°C
T _{SOL}	Lead Solder Temperature	260 for 10 seconds	°C
	Total Device Power Dissipation @ T _A = 25°C	270	mW
P_{D}	Derate Above 25°C	2.94	mW/°C
EMITTER			
I _F	DC/Average Forward Input Current	60	mA
V _R	Reverse Input Voltage	3	V
В	LED Power Dissipation @ T _A = 25°C	120	mW
P_{D}	Derate Above 25°C	1.41	mW/°C
DETECTOR			
I _C	Continuous Collector Current	150	mA
V _{CEO}	Collector-Emitter Voltage MOC8021M	50	V
	MOC8050M	80	V
P_{D}	Detector Power Dissipation @ T _A = 25°C	150	mW
	Derate Above 25°C	1.76	mW/°C

Electrical Characteristics

 $T_A = 25$ °C Unless otherwise specified.

Individual Component Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
EMITTER			•			
V _F	Input Forward Voltage	I _F = 10 mA		1.18	2.00	V
I _R	Reverse Leakage Current	V _R = 3.0 V		0.001	10	μΑ
DETECTO	R		•	•		!
	Collector-Emitter Breakdown Voltage					
BV_CEO	MOC8021M	$I_C = 1.0 \text{ mA}, I_F = 0$	50	100		V
	MOC8050M		80	100		V
BV _{ECO}	Emitter-Collector Breakdown Voltage	I _E = 100 μA, I _F = 0	5	10		V
I _{CEO}	Collector-Emitter Dark Current	V _{CE} = 60 V, I _F = 0			1	μΑ
C _{CE}	Capacitance	V _{CE} = 0 V, f = 1 MHz		8		pF

Transfer Characteristics

Symbol	Parameter	Test Conditions	Min.	Тур.	Max.	Unit
DC CHARA	CTERISTICS			\		
	Current Transfer Ratio, Collector to Emitter					
CTR	MOC8021M	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	1,000			%
	MOC8050M	$I_F = 10 \text{ mA}, V_{CE} = 1.5 \text{ V}$	500			%
AC CHARA	CTERISTICS					
t _{on}	Turn-on Time	$I_F = 5 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_L = 100 \Omega$		8.5		μs
t _{off}	Turn-off Time	$I_F = 5 \text{ mA}, V_{CC} = 10 \text{ V},$ $R_L = 100 \Omega$		95		μs

Isolation Characteristics

Symbol	Characteristic	Test Conditions	Min.	Тур.	Max.	Unit
V _{ISO}	Input-Output Isolation Voltage	t = 1 Minute	4170			VAC _{RMS}
C _{ISO}	Isolation Capacitance	V _{I-O} = 0 V, f = 1 MHz		0.2		pF
R _{ISO}	Isolation Resistance	V _{I-O} = ±500 VDC, T _A = 25°C	10 ¹¹			Ω

Typical Performance Curves

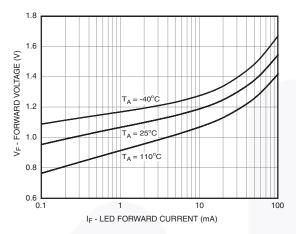


Figure 3. LED Forward Voltage vs. Forward Current

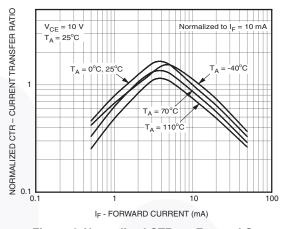


Figure 4. Normalized CTR vs. Forward Current

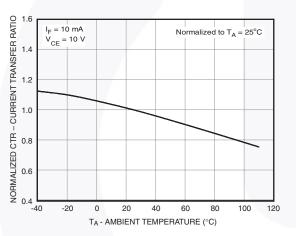


Figure 5. Normalized CTR vs. Ambient Temperature

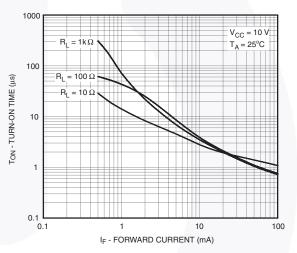


Figure 6. Turn-on Time vs. Forward Current

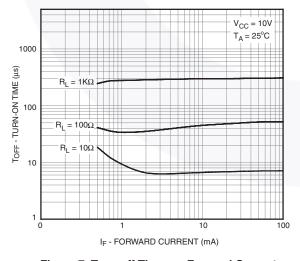


Figure 7. Turn-off Time vs. Forward Current

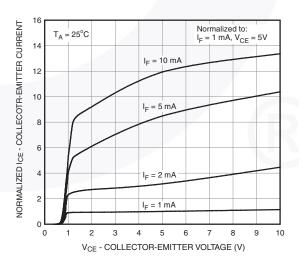


Figure 8. Normalized Collector-Emitter Current vs. Collector-Emitter Voltage

Typical Performance Curves (Continued)

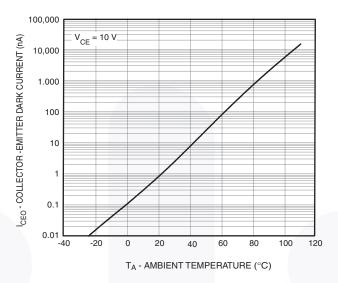


Figure 9. Dark Current vs. Ambient Temperature

Switching Time Test Circuit and Waveform

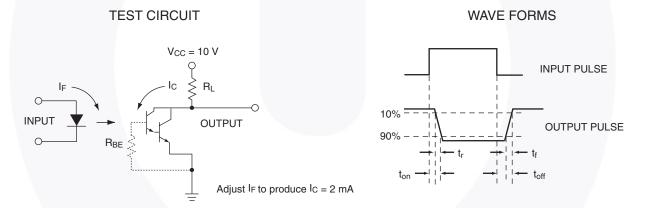
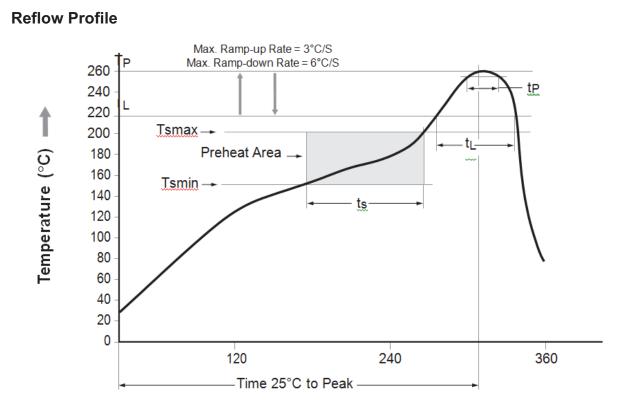


Figure 10. Switching Time Test Circuit and Waveform



Ordering Information

Part Number	Package	Packing Method
MOC8021M	DIP 6-Pin	Tube (50 Units)
MOC8021SM	SMT 6-Pin (Lead Bend)	Tube (50 Units)
MOC8021SR2M	SMT 6-Pin (Lead Bend)	Tape and Reel (1000 Units)
MOC8021VM	DIP 6-Pin, DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MOC8021SVM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tube (50 Units)
MOC8021SR2VM	SMT 6-Pin (Lead Bend), DIN EN/IEC60747-5-5 Option	Tape and Reel (1000 Units)
MOC8021TVM	DIP 6-Pin, 0.4" Lead Spacing, DIN EN/IEC60747-5-5 Option	Tube (50 Units)

Note:

2. The product orderable part number system listed in this table also applies to the MOC8050M device.

Marking Information

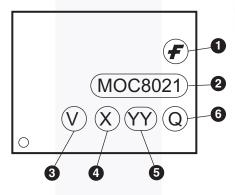
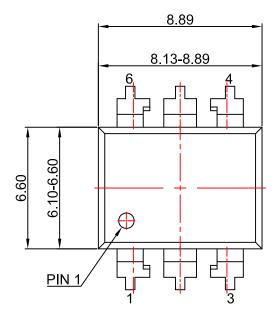
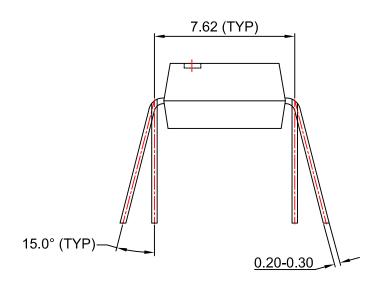


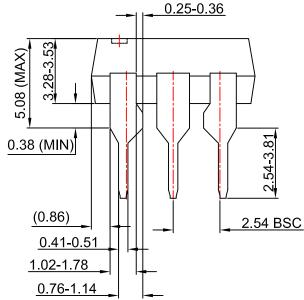
Figure 12. Top Mark

Table 1. Top Mark Definitions

1	Fairchild Logo
2	Device Number
3	DIN EN/IEC60747-5-5 Option (only appears on component ordered with this option)
4	One-Digit Year Code, e.g., "5"
5	Digit Work Week, Ranging from "01" to "53"
6	Assembly Package Code



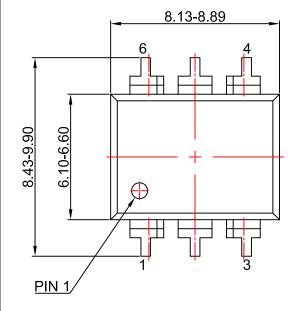


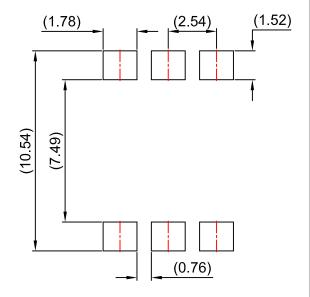


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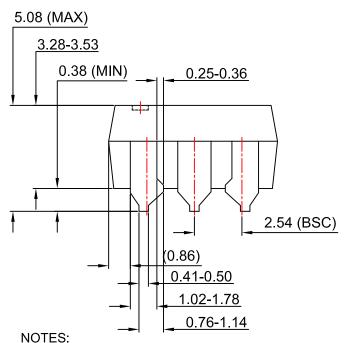
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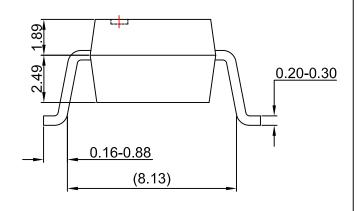






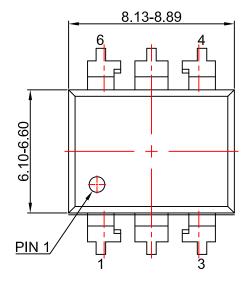
LAND PATTERN RECOMMENDATION

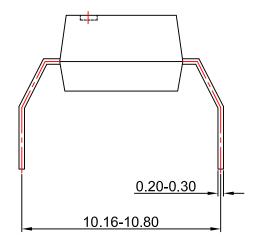


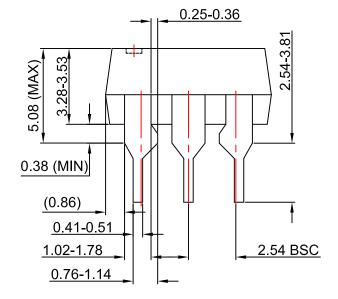


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