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2.0A Single Cell Switch Mode Battery Charger with Power Path Management (PPM) and 1.5A System Boost Current

#### **DESCRIPTION**

The MP2635 is a highly-integrated, flexible, switch-mode battery charge management and system power path management device for a single-cell Li-ion and Li-Polymer battery used in a wide range of portable applications.

The MP2635 has two operating modes—charge mode and boost mode—to allow management of system and battery power based on the state of the input.

When input power is present, the device operates in charge mode. It automatically detects the battery voltage and charges the battery in the three phases: trickle current, constant current and constant voltage. Other features include charge termination and autorecharge. This device also integrates both input current limit and input voltage regulation in order to manage input power and meet the priority of the system power demand.

In the absence of an input source, the MP2635 switches to boost mode through the MODE pin to power the SYS pins from the battery. The OLIM pin programs the output current limit in boost mode. The MP2635 also allows an output short-circuit thanks to an output disconnect feature, and can auto-recover when the short circuit fault is removed.

The MP2635 provides full operating status indication to distinguish charge mode from boost mode.

MP2635 The achieves EMI/EMC low performance with well-controlled switching edges.

To guarantee safe operation, the MP2635 limits the die temperature to a preset value 120°C. Other safety features include input over-voltage battery over-voltage protection, protection, thermal shutdown. batterv temperature monitoring, and a programmable timer to prevent prolonged charging of a dead battery.

The MP2635 has two battery full options. MP2635: 4.2V/3.6V; MP2635B: 4.35V/4.2V.

#### **FEATURES**

- 4.5V-to-6V Operating Input Voltage Range
- Power Management Function Integrated Input-Current Limit and Input-Voltage Regulation
- Up to 2A Programmable Charge Current
- Trickle-Charge Function
- Selectable Charge Voltage with 0.5% Accuracy: 3.6V/4.2V(MP2635) or 4.35V/4.2V(MP2635B)
- Negative Temperature Coefficient Pin for **Battery Temperature Monitoring**
- Programmable Timer Back-Up Protection
- Thermal Regulation and Thermal Shutdown
- Internal Battery Reverse Leakage Blocking
- Reverse Boost Operation Mode for System
- Up to 91% 5V Boost Mode Efficiency @ 1A
- Programmable Output Current Limit for **Boost Mode**
- Integrated Short Circuit Protection for Boost Mode

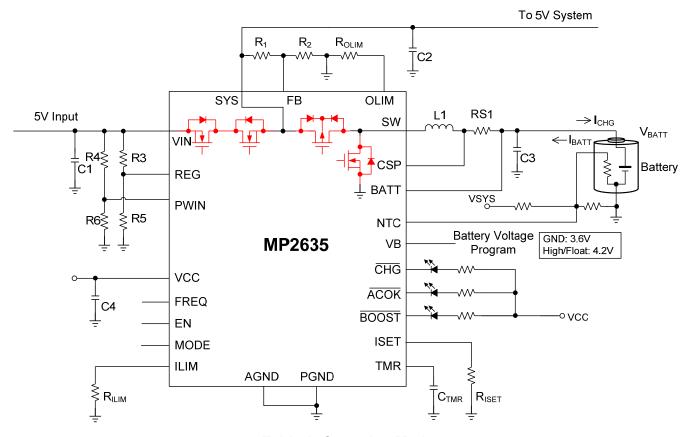
#### **APPLICATIONS**

- **Sub-Battery Applications**
- Power-Bank Applications for Smart-Phone Tablet and Other Portable Devices

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# **TYPICAL APPLICATION**



**Table 1: Operation Mode** 

rabio ii opolation modo								
Power Source	ACOK	EN	MODE	Operating Mode				
0.0\/ <d\\ \\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\<="" td=""><td rowspan="2">Low</td><td>High</td><td>~</td><td>Charge Mode, Enable Charging</td></d\\>	Low	High	~	Charge Mode, Enable Charging				
0.8V <pwin<1.15v &="" v<sub="">IN&gt;V<sub>BATT</sub>+300mV</pwin<1.15v>		Low	Х	Charge Mode, Disable Charging				
PWIN<0.8V or PWIN >1.15V or V <sub>IN</sub> <v<sub>BATT+300mV</v<sub>	High	Х	High	Boost Mode				
V <sub>IN</sub> <2V	High	Х	Low	Sleep Mode				

X=Don't Care.

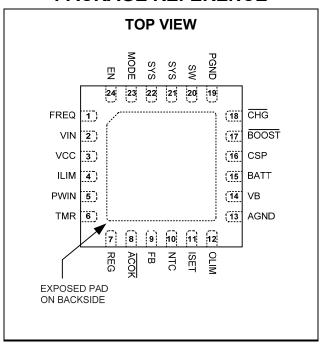


#### ORDERING INFORMATION

Part Number*	Battery Full Voltage	Package	Top Marking
MP2635GR	4.2V (VB=logic high/float) or 3.6V (VB=GND)	QFN-24 (4mm×4mm)	M2635E
MP2635BGR	4.2V (VB=logic high/float) or 4.35V (VB=GND)	QFN-24 (4mm×4mm)	M2635B

<sup>\*</sup> For Tape & Reel, add suffix –Z (e.g. MP2635GR–Z, MP2635BGR-Z);

#### PACKAGE REFERENCE



ABSOLUTE MAXIMUM RATINGS (1)
VIN0.3V to 20V
SYS0.3V to 6.5V
SW
-0.3V (-2V for <20ns) to 6.5V (8.5V for
<20ns)
BATT0.3V to 6.5V
ACOK, CHG, BOOST0.3V to 6.5V
All Other Pins–0.3V to 6.5V
Junction Temperature150°C
Lead Temperature260°C
Continuous Power Dissipation $(T_A = +25^{\circ}C)^{(2)}$
2.97W
Junction Temperature150°C
Operating Temperature –20°C to +85°C

Recommended Operating Conditions (3)						
Supply Voltage VIN						
Battery Voltage V <sub>OUT</sub>	2.5V to 4.35V					
Operating Junction Temp. (T <sub>J</sub> )	40°C to +125°C					

Thermal Resistance <sup>(4)</sup>	$oldsymbol{ heta}_{JA}$	$oldsymbol{ heta}_{JC}$	
QFN-24 (4mm×4mm)	42	9	°C/W

#### Notes:

- 1) Exceeding these ratings may damage the device.
- 2) The maximum allowable power dissipation is a function of the maximum junction temperature  $T_{\rm J}$  (MAX), the junction-to-ambient thermal resistance  $\theta_{\rm JA}$ , and the ambient temperature  $T_{\rm A}$ . The maximum allowable continuous power dissipation at any ambient temperature is calculated by  $P_{\rm D}$  (MAX) = ( $T_{\rm J}$  (MAX)- $T_{\rm A}$ )/ $\theta_{\rm JA}$ . Exceeding the maximum allowable power dissipation will cause excessive die temperature, and the regulator will go into thermal shutdown. Internal thermal shutdown circuitry protects the device from permanent damage.
- The device is not guaranteed to function outside of its operating conditions.
- Measured on JESD51-7, 4-layer PCB.



# **ELECTRICAL CHARACTERISTICS**

 $V_{IN}$  = 5.0V,  $T_A$  = 25°C, unless otherwise noted.

Parameter	Symbol	Condition		Min	Тур	Max	Units
IN to SYS NMOS ON Resistance	R <sub>IN to SYS</sub>				100		mΩ
High-side PMOS ON Resistance	R <sub>H DS</sub>				72		mΩ
Low-side NMOS ON Resistance	R <sub>L DS</sub>				70		mΩ
High-Side PMOS Peak Current Limit	I <sub>PEAK_HS</sub>	Mode	Mode/ Boost		4		А
		TC Charge	Mode		1.5		A
Low-Side NMOS Peak Current Limit	I <sub>PEAK_LS</sub>				4.5		Α
Switching Frequency	f <sub>sw</sub>	FREQ = 0 FREQ = Flo	nat/ High		600 1200		kHz
VCC UVLO	V <sub>CC UVLO</sub>	111CLQ 111	out riigii	2	2.2	2.4	V
VCC UVLO Hysteresis	- CC OVLO				100		mV
PWIN Lower Threshold	V <sub>PWIN L</sub>			0.75	0.8	0.85	V
Lower Threshold Hysteresis					40		mV
PWIN Upper Threshold	V <sub>PWIN H</sub>			1.1	1.15	1.2	V
Upper Threshold Hysteresis					65		mV
Charge Mode	•	•					
Input Quiescent Current	I <sub>IN</sub>	EN = 5V, Battery Float				2.5	mA
mpat Quiocoont Carront		EN = 0				1.5	mA
	I <sub>IN_LIMIT</sub>	R <sub>ILIM</sub> = 90.9k		400	450	500	mA
Input Current Limit		R <sub>ILIM</sub> = 49.9k		720	810	900	
		R <sub>ILIM</sub> = 15k		2400	2700	3000	
Input Over-Current Threshold	I <sub>IN(OCP)</sub>				3		A
Input Over-Current Blanking Time <sup>(5)</sup>	T <sub>INOCBLK</sub>				120		μs
Input Over-Current Recover Time <sup>(5)</sup>	T <sub>INRECVR</sub>				100		ms
			VB=GND	3.582	3.6	3.618	
Torminal Pattony Voltago	\ \ '	MP2635	VB=floating or logic HIGH	4.179	4.2	4.221	
Terminal Battery Voltage	V <sub>BATT_FULL</sub>	MP2635B	VB= floating or logic HIGH	4.179	4.2	4.221	V
			VB=GND	4.328	4.35	4.372	]
Recharge Threshold			VB=GND	3.39	3.44	3.49	
	V <sub>RECH</sub>	MP2635	VB=floating or logic HIGH	3.95	4.01	4.07	
		MP2635B	VB= floating or logic HIGH	3.95	4.01	4.07	V
			VB=GND	4.091	4.153	4.215	



# ELECTRICAL CHARACTERISTICS (continued) $V_{IN} = 5.0V$ , $T_A = 25$ °C, unless otherwise noted.

Parameter	Symbol	Condition		Min	Тур	Max	Units
Recharge Threshold Hysteresis					200		mV
Battery Over Voltage Threshold					103.3%		V <sub>BATT</sub>
Constant Charge (CC) Current	I <sub>CC</sub>		RS1 = $40$ mΩ, R <sub>ISET</sub> = $69.8$ k		1000	1100	mA
Constant Charge (CC) Current			RS1 = $40m\Omega$ , R <sub>ISET</sub> = $34.9k$		2000	2200	
Trickle Charge Current	I <sub>TC</sub>				230		mA
			VB=GND	2.47	2.57	2.67	
	W	MP2635	VB=floating or logic HIGH	2.9	3	3.1	V
Trickle Charge Voltage Threshold	$V_{BATT\_TC}$		VB=GND	3.003	3.107	3.21	v
		MP2635B	VB=floating or logic HIGH	2.9	3	3.1	
Trickle Charge Hysteresis			•		200		mV
		RS1 = $40m\Omega$ , R <sub>ISET</sub> =69.8k		2.5%	10%	17.5%	I <sub>cc</sub>
Termination Charge Current	I <sub>BF</sub>	RS1 = $40m\Omega$ , R <sub>ISET</sub> =34.9k		5%	10%	15%	I <sub>cc</sub>
Input-Voltage-Regulation Reference	$V_{REG}$			1.18	1.2	1.22	V
Boost Mode							•
SYS Voltage Range				4.2		5.6	V
Feedback Voltage				1.18	1.2	1.22	V
Feedback Input Current		V <sub>FB</sub> =1V				200	nA
Boost SYS Over-Voltage Protection Threshold	V <sub>SYS(OVP</sub> )	Threshold over V <sub>SYS</sub> to turn off the converter during boost mode		5.8	6	6.2	V
SYS Over Voltage Protection Threshold Hysteresis		V <sub>SYS</sub> falling f	From V <sub>SYS(OVP)</sub>		125		mV
Boost Quiescent Current		I <sub>SYS</sub> = 0, MO	DE = 5V			1.4	mA
Programmable Boost Output Current Limit Accuracy	I <sub>OLIM</sub>	RS1 = 40mΩ,	, R <sub>OLIM</sub> = 100k	0.896	1.12	1.344	Α
Programmable Boost Output Current <sup>(5)</sup>		RS1 = $50m\Omega$ , $R_{OLIM} = 59k$		1.5			Α
SYS Over-Current Blanking Time <sup>(5)</sup>	T <sub>SYSOCBLK</sub>				120		μs
SYS Over-Current Recover Time <sup>(5)</sup>	T <sub>SYSRECVR</sub>				1		ms
		During boosting			2.5		V
Weak-Battery Threshold	$V_{BATT(LOW)}$	Before Boost starts			2.9	3.05	V
Sleep Mode							
Battery Leakage Current	I <sub>LEAKAGE</sub>	$V_{BATT} = 4.2V$ $V_{IN} = 0V$ , MC	, SYS Float, DDE = 0V		15	30	μA



# ELECTRICAL CHARACTERISTICS (continued) $V_{IN} = 5.0V$ , $T_A = 25$ °C, unless otherwise noted.

Parameter	Symbol	Condition	Min	Тур	Max	Units
Indication and Logic						
ACOK, CHG, BOOST pin output low voltage		Sinking 1.5mA			400	mV
ACOK , CHG , BOOST pin leakage current		Connected to 5V			1	μΑ
NTC and Time-out Fault Blinking Frequency <sup>(5)</sup>		$C_{TMR} = 0.1 \mu F, I_{CHG} = 1A$		13.7		Hz
EN Input Logic Low Voltage					0.4	V
EN Input High Voltage			1.4			V
Mode Input Logic Low Voltage					0.4	V
Mode Input Logic High Voltage			1.4			V
Protection						
Trickle Charge Time		C <sub>TMR</sub> =0.1µF, remains in TC Mode, I <sub>CHG</sub> = 1A		60		Min
Total Charge Time		C <sub>TMR</sub> =0.1µF, I <sub>CHG</sub> = 1A		360		Min
NTC Low Temp, Rising Threshold		D -NCD49VLI402/09C)	65%	66%	67%	
NTC Low Temp, Rising Threshold Hysteresis		R <sub>NTC</sub> =NCP18XH103(0°C)		1%		
NTC High Temp, Rising Threshold		D -NCD49VLI402/E09C)	34%	35%	36%	$V_{SYS}$
NTC High Temp, Rising Threshold Hysteresis		R <sub>NTC</sub> =NCP18XH103(50°C)		1%		
Charging Current Foldback Threshold <sup>(5)</sup>		Charge Mode		120		°C
Thermal Shutdown Threshold <sup>(5)</sup>				150		°C

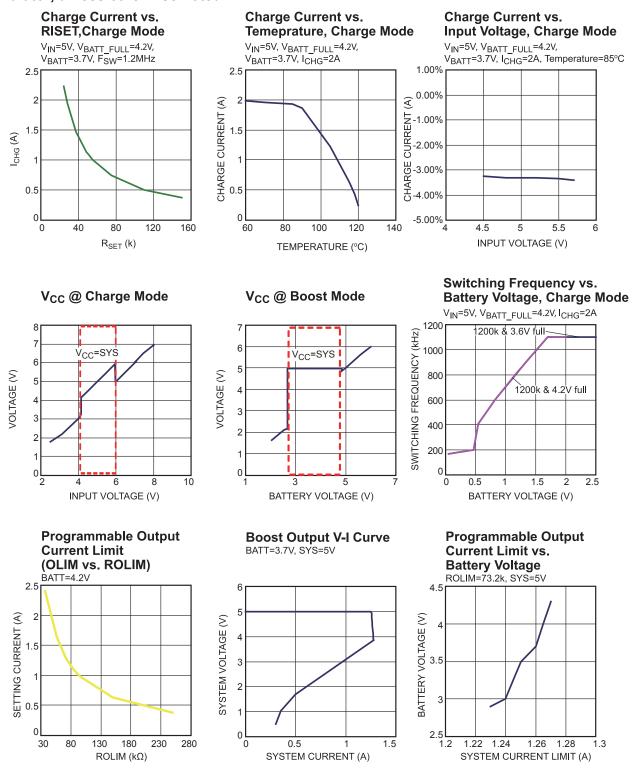
#### Notes:

<sup>5)</sup> Guaranteed by design.



#### TYPICAL CHARACTERISTICS

MP2635,  $C_{IN}$ = $C_{BATT}$ = $C_{SYS}$ =C3=22μF, C1=C2=1μF, L1=4.7μH, RS1=50mΩ, C4= $C_{TMR}$ =0.1μF, Battery Simulator, unless otherwise noted.



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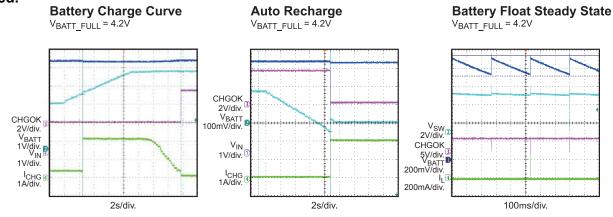


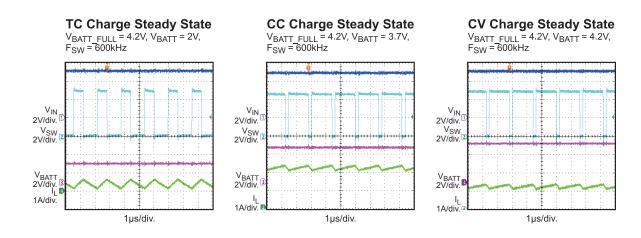
#### TYPICAL PERFORMANCE CHARACTERISTICS

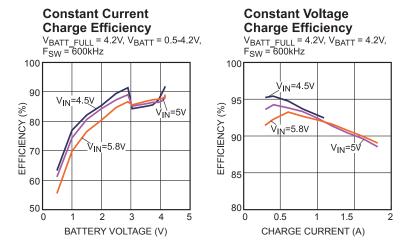
MP2635, For Charge Mode:  $V_{IN}$ =5V,  $I_{CHG}$ =1.5A,  $L_{IN\_LIM}$ =2.7A,  $I_{SYS}$ =0A

For Boost: BATT=3.7V, SYS\_SET=5V, OLIM=1.4A

 $C_{\text{IN}} = C_{\text{BATT}} = C_{\text{SYS}} = C3 = 22 \mu F, \quad C1 = C2 = 1 \mu F, \quad L1 = 4.7 \mu H, \quad RS1 = 50 m \Omega, \quad C4 = C_{\text{TMR}} = 0.1 \mu F, \quad RILIM = 15 k \Omega, \\ RISET = 28 k \Omega, \; ROLIM = 63.4 k \Omega, \; BATT_FULL = 4.2 V, \; F_{\text{S}} = 1.2 \text{MHz}, \; Battery \; Simulator, \; unless \; otherwise noted.$ 







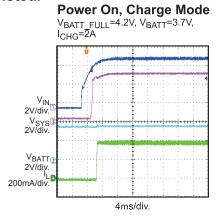


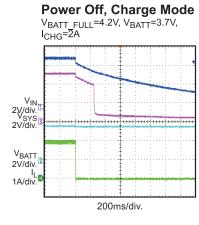
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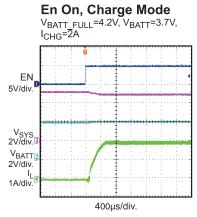
MP2635, For Charge Mode:  $V_{IN}$ =5V,  $I_{CHG}$ =1.5A,  $L_{IN\_LIM}$ =2.7A,  $I_{SYS}$ =0A

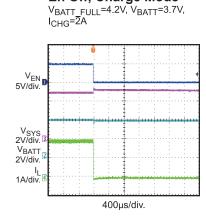
For Boost: BATT=3.7V, SYS\_SET=5V, OLIM=1.4A

 $C_{\text{IN}}$ = $C_{\text{BATT}}$ = $C_{\text{SYS}}$ =C3=22μF, C1=C2=1μF, L1=4.7μH, RS1=50mΩ, C4= $C_{\text{TMR}}$ =0.1μF, RILIM=15kΩ, RISET=28kΩ, ROLIM=63.4kΩ, BATT\_FULL=4.2V, F<sub>S</sub>=1.2MHz, Battery Simulator, unless otherwise noted.

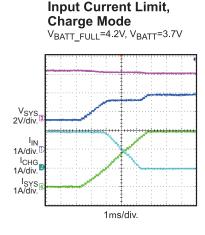


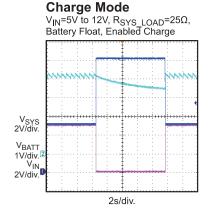




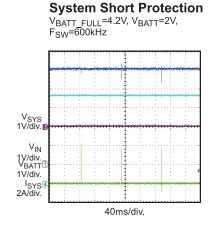


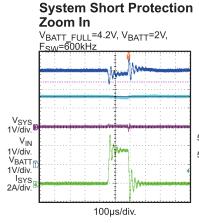
En Off, Charge Mode

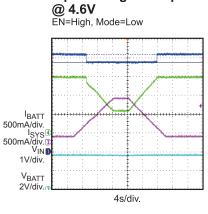




Input Over Voltage Protection,







**Input Voltage Clamp** 

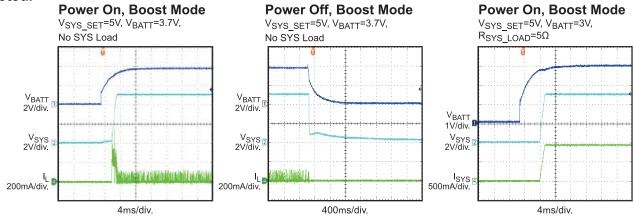


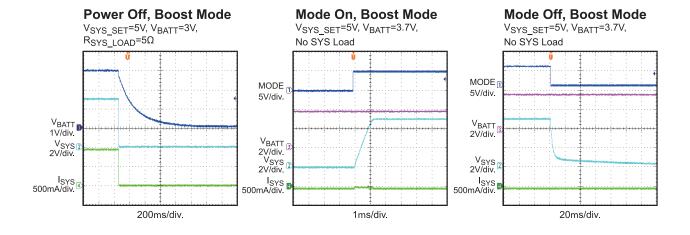
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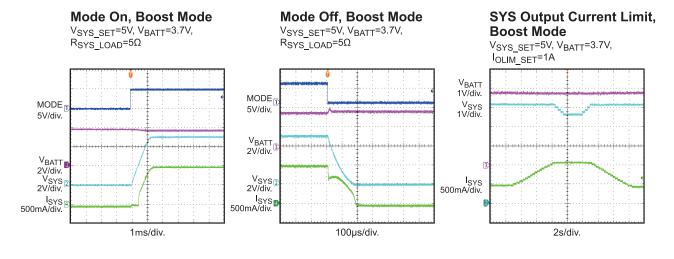
MP2635, For Charge Mode:  $V_{IN}$ =5V,  $I_{CHG}$ =1.5A,  $L_{IN\_LIM}$ =2.7A,  $I_{SYS}$ =0A

For Boost: BATT=3.7V, SYS\_SET=5V, OLIM=1.4A

 $C_{\text{IN}}$ = $C_{\text{BATT}}$ = $C_{\text{SYS}}$ =C3=22μF, C1=C2=1μF, L1=4.7μH, RS1=50mΩ, C4= $C_{\text{TMR}}$ =0.1μF, RILIM=15kΩ, RISET=28kΩ, ROLIM=63.4kΩ, BATT\_FULL=4.2V, F<sub>S</sub>=1.2MHz, Battery Simulator, unless otherwise noted.







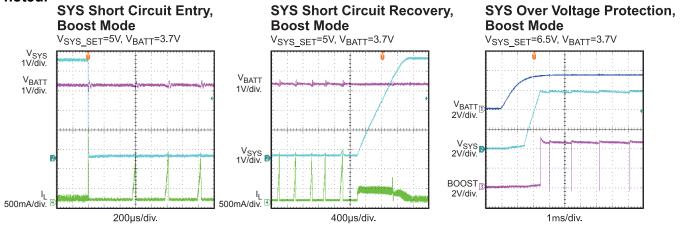


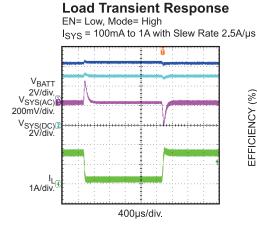
# TYPICAL PERFORMANCE CHARACTERISTICS (continued)

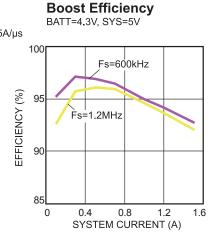
MP2635, For Charge Mode: V<sub>IN</sub>=5V, I<sub>CHG</sub>=1.5A, L<sub>IN\_LIM</sub>=2.7A, I<sub>SYS</sub>=0A

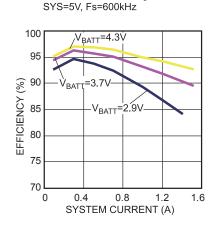
For Boost: BATT=3.7V, SYS SET=5V, OLIM=1.4A

 $C_{\text{IN}}$ = $C_{\text{BATT}}$ = $C_{\text{SYS}}$ =C3=22μF, C1=C2=1μF, L1=4.7μH, RS1=50mΩ, C4= $C_{\text{TMR}}$ =0.1μF, RILIM=15kΩ, RISET=28kΩ, ROLIM=63.4kΩ, BATT\_FULL=4.2V, F<sub>S</sub>=1.2MHz, Battery Simulator, unless otherwise noted.









**Boost Efficiency** 



# **PIN FUNCTIONS**

Pin#	Name	Description								
1	FREQ		GND to program the operating frequency to 600kHz. Leave floating or connect to gram the operating frequency to 1.2MHz.							
2	VIN	Adapter Inpu	Adapter Input. Place a bypass capacitor close to this pin to prevent large input voltage spikes.							
3	VCC		ternal Circuit Power Supply. Bypass to GND with a 100nF ceramic capacitor. This pin can ot carry external load higher than 5mA.							
4	ILIM	Input Currer charge mode	put Current Set. Connect to GND with an external resistor to program input current limit in narge mode.							
5	PWIN	AC Input De	tect. Detect the presence of valid input power.							
6	TMR		eriod Timer. Connect a timing capacitor between this pin and GND to set the riod. Short to GND to disable the Timer function.							
7	REG	divider from	e Feedback for input voltage regulation loop. Connect to tap of an external resistor VIN to GND to program the input voltage regulation. Once the voltage at REG pin inner threshold, the charge current is reduced to maintain the input voltage at the alue.							
8	ACOK	Valid Input S supply.	Supply Indicator. Logic LOW on this pin indicates the presence of a valid power							
9	FB	System volta	age feedback input.							
10	NTC	Negative Te	mperature Coefficient (NTC) Thermistor.							
11	ISET	Charge Curr	rent Set. Connect an external resistor to GND to program the charge current.							
12	OLIM		ole Output-Current Limit for boost mode. Connect an external resistor to GND to system current in boost mode.							
13	AGND	Analog Grou	ınd							
14	VB	MP2635	Programmable Battery-Full Voltage. Connect to GND for 3.6V. Leave floating or connect to logic HIGH for 4.2V							
14	VD	MP2635B	Programmable Battery-Full Voltage. Connect to GND for 4.35V. Leave floating or connect to logic HIGH for 4.2V							
15	BATT	Positive Batt	tery Terminal / Battery Charge Current Sense Negative Input.							
16	CSP	Battery Char	rge Current Sense Positive Input.							
17	BOOST		indicator. Logic LOW indicates boost mode in operation. The pin becomes an when the part operates in charge mode or sleep mode.							
18	CHG		npletion Indicator. Logic LOW indicates charge mode. The pin becomes an open ne charging has completed or is suspended.							
19	PGND, Exposed Pad	Power Grou	Power Ground. Connect the exposed pad and GND pin to the same ground plane.							
20	SW	Switch Output Node.								
21, 22	SYS		System Output. Please make sure the enough bulk capacitors from SYS to GND. Suggest 1.7µF at least. The capacitor should be put as close as possible to SYS and GND.							
23	MODE		t. Logic HIGH→boost mode. Logic LOW→sleep mode. Active only when ACOK is power is not available).							
24	EN	Charge Con	trol Input. Logic HIGH enables charging. Logic LOW disables charging. Active only							
27	,	when ACOK	is low (input power is OK).							



#### **BLOCK DIAGRAM**

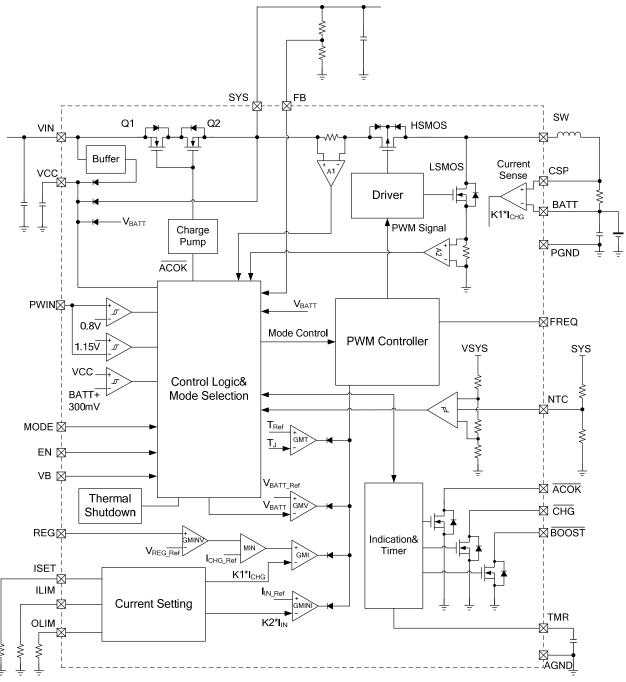


Figure 1: Functional Block Diagram in Charge Mode



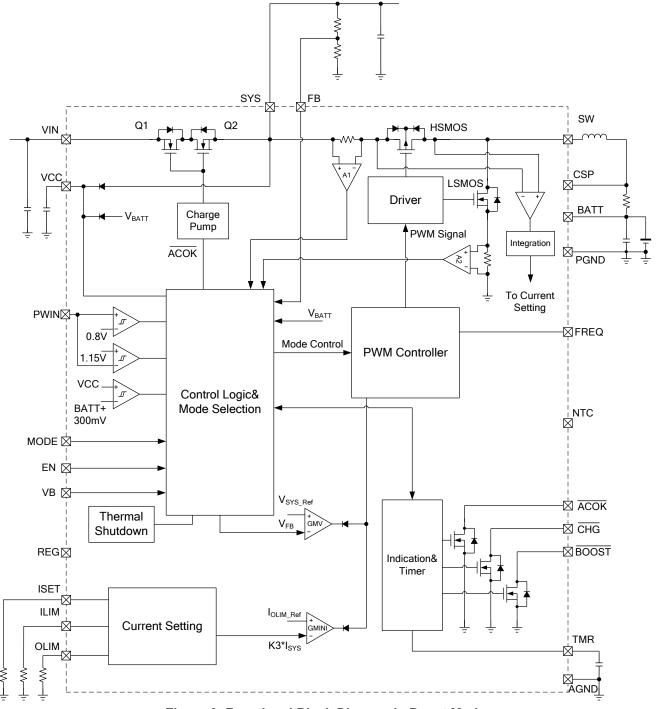
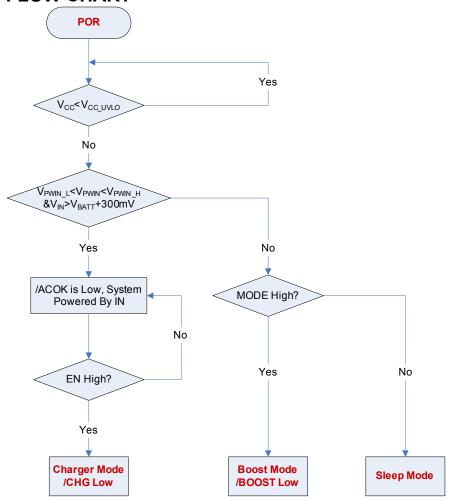


Figure 2: Functional Block Diagram in Boost Mode



# **OPERATION FLOW CHART**



**Figure 3: Mode Selection Flow Chart** 



# **OPERATION FLOW CHART (continued)**

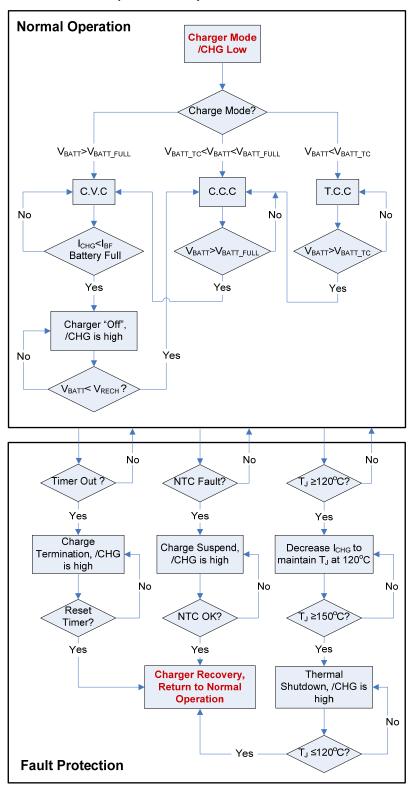


Figure 4: Normal Operation and Fault Protection in Charge Mode

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# **OPERATION FLOW CHART** (continued)

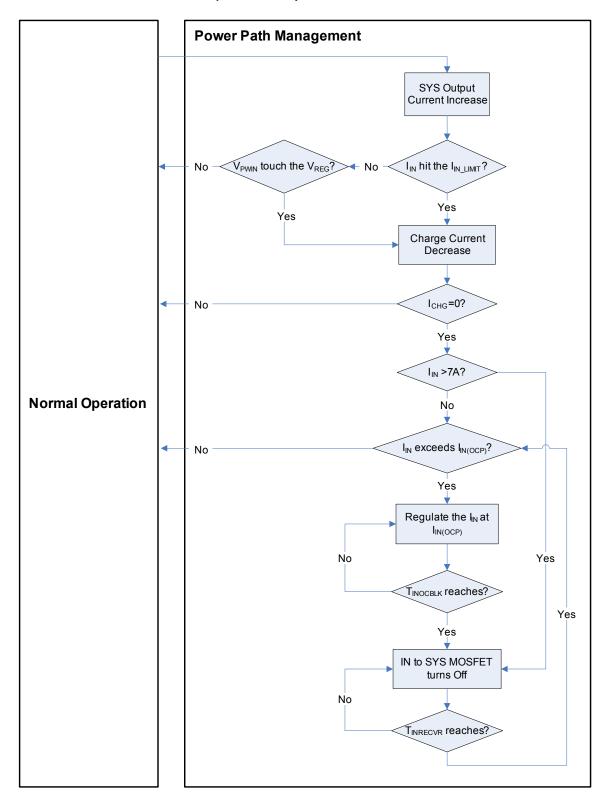


Figure 5: Power-path Management in Charge Mode



# **OPERATION FLOW CHART (continued)**

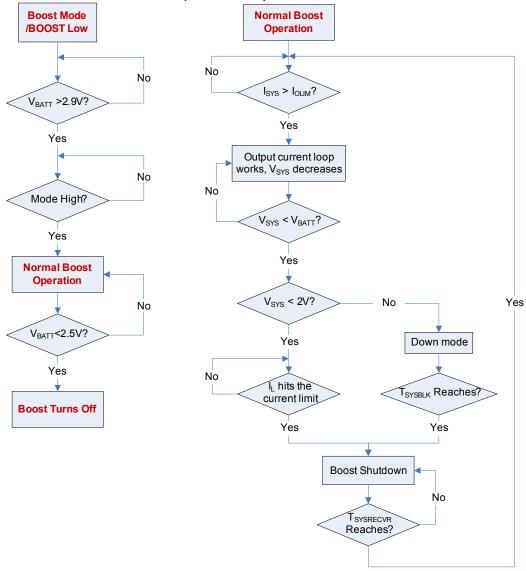


Figure 6: Operation Flow Chart in Boost Mode



#### START UP TIME FLOW IN CHARGE MODE

Condition: EN = 5V, Mode = 0V, /ACOK and /CHG are always pulled up to an external constant 5V

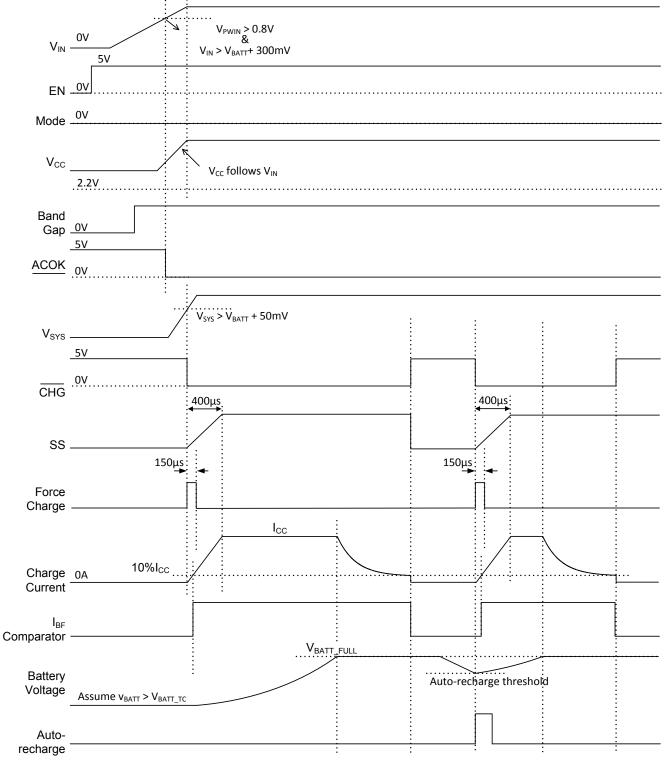


Figure 7: Input Power Start-Up Time Flow in Charge Mode



# START UP TIME FLOW IN CHARGE MODE

Condition:  $V_{IN} = 5V$ , Mode = 0V, /ACOK and /CHG are always pulled up to an external constant 5V.

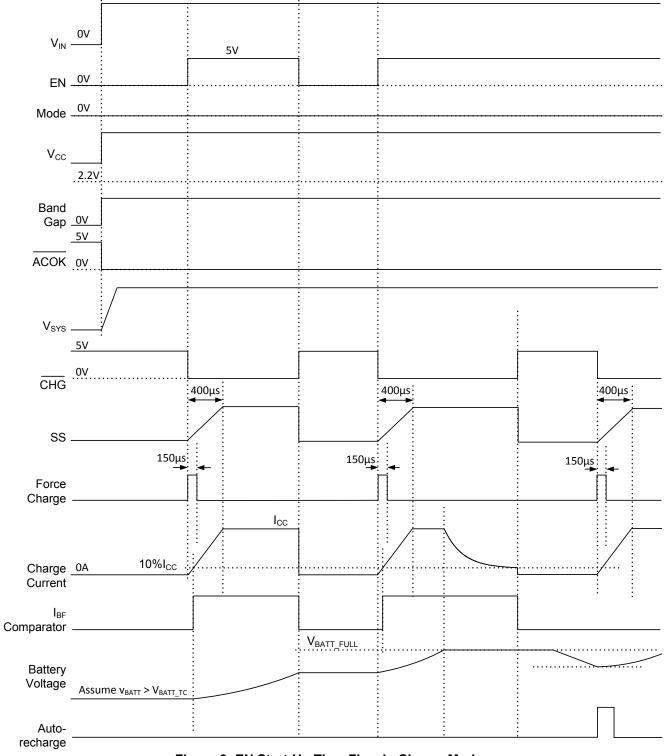


Figure 8: EN Start-Up Time Flow in Charge Mode



# START UP TIME FLOW IN BOOST MODE

Condition:  $V_{IN} = 0V$ , Mode = 5V, /Boost is always pulled up to an external constant 5V.

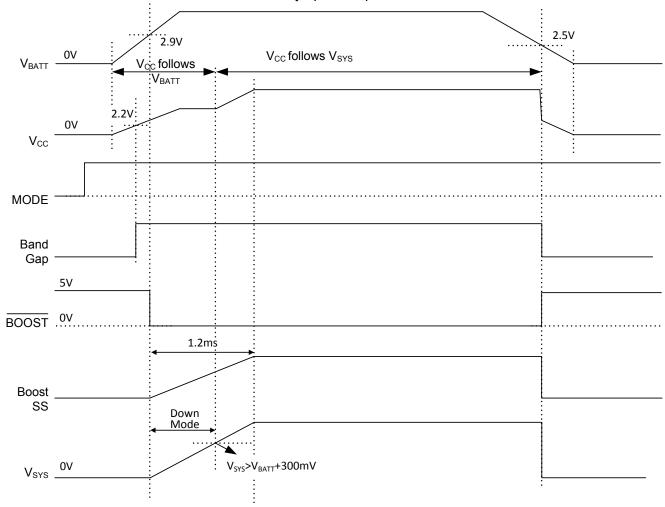


Figure 9: Battery Power Start-Up Time Flow in Boost Mode



# START UP TIME FLOW IN BOOST MODE

Condition:  $V_{IN} = 0V$ , /Boost is always pulled up to an external constant 5V.

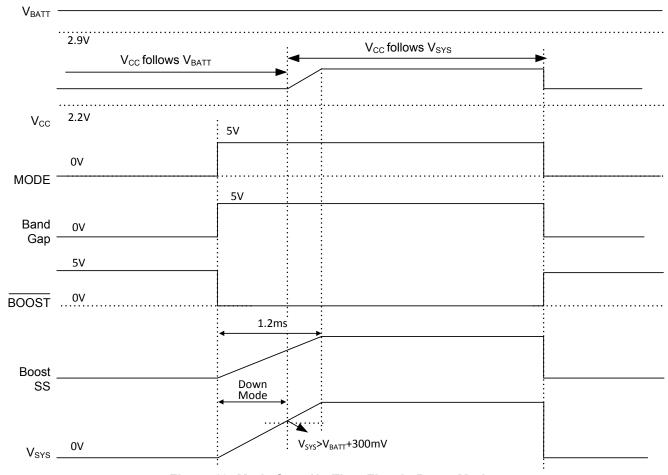


Figure 10: Mode Start-Up Time Flow in Boost Mode



#### **OPERATION**

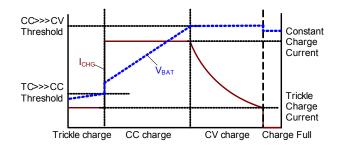
#### INTRODUCTION

The MP2635 is a highly-integrated, synchronous. switching charger with bi-directional operation for a boost function that can step-up the battery voltage to power the system. Depending on the VIN value, it operates in one of three modes: charge mode, boost mode and sleep mode. In charge mode, the MP2635 supports a precision Li-ion or Li-polymer charging system for singlecell applications. In boost mode, MP2635 boosts the battery voltage to V<sub>SYS</sub> to power highervoltage systems. In sleep mode, the MP2635 stops charging or boosting and operates at a low current from the input or the battery to reduce power consumption when the IC isn't operating. The MP2635 monitors VIN to allow smooth transition between different modes of operation.

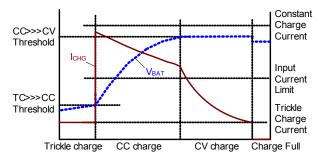
#### **CHARGE MODE OPERATION**

# Charge Cycle (Trickle Charge→CC Charge→CV Charge)

In charge mode, the MP2635 has five control loops to regulate the input current, input voltage, charge current, charge voltage, and device junction temperature. It charges the battery in three phases: trickle current (TC), constant current (CC), and constant voltage (CV). While charging, all four loops are active but only one determines the IC behavior. Figure 11(a) shows a typical battery charge profile. The charger stays in TC charge mode until the battery voltage reaches a TC-to-CC threshold. Otherwise the charger enters CC charge mode. When the battery voltage rises to the CV-mode threshold, the charger operates in constant voltage mode. Figure 11(b) shows a typical charge profile when the input-current-limit loop dominates during the CC charge mode, and in this case the charge current exceeds the input current, resulting in faster charging than a traditional linear solution that is well-suited for USB applications.



a) Without input current limit



b) With input current limit

Figure 11: Typical Battery Charge Profile

### **Auto-Recharge**

Once the battery charge cycle completes, the charger remains off. During this process, the system load may consume battery power, or the battery may self discharge. To ensure the battery will not go into depletion, a new charge cycle automatically begins when the battery voltage falls below the auto-recharge threshold and the input power is present. The timer resets when the auto-recharge cycle begins.

During the off state after the battery is fully charged, if the input power re-starts or the EN signal refreshes, the charge cycle will start and the timer will reset no matter what the battery voltage is.

#### **Battery Over-Voltage Protection**

The MP2635 has battery over-voltage protection. If the battery voltage exceeds the battery over-voltage threshold, (103.3% of the battery-full voltage), charging is disabled. Under this condition, an internal current source draws a current from the BATT pin to decrease the battery voltage and protect the battery.



#### **Timer Operation in Charge Mode**

The MP2635 uses an internal timer to terminate the charging. The timer remains active during the charging process. An external capacitor between TMR and GND programs the charge cycle duration.

If charging remains in TC mode beyond the trickle-charge time,  $\tau_{TRICKLE\_TMR}$ , charging will terminate. The following determines the length of the trickle-charge period:

$$\tau_{\text{TRICKLE}_{\text{TMR}}} = 60 \, \text{min s} \times \frac{C_{\text{TMR}}(\mu F)}{0.1 \mu F} \times \frac{1A}{I_{\text{CHG}}(A)}$$
(1)

The maximum total charge time is:

$$\tau_{\text{TOTAL}_{\text{TMR}}} = 6 \text{Hours} \times \frac{C_{\text{TMR}}(\mu F)}{0.1 \mu F} \times \frac{1 \text{A}}{I_{\text{CHG}}(A)}$$
(2)

# Negative Temperature Coefficient (NTC) Input for Battery Temperature Monitoring

The MP2635 has a built-in NTC resistance window comparator, which allows the MP2635 to monitor the battery temperature via the battery-integrated thermistor. Connect an appropriate resistor from  $V_{\text{SYS}}$  to the NTC pin and connect the thermistor from the NTC pin to GND. The resistor divider determines the NTC voltage depending on the battery temperature. If the NTC voltage falls outside of the NTC window, the MP2635 stops charging. The charger will then restart if the temperature goes back into NTC window range.

#### Input-Current Limiting in Charge Mode

The MP2635 has a dedicated pin that programs the input-current limit. The current at ILIM is a fraction of the input current; the voltage at ILIM indicates the average input current of the switching regulator as determined by the resistor value between ILIM and GND. As the input current approaches the programmed input current limit, charge current is reduced to allow priority to system power.

Use the following equation to determine the input current limit threshold.

$$I_{ILIM} = \frac{40.5(k\Omega)}{R_{ILIM}(k\Omega)}(A)$$
 (3)

#### **Input Over-Current Protection**

The MP2635 features input over-current protection (OCP): when the input current exceeds 3A, Q2 is controlled linearly to regulate the current. If the current still exceeds 3A after a 120µs blanking time, Q2 will turn off. A fast off function turns off Q2 quickly when the input current exceeds 7A to protect both Q1 and Q2.

#### Input Voltage Regulation in Charge Mode

In charge mode, if the input power source is not sufficient to support both the charge current and system load current, the input voltage will decrease. As the input voltage approaches the programmed input voltage regulation value, charge current is reduced to allow priority of system power and maintain the input voltage avoid dropping further.

The input voltage can be regulated by a resistor divider from IN pin to REG pin to AGND according to the following expression:

$$V_{REG} = V_{IN_R} \times \frac{R5}{R3 + R5}(V)$$
 (4)

Where the  $V_{\text{REG}}$  is the internal voltage reference, 1.2V.

#### **Setting the Charge Current**

The external sense resistors, RS1 and  $R_{\text{ISET}}$ , program the battery charge current,  $I_{\text{CHG}}$ . Select  $R_{\text{ISET}}$  based on RS1:

$$I_{CHG}(A) = \frac{70(k\Omega)}{R_{ISFT}(k\Omega)} \times \frac{40(mV)}{RS1(m\Omega)}$$
 (5)

Where the 40mV is the charge current limiting reference.

#### **Battery Short Protection**

The MP2635 has two current limit thresholds. CC and CV modes have a peak current limit threshold of 3.6A, while TC mode has a current limit threshold of 1.5A. Therefore, the current limit threshold decreases to 1.5A when the battery voltage drops below the TC threshold. Moreover, the switching frequency also decreases when the BATT voltage drops to 40% of the charge-full voltage.



#### **Thermal Foldback Function**

The MP2635 implements thermal protection to prevent thermal damage to the IC and the surrounding components. An internal thermal sense and feedback loop automatically decreases the programmed charge current when the die temperature reaches 120°C. This function is called the charge-current-thermal foldback. Not only does this function protect against thermal damage, it can also set the charge current based

on requirements rather than worst-case conditions while ensuring safe operation. Furthermore, the part includes thermal shutdown protection where the ceases charging if the junction temperature rises to 150°C.

#### **Full-Operation Indication**

The MP2635 integrates indicators for the following conditions as shown in Table2.

**Table 2: Indicator for Each Operation Mode** 

Operation	ACOK	CHG	BOOST		
	In Charging		Low	High	
Charge Mode	End of Charge, charging disabled	Low	High		
	NTC Fault, Timer Out		Blinking		
Boost Mode	High	High	Low		
Sleep Mode, VCC	High	High	High		