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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









The Future of Analog IC Technology DESCRIPTION

The MP3801 is a high output voltage, DC to AC designed for converter Electroluminescent (EL) lamps of up to 5 square inches. The device operates from an input voltage range of 2.5V to 5.5V, making it suitable for 1-cell Li-Ion and 2 or alkaline/NiCad/NiMH battery applications. The device uses a single inductor and a minimum number of passive components. The MP3801 converts a low-voltage DC input to a ±95V regulated output voltage that drives the EL lamp.

The MP3801 is comprised of two stages: a boost stage and a H-bridge lamp driver stage. The boost stage converts the input voltage up to 95V. The H-bridge stage alternately switches the 95V to each terminal of the EL lamp.

The MP3801 has two separate internal oscillators for the boost and H-bridge stages. The frequency of each oscillator is set independently via external resistors. This flexibility allows the EL lamp circuit to be optimized for maximum performance. Also, the IC can be enabled /disabled by connecting these two resistors to $V_{\rm DD}/{\rm GND}$.

The MP3801 is available in an 8-pin MSOP package.

FEATURES

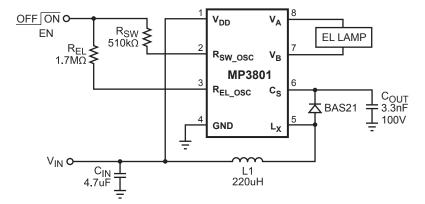
- 2.5V to 5.5V DC Input Voltage
- 190V_{PP} Regulated AC Output Waveform
- Single Cell Lithium-Ion Compatible
- 10nA Shutdown Current
- Adjustable EL Lamp Frequency
- Adjustable Converter Frequency
- Available in MSOP-8
- Split Supply Capability

APPLICATIONS

- Portable Multimedia Players
- LCD Backlighting
- PDAs
- Handheld Wireless Communication
- Mobile Phones
- Remote Controls
- Global Positioning Systems (GPS)

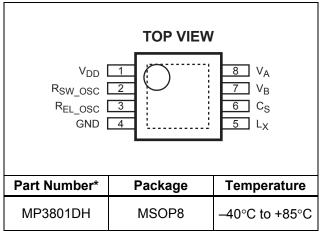
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TYPICAL APPLICATION





PACKAGE REFERENCE



For Tape & Reel, add suffix –Z (eg. MP3801DH–Z)
For RoHS Compliant Packaging, add suffix –LF
(eg. MP3801DH–LF–Z)

ABSOLUTE MAXIMUM RATINGS (1)

Supply Voltage (V _{DD}).	0.5V to +6.0V
Output Voltage (V _{CS} , V	V_A , V_B)–0.5V to +120V
All Other Pins	$-0.5V$ to $V_{DD} + 0.3V$
Junction Temperature	150°C
Lead Temperature	260°C
Storage Temperature	65°C to +150°C

Recommended Operating Conditions (2)

Thermal Resistance (3) $\theta JA \theta JC$

MSOP8 150 65... °C/W

Notes:

- 1) Exceeding these ratings may damage the device.
- The device is not guaranteed to function outside of its operating conditions.
- 3) Measured on approximately 1" square of 1 oz copper.

ELECTRICAL CHARACTERISTICS

 $V_{IN} = V_{DD} = 3.0V$, $R_{EL} = 1.7M\Omega$, $R_{SW} = 510K\Omega$, $T_A = +25^{\circ}C$, unless otherwise noted.

Parameter	Symbol	Condition	Min	Тур	Max	Units
On Resistance of Switching Transistor	R _{DS(ON)}	I _{SW} = 100mA, V _{CS} = 95V		9		Ω
Output Voltage Regulation	V _{CS}		90	95	100	V
Peak to Peak Output Voltage	$V_A - V_B$		180	190	200	V
Input Low Voltage (Turn Off)	V _{EN-L}			$V_{DD} - 0.6$		V
Input High Voltage (Turn On)	V _{EN-H}			$V_{DD} - 0.3$		V
Shutdown Current	I _{SD}	V _{EN} = 0V		10		nA
Input Supply Current	I _{VDD}	V_A , V_B Open, $V_{EN} = V_{IN}$		92		Α
Input Current Including Inductor Current	I _{IN}	A $2k\Omega$ resistor is series with a 10nF capacitor connected between V_A and V_B		20		mA
EL Lamp Frequency	f _{EL}		200	240	274	Hz
Switching Transistor Frequency	f _{SW}			85		KHz
Switching Transistor Duty Cycle	D			92		%

Note:

4) Shutdown current is defined as the sum of currents going into $V_{\text{DD}},\,V_{\text{CS}},$ and SW nodes.

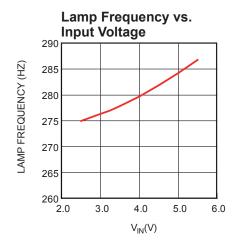


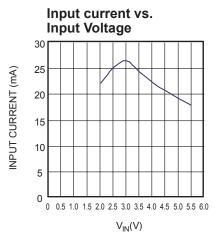
PIN FUNCTIONS

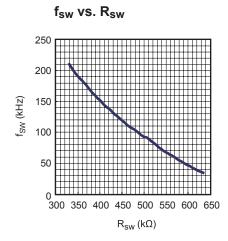
Pin#	Name	Description
1	V_{DD}	Supply Voltage.
2	R _{SW_OSC}	Boost Converter Frequency Setting Pin. Refer the frequency setting curve in the typical performance characteristics.
3	R _{EL_OSC}	EL Driver Frequency Setting Pin. Roughly, $f_{\text{EL}} = \frac{1.7 M\Omega \times 240 Hz}{R_{\text{EL}}}$
4	GND	Ground. Connect the exposed pad to this pin.
5	L _X	Boost Converter Switching Pin. This pin connects to the N-Channel MOSFET drain.
6	Cs	Boost Converter Output. Put a 100V, NPO ceramic capacitor at this pin to stone the energy transferred from the inductor.
7	V_B	Output Voltage B on EL Device.
8	V_A	Output Voltage A on EL Device.



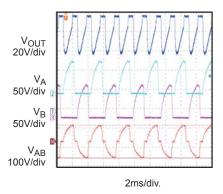
TYPICAL PERFORMANCE CHARACTERISTICS



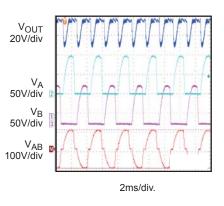




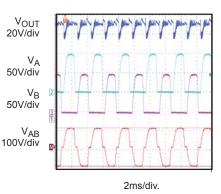
2.5V Operation: 25°C



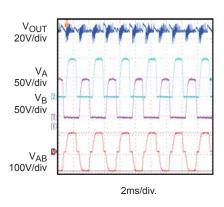
3.5V Input Operation: 25°C



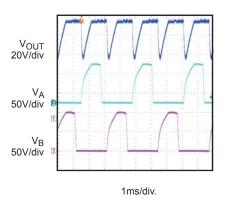
4.5 Input Operation: 25°C



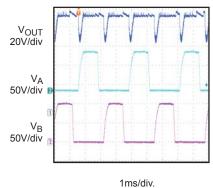
5.5 Input Operation: 25°C



2.5V Input Operation: -40°C

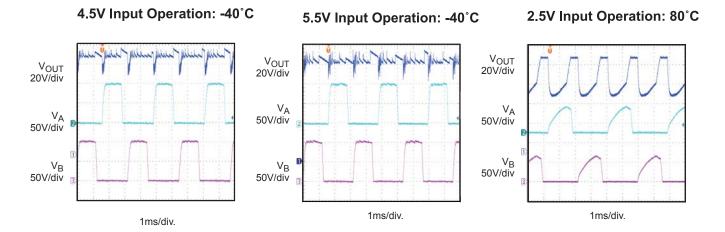


3.5V Input Operation: -40°C





TYPICAL PERFORMANCE CHARACTERISTICS (continued)



4.5V Input Operation: 80°C 5.5V Input Operation: 80°C 3.5V Input Operation: 80°C V_{OUT} 20V/div V_{OUT} 20V/div V_{OUT} 20V/div V_A 50V/div V_A 50V/div V_A 50V/div V_B V_{B} V_{B} 50V/div 50V/div 50V/div 1ms/div. 1ms/div. 1ms/div.



BLOCK DIAGRAM

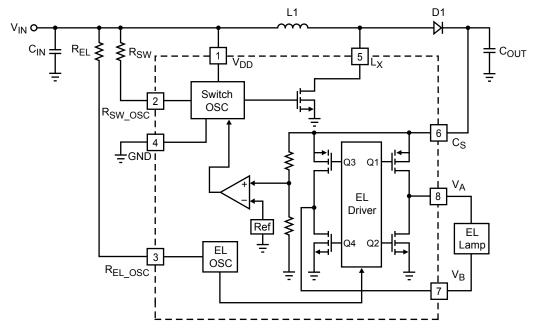
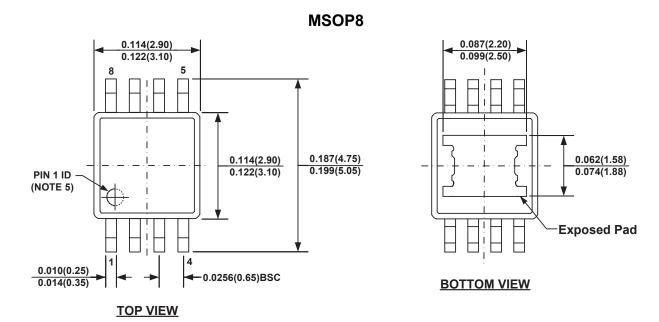
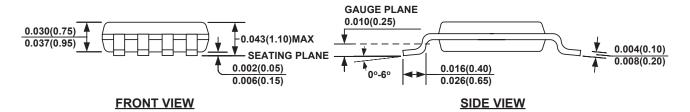


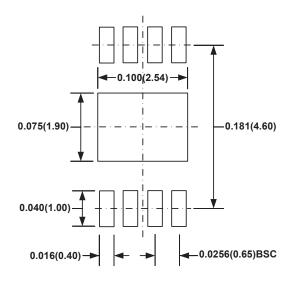
Figure 1—Functional Block Diagram



PACKAGE INFORMATION







NOTE:

- 1) CONTROL DIMENSION IS IN INCHES. DIMENSION IN BRACKET IS IN MILLIMETERS.
- PACKAGE LENGTH DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR.
- 3) PACKAGE WIDTH DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSION.
- 4) LEAD COPLANARITY (BOTTOM OF LEADS AFTER FORMING) SHALL BE 0.004" INCHES MAX.
- 5) PIN 1 IDENTIFICATION HAS HALF OR FULL CIRCLE OPTION.
- 6) DRAWING MEETS JEDEC MO-187, VARIATION AA-T.
- 7) DRAWING IS NOT TO SCALE.

RECOMMENDED LAND PATTERN

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