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MPS6717

One Watt Amplifier Transistor

NPN Silicon

Features

Pb–Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V _{CEO}	80	Vdc
Collector-Base Voltage	V _{CBO}	80	Vdc
Emitter – Base Voltage	V_{EBO}	5.0	Vdc
Collector Current – Continuous	Ι _C	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	PD	1.0 8.0	W mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	2.5 20	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

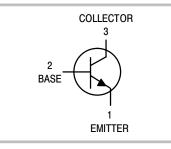
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	125	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	50	°C/W

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



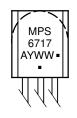
ON Semiconductor®

http://onsemi.com





MARKING DIAGRAM



MPS6717 = Device Code А

- = Assembly Location
- = Year

Y

WW

- = Work Week
- = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MPS6717	TO-92	5000 Units / Bulk
MPS6717G	TO-92 (Pb-Free)	5000 Units / Bulk
MPS6717RLRA	TO-92	2000/Tape & Reel
MPS6717RLRAG	TO-92 (Pb-Free)	2000/Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

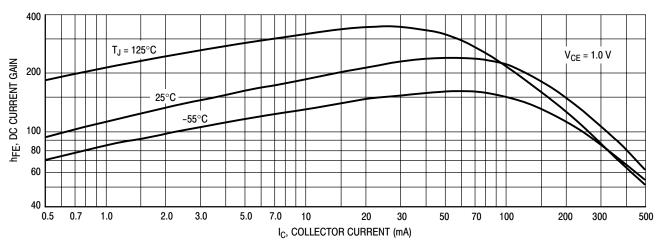
*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MPS6717

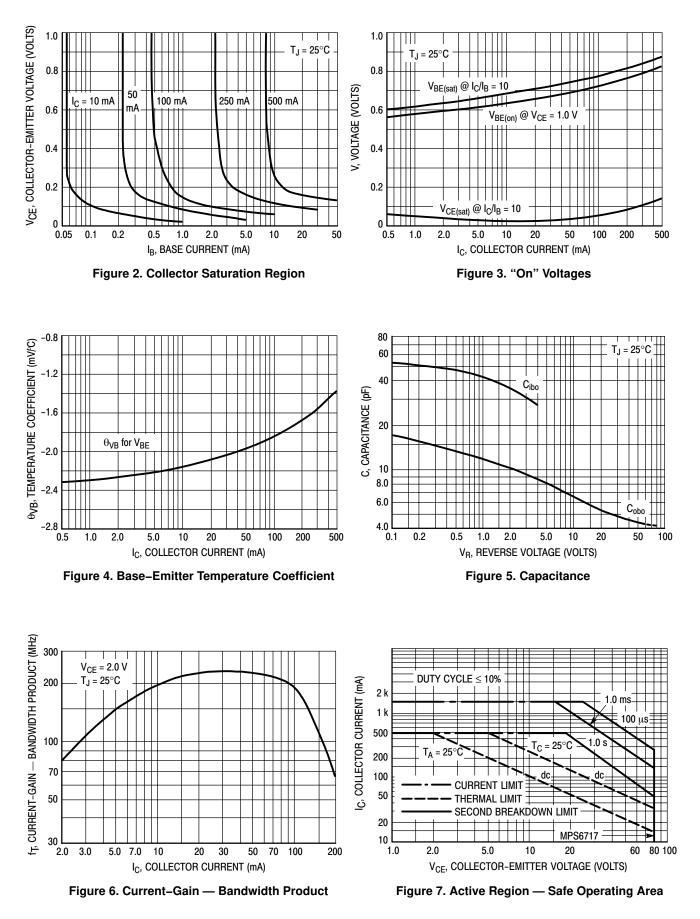
ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS	÷			
Collector – Emitter Breakdown Voltage (Note 1) $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V _(BR) CEO	80	-	Vdc
Collector – Base Breakdown Voltage ($I_C = 100 \ \mu Adc, I_E = 0$)	V _{(BR)CBO}	80	-	Vdc
Emitter – Base Breakdown Voltage ($I_E = 10 \ \mu Adc, I_C = 0$)	V _{(BR)EBO}	5.0	-	Vdc
Collector Cutoff Current ($V_{CB} = 60$ Vdc, $I_E = 0$)	I _{CBO}	-	0.1	μAdc
Emitter Cutoff Current $(V_{EB} = 5.0 \text{ Vdc}, I_C = 0)$	I _{EBO}	-	10	μAdc
ON CHARACTERISTICS		•		
DC Current Gain (I _C = 50 mAdc, V _{CE} = 1.0 Vdc) (I _C = 250 mAdc, V _{CE} = 1.0 Vdc)	h _{FE}	80 50	250	-
Collector – Emitter Saturation Voltage $(I_C = 250 \text{ mAdc}, I_B = 10 \text{ mAdc})$	V _{CE(sat)}	-	0.5	Vdc
Base – Emitter On Voltage $(I_C = 250 \text{ mAdc}, V_{CE} = 1.0 \text{ Vdc})$	V _{BE(on)}	-	1.2	Vdc
SMALL-SIGNAL CHARACTERISTICS	ł			
Collector–Base Capacitance $(V_{CB} = 10 \text{ Vdc}, I_E = 0, f = 1.0 \text{ MHz})$	C _{cb}	-	30	pF
Small–Signal Current Gain (I _C = 200 mAdc, V _{CE} = 5.0 Vdc, f = 20 MHz)	h _{fe}	2.5	25	_

1. Pulse Test: Pulse Width \leq 300 µs; Duty Cycle \leq 2.0%.



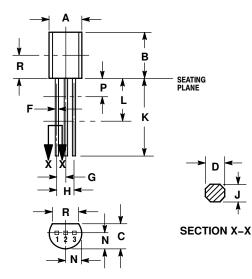




MPS6717

PACKAGE DIMENSIONS

TO-92 (TO-226) CASE 29-10 ISSUE AL



NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- 2. CONTROLLING DIMENSION: INCH.
- 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
- L. DIMENSION F APPLIES BETWEEN P AND L. DIMENSIONS D AND J APPLY BETWEEN L AND K MIMIMUM. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.44	5.21
В	0.290	0.310	7.37	7.87
С	0.125	0.165	3.18	4.19
D	0.018	0.021	0.457	0.533
F	0.016	0.019	0.407	0.482
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
ſ	0.018	0.024	0.46	0.61
Κ	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.135		3.43	

STYLE 1: PIN 1. EMITTER

2. BASE

3. COLLECTOR

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