

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







MPSA14 is a Preferred Device

Darlington Transistors

NPN Silicon

Features

• Pb-Free Packages are Available*

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector - Emitter Voltage	V _{CES}	30	Vdc
Collector - Base Voltage	V _{CBO}	30	Vdc
Emitter – Base Voltage	V _{EBO}	10	Vdc
Collector Current - Continuous	I _C	500	mAdc
Total Device Dissipation @ T _A = 25°C Derate above 25°C	P _D	625 5.0	mW mW/°C
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	1.5 12	W mW/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

THERMAL CHARACTERISTICS

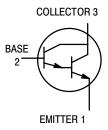
Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	200	°C/mW
Thermal Resistance, Junction-to-Case	$R_{ heta JC}$	83.3	°C/mW

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.



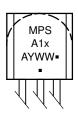
ON Semiconductor®

http://onsemi.com



MARKING DIAGRAM





x = 3 or 4

A = Assembly Location

Y = Year WW = Work Week = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

^{*}For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ELECTRICAL CHARACTERISTICS ($T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic		Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage ($I_C = 100 \mu Adc$, $I_B = 0$)		V _{(BR)CES}	30	-	Vdc
Collector Cutoff Current (V _{CB} = 30 Vdc, I _E = 0)		Ісво	-	100	nAdc
Emitter Cutoff Current (V _{EB} = 10 Vdc, I _C = 0)		I _{EBO}	-	100	nAdc
ON CHARACTERISTICS (Note 1)					
DC Current Gain $(I_C = 10 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$ $(I_C = 100 \text{ mAdc}, V_{CE} = 5.0 \text{ Vdc})$	MPSA13 MPSA14 MPSA13	h _{FE}	5,000 10,000 10,000	- - -	-
Collector – Emitter Saturation Voltage (I _C = 100 mAdc, I _B = 0.1 mAdc)	MPSA14	V _{CE(sat)}	20,000	1.5	Vdc
Base – Emitter On Voltage (I _C = 100 mAdc, V _{CE} = 5.0 Vdc)		V _{BE(on)}	-	2.0	Vdc
SMALL-SIGNAL CHARACTERISTICS					
Current–Gain – Bandwidth Product (Note 2) (I _C = 10 mAdc, V _{CE} = 5.0 Vdc, f = 100 MHz)		f _T	125	-	MHz

^{1.} Pulse Test: Pulse Width \leq 300 µs; Duty Cycle \leq 2.0%.

ORDERING INFORMATION

Device	Package	Shipping [†]
MPSA13	TO-92	5000 Units / Box
MPSA13G	TO-92 (Pb-Free)	5000 Units / Box
MPSA13RLRA	TO-92	2000 / Tape & Reel
MPSA13RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA13RLRM	TO-92	2000 / Ammo Pack
MPSA13RLRMG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA13RLRP	TO-92	2000 / Ammo Pack
MPSA13RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA13ZL1	TO-92	2000 / Ammo Pack
MPSA13ZL1G	TO-92 (Pb-Free)	2000 / Ammo Pack
MPSA14	TO-92	5000 Units / Box
MPSA14G	TO-92 (Pb-Free)	5000 Units / Box
MPSA14RLRA	TO-92	2000 / Tape & Reel
MPSA14RLRAG	TO-92 (Pb-Free)	2000 / Tape & Reel
MPSA14RLRP	TO-92	2000 / Ammo Pack
MPSA14RLRPG	TO-92 (Pb-Free)	2000 / Ammo Pack

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

^{2.} $f_T = |h_{fe}| \cdot f_{test}$.

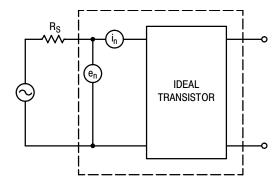


Figure 1. Transistor Noise Model

NOISE CHARACTERISTICS

 $(V_{CE}=5.0~Vdc,~T_{A}=25^{\circ}C)$

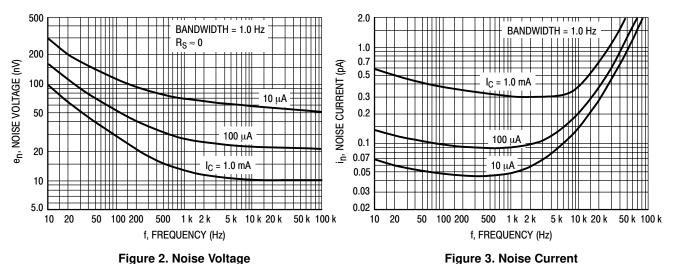


Figure 2. Noise Voltage

BANDWIDTH = 10 Hz TO 15.7 kHz

100 μΑ

1.0 mA

200

70

50

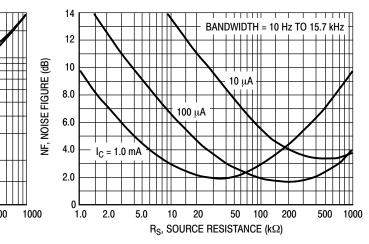
30

20

10

1.0 2.0

V_T, TOTAL WIDEBAND NOISE VOLTAGE (nV)



 R_S , SOURCE RESISTANCE ($k\Omega$) Figure 4. Total Wideband Noise Voltage

50

Figure 5. Wideband Noise Figure

SMALL-SIGNAL CHARACTERISTICS

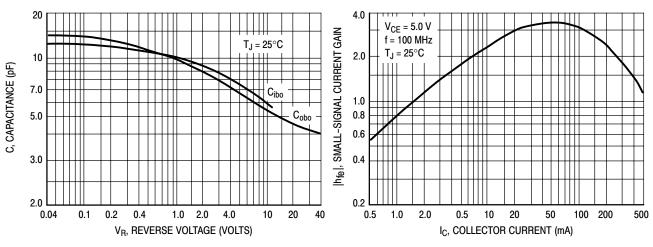


Figure 6. Capacitance

Figure 7. High Frequency Current Gain

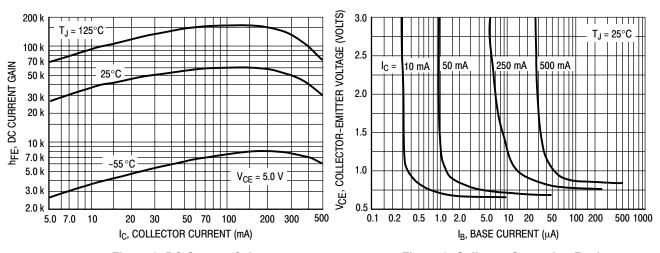


Figure 8. DC Current Gain

Figure 9. Collector Saturation Region

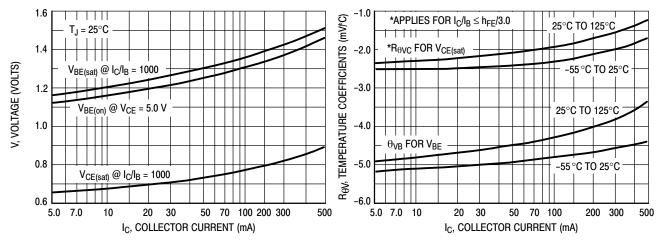


Figure 10. "On" Voltages

Figure 11. Temperature Coefficients

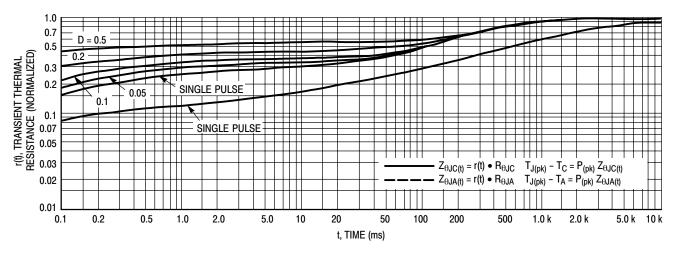
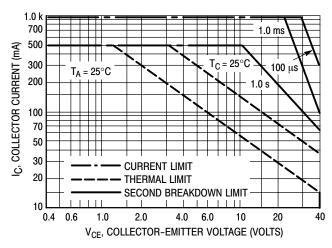


Figure 12. Thermal Response



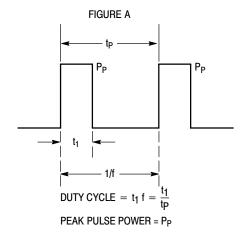
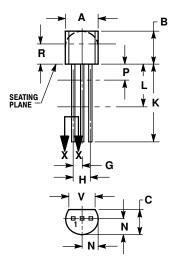


Figure 13. Active Region Safe Operating Area

Design Note: Use of Transient Thermal Resistance Data

PACKAGE DIMENSIONS

TO-92 **TO-226AA** CASE 29-11 **ISSUE AL**





NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- Y14.5M, 1982. CONTROLLING DIMENSION: INCH.
- CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES MILLI		IETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Н	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500		12.70	
L	0.250		6.35	
N	0.080	0.105	2.04	2.66
Р		0.100		2.54
R	0.115		2.93	
V	0 135		3 43	

STYLE 1: PIN 1. EMITTER

BASE

COLLECTOR

ON Semiconductor and un are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 61312, Phoenix, Arizona 85082-1312 USA **Phone**: 480–829–7710 or 800–344–3860 Toll Free USA/Canada **Fax**: 480–829–7709 or 800–344–3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free

Japan: ON Semiconductor, Japan Customer Focus Center 2-9-1 Kamimeguro, Meguro-ku, Tokyo, Japan 153-0051 Phone: 81-3-5773-3850

ON Semiconductor Website: http://onsemi.com

Order Literature: http://www.onsemi.com/litorder

For additional information, please contact your local Sales Representative.