



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



MPSA18

Preferred Device

Low Noise Transistor

NPN Silicon

Features

- These are Pb-Free Devices*

MAXIMUM RATINGS

| Rating | Symbol | Value | Unit |
|--|----------------|-------------|----------------------------|
| Collector–Emitter Voltage | V_{CEO} | 45 | Vdc |
| Collector–Base Voltage | V_{CBO} | 45 | Vdc |
| Emitter–Base Voltage | V_{EBO} | 6.5 | Vdc |
| Collector Current – Continuous | I_C | 200 | mAdc |
| Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C | P_D | 625 5.0 | mW mW/ $^\circ\text{C}$ |
| Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C | P_D | 1.5 12 | W mW/ $^\circ\text{C}$ |
| Operating and Storage Junction Temperature Range | T_J, T_{stg} | -55 to +150 | $^\circ\text{C}$ |

THERMAL CHARACTERISTICS

| Characteristic | Symbol | Max | Unit |
|---|-----------------|------|---------------------------|
| Thermal Resistance, Junction-to-Ambient (Note 1) | $R_{\theta JA}$ | 200 | $^\circ\text{C}/\text{W}$ |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 83.3 | $^\circ\text{C}/\text{W}$ |

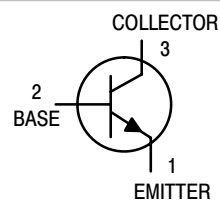
Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

1. $R_{\theta JA}$ is measured with the device soldered into a typical printed circuit board.

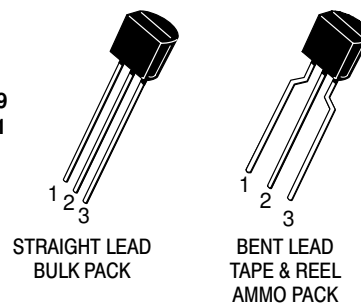


ON Semiconductor®

<http://onsemi.com>



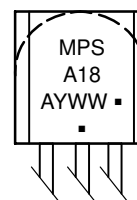
TO-92
CASE 29
STYLE 1



STRAIGHT LEAD
BULK PACK

BENT LEAD
TAPE & REEL
AMMO PACK

MARKING DIAGRAM



A = Assembly Location
Y = Year
WW = Work Week
▪ = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

| Device | Package | Shipping† |
|-------------|--------------------|-------------------|
| MPSA18G | TO-92 (Pb-Free) | 5000 Units / Bulk |
| MPSA18RLRAG | TO-92 (Pb-Free) | 2000/Tape & Reel |
| MPSA18RLRMG | TO-92 (Pb-Free) | 2000/Ammo Pack |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Preferred devices are recommended choices for future use and best overall value.

*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MPSA18

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

| Characteristic | Symbol | Min | Typ | Max | Unit |
|--|---------------|-----|-----|-----|------|
| OFF CHARACTERISTICS | | | | | |
| Collector–Emitter Breakdown Voltage (Note 2) ($I_C = 10\text{ mA}$, $I_B = 0$) | $V_{(BR)CEO}$ | 45 | – | – | Vdc |
| Collector–Base Breakdown Voltage ($I_C = 100\text{ }\mu\text{A}$, $I_E = 0$) | $V_{(BR)CBO}$ | 45 | – | – | Vdc |
| Emitter–Base Breakdown Voltage ($I_E = 10\text{ }\mu\text{A}$, $I_C = 0$) | $V_{(BR)EBO}$ | 6.5 | – | – | Vdc |
| Collector Cutoff Current ($V_{CB} = 30\text{ Vdc}$, $I_E = 0$) | I_{CBO} | – | 1.0 | 50 | nAdc |

ON CHARACTERISTICS (Note 2)

| | | | | | |
|---|---------------|--------------------------|----------------------------|---------------------|-----|
| DC Current Gain ($I_C = 10\text{ }\mu\text{A}$, $V_{CE} = 5.0\text{ Vdc}$) ($I_C = 100\text{ }\mu\text{A}$, $V_{CE} = 5.0\text{ Vdc}$) ($I_C = 1.0\text{ mA}$, $V_{CE} = 5.0\text{ Vdc}$) ($I_C = 10\text{ mA}$, $V_{CE} = 5.0\text{ Vdc}$) | h_{FE} | 400 500 500 500 | 580 850 1100 1150 | – – – 1500 | – |
| Collector–Emitter Saturation Voltage ($I_C = 10\text{ mA}$, $I_B = 0.5\text{ mA}$) ($I_C = 50\text{ mA}$, $I_B = 5.0\text{ mA}$) | $V_{CE(sat)}$ | – – | – 0.08 | 0.2 0.3 | Vdc |
| Base–Emitter On Voltage ($I_C = 1.0\text{ mA}$, $V_{CE} = 5.0\text{ Vdc}$) | $V_{BE(on)}$ | – | 0.6 | 0.7 | Vdc |

SMALL–SIGNAL CHARACTERISTICS

| | | | | | |
|--|----------|--------|------------|----------|------------------------------|
| Current–Gain – Bandwidth Product ($I_C = 1.0\text{ mA}$, $V_{CE} = 5.0\text{ Vdc}$, $f = 100\text{ MHz}$) | f_T | 100 | 160 | – | MHz |
| Collector–Base Capacitance ($V_{CB} = 5.0\text{ Vdc}$, $I_E = 0$, $f = 1.0\text{ MHz}$) | C_{cb} | – | 1.7 | 3.0 | pF |
| Emitter–Base Capacitance ($V_{EB} = 0.5\text{ Vdc}$, $I_C = 0$, $f = 1.0\text{ MHz}$) | C_{eb} | – | 5.6 | 6.5 | pF |
| Noise Figure ($I_C = 100\text{ }\mu\text{A}$, $V_{CE} = 5.0\text{ Vdc}$, $R_S = 10\text{ k}\Omega$, $f = 1.0\text{ kHz}$) ($I_C = 100\text{ }\mu\text{A}$, $V_{CE} = 5.0\text{ Vdc}$, $R_S = 1.0\text{ k}\Omega$, $f = 100\text{ Hz}$) | NF | – – | 0.5 4.0 | 1.5 – | dB |
| Equivalent Short Circuit Noise Voltage ($I_C = 100\text{ }\mu\text{A}$, $V_{CE} = 5.0\text{ Vdc}$, $R_S = 1.0\text{ k}\Omega$, $f = 100\text{ Hz}$) | V_T | – | 6.5 | – | $\text{nV}/\sqrt{\text{Hz}}$ |

2. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

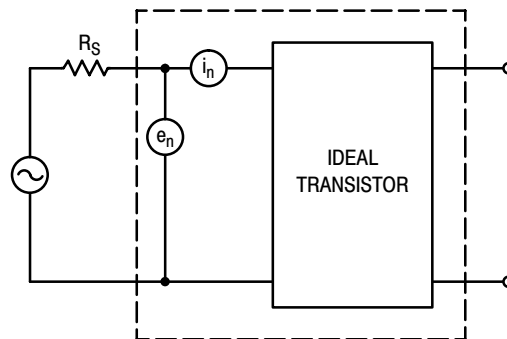


Figure 1. Transistor Noise Model

MPSA18

NOISE CHARACTERISTICS

($V_{CE} = 5.0 \text{ Vdc}$, $T_A = 25^\circ\text{C}$)

NOISE VOLTAGE

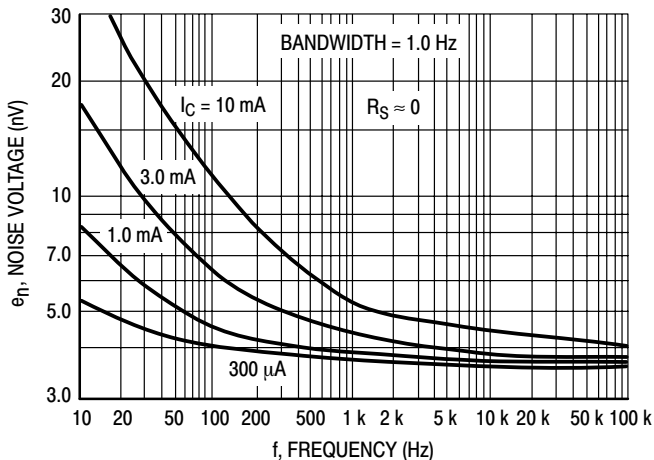


Figure 2. Effects of Frequency

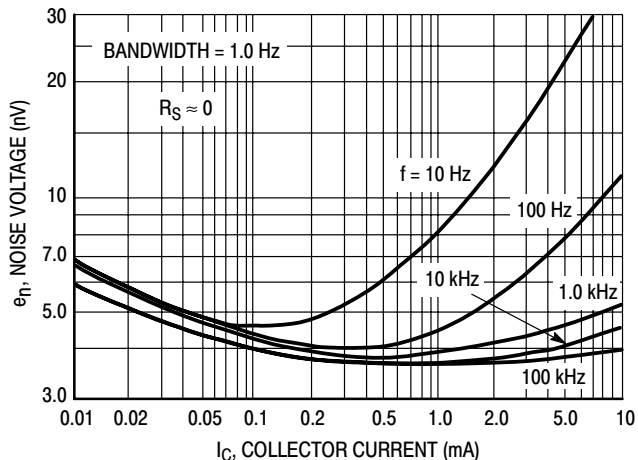


Figure 3. Effects of Collector Current

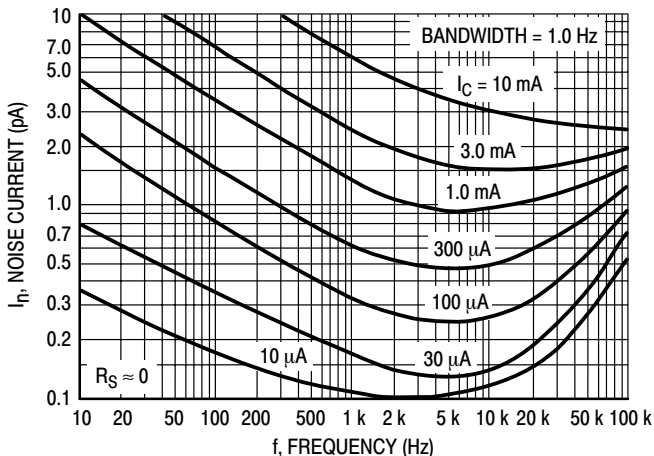


Figure 4. Noise Current

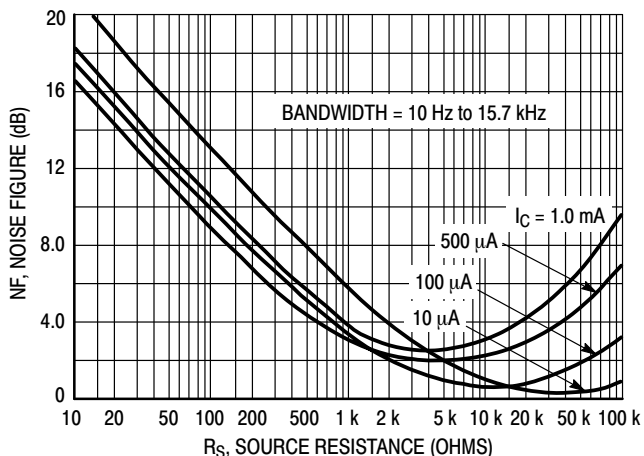


Figure 5. Wideband Noise Figure

100 Hz NOISE DATA

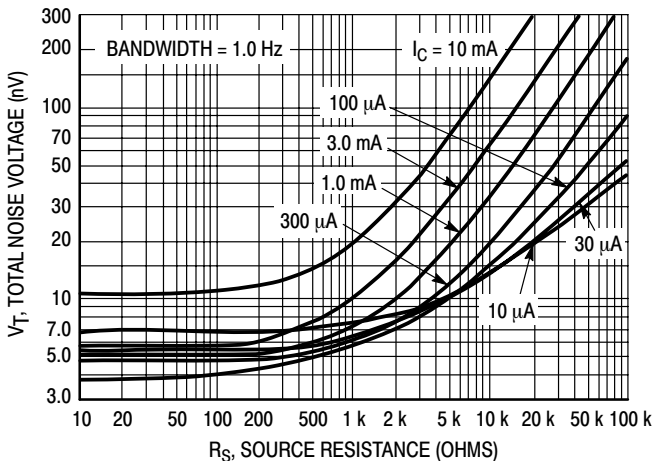


Figure 6. Total Noise Voltage

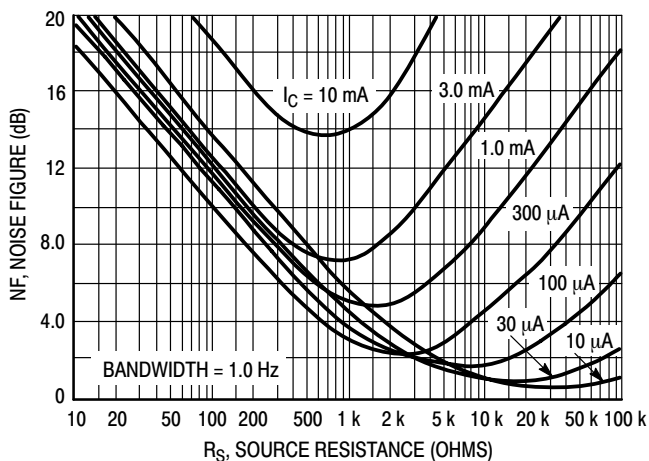


Figure 7. Noise Figure

MPSA18

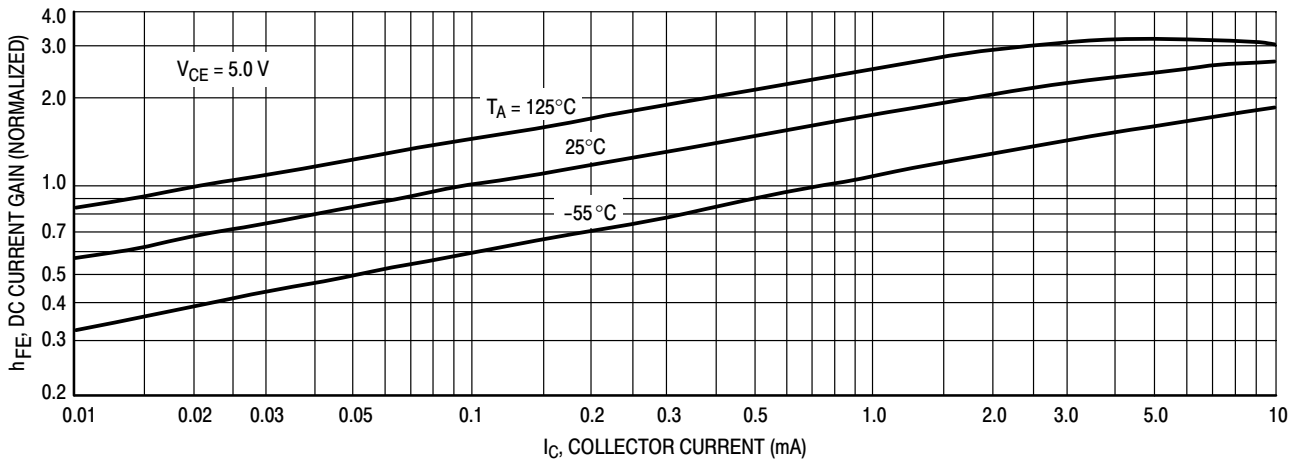


Figure 8. DC Current Gain

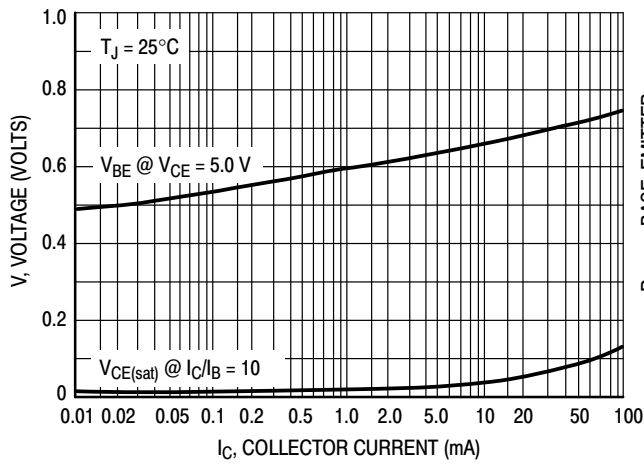


Figure 9. "On" Voltages

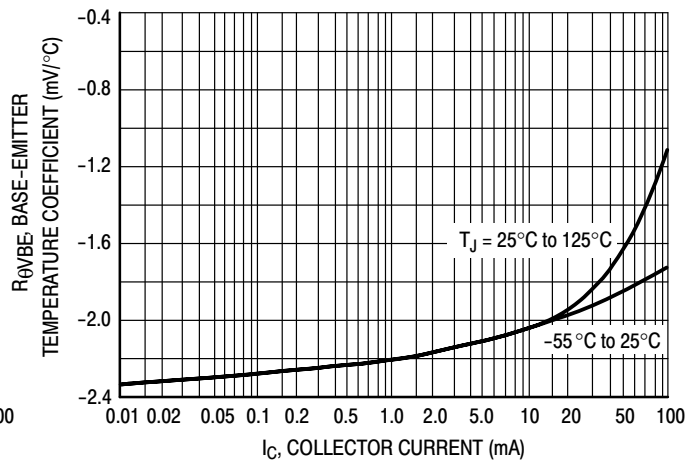


Figure 10. Temperature Coefficients

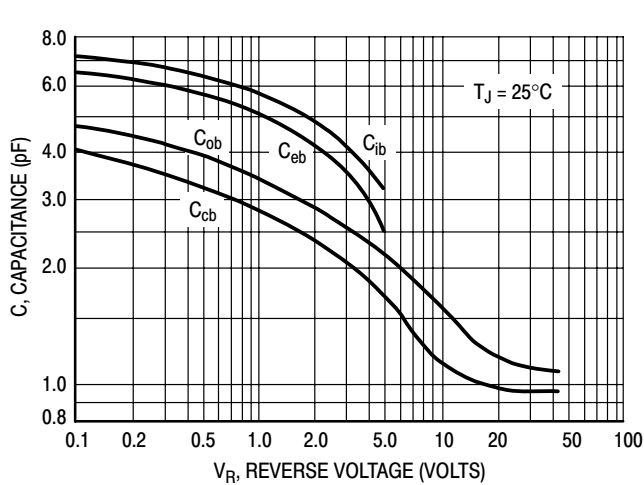


Figure 11. Capacitance

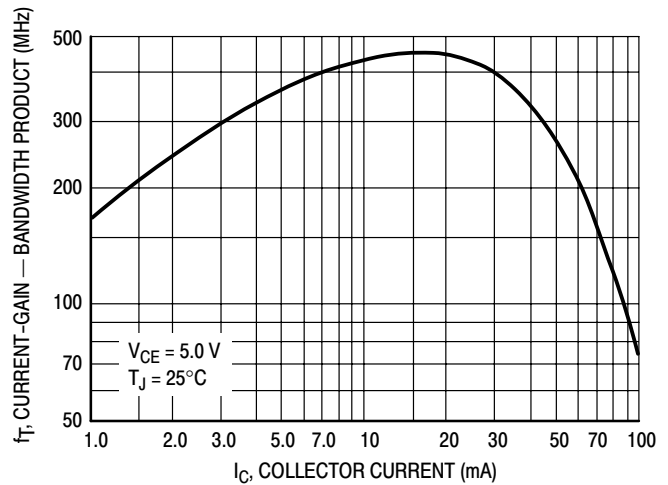
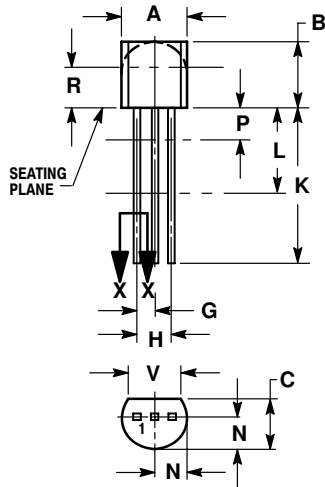


Figure 12. Current-Gain - Bandwidth Product

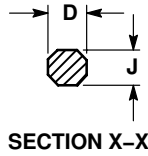
MPSA18

PACKAGE DIMENSIONS

TO-92 (TO-226)
CASE 29-11
ISSUE AM



STRAIGHT LEAD
BULK PACK

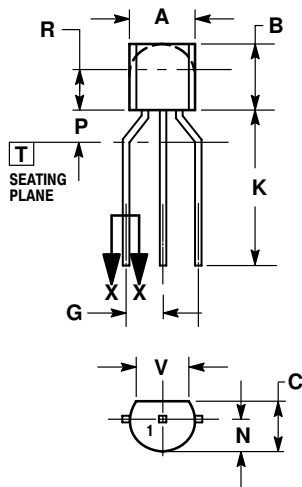


SECTION X-X

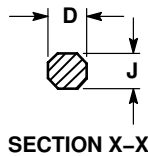
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

| DIM | INCHES | | MILLIMETERS | |
|-----|--------|-------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.175 | 0.205 | 4.45 | 5.20 |
| B | 0.170 | 0.210 | 4.32 | 5.33 |
| C | 0.125 | 0.165 | 3.18 | 4.19 |
| D | 0.016 | 0.021 | 0.407 | 0.533 |
| G | 0.045 | 0.055 | 1.15 | 1.39 |
| H | 0.095 | 0.105 | 2.42 | 2.66 |
| J | 0.015 | 0.020 | 0.39 | 0.50 |
| K | 0.500 | --- | 12.70 | --- |
| L | 0.250 | --- | 6.35 | --- |
| N | 0.080 | 0.105 | 2.04 | 2.66 |
| P | --- | 0.100 | --- | 2.54 |
| R | 0.115 | --- | 2.93 | --- |
| V | 0.135 | --- | 3.43 | --- |



BENT LEAD
TAPE & REEL
AMMO PACK



SECTION X-X

NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

| DIM | MILLIMETERS | |
|-----|-------------|------|
| | MIN | MAX |
| A | 4.45 | 5.20 |
| B | 4.32 | 5.33 |
| C | 3.18 | 4.19 |
| D | 0.40 | 0.54 |
| G | 2.40 | 2.80 |
| J | 0.39 | 0.50 |
| K | 12.70 | --- |
| N | 2.04 | 2.66 |
| P | 1.50 | 4.00 |
| R | 2.93 | --- |
| V | 3.43 | --- |

STYLE 1:

1. PIN 1. EMITTER
2. BASE
3. COLLECTOR

ON Semiconductor and **ON** are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:
Literature Distribution Center for ON Semiconductor
P.O. Box 5163, Denver, Colorado 80217 USA
Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada
Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada
Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free
USA/Canada
Europe, Middle East and Africa Technical Support:
Phone: 421 33 790 2910
Japan Customer Focus Center
Phone: 81-3-5773-3850

ON Semiconductor Website: www.onsemi.com
Order Literature: <http://www.onsemi.com/orderlit>
For additional information, please contact your local Sales Representative