



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# MSA1162GT1G

## General Purpose Amplifier Transistors

### PNP Surface Mount

#### Features

- Moisture Sensitivity Level: 1
- This is a Pb-Free Device

#### MAXIMUM RATINGS (T<sub>A</sub> = 25°C)

Rating	Symbol	Value	Unit
Collector-Base Voltage	V <sub>(BR)CBO</sub>	60	Vdc
Collector-Emitter Voltage	V <sub>(BR)CEO</sub>	50	Vdc
Emitter-Base Voltage	V <sub>(BR)EBO</sub>	7.0	Vdc
Collector Current - Continuous	I <sub>C</sub>	100	mAdc
Collector Current - Peak	I <sub>C(P)</sub>	200	mAdc

#### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Power Dissipation	P <sub>D</sub>	200	mW
Junction Temperature	T <sub>J</sub>	150	°C
Storage Temperature	T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

#### ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

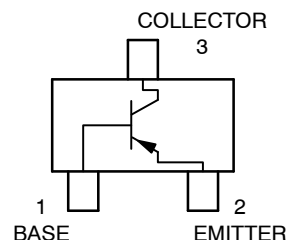
Characteristic	Symbol	Min	Max	Unit
Collector-Emitter Breakdown Voltage (I <sub>C</sub> = 2.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	50	-	Vdc
Collector-Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	-	Vdc
Emitter-Base Breakdown Voltage (I <sub>E</sub> = 10 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	7.0	-	Vdc
Collector-Base Cutoff Current (V <sub>CB</sub> = 45 Vdc, I <sub>E</sub> = 0)	I <sub>CBO</sub>	-	0.1	μAdc
Collector-Emitter Cutoff Current (V <sub>CE</sub> = 10 Vdc, I <sub>B</sub> = 0)	I <sub>CEO</sub>	-	0.1	μAdc
(V <sub>CE</sub> = 30 Vdc, I <sub>B</sub> = 0)		-	2.0	μAdc
(V <sub>CE</sub> = 30 Vdc, I <sub>B</sub> = 0, T <sub>A</sub> = 80°C)		-	1.0	mAdc
DC Current Gain (Note 1) (V <sub>CE</sub> = 6.0 Vdc, I <sub>C</sub> = 2.0 mAdc)	h <sub>FE</sub>	200	400	-
Collector-Emitter Saturation Voltage (I <sub>C</sub> = 100 mAdc, I <sub>B</sub> = 10 mAdc)	V <sub>CE(sat)</sub>	-	0.5	Vdc
Current-Gain-Bandwidth Product (I <sub>C</sub> = 1 mA, V <sub>CE</sub> = 10.0 V, f = 10 MHz)	f <sub>T</sub>	80	-	MHz

1. Pulse Test: Pulse Width ≤ 300 μs, D.C. ≤ 2%.



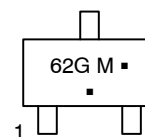
ON Semiconductor®

<http://onsemi.com>



SC-59  
CASE 318D  
STYLE 1

#### MARKING DIAGRAM



62G = Device Code  
M = Date Code\*  
▪ = Pb-Free Package

(Note: Microdot may be in either location)  
\*Date Code orientation may vary depending upon manufacturing location.

#### ORDERING INFORMATION

Device	Package	Shipping†
MSA1162GT1G	SC-59 (Pb-Free)	3000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

TYPICAL ELECTRICAL CHARACTERISTICS

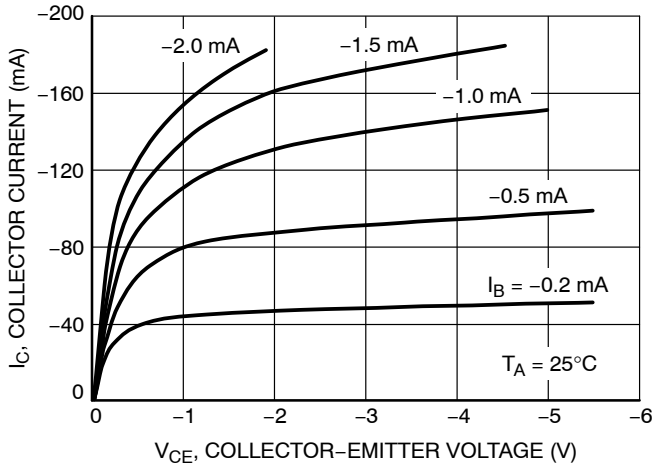


Figure 1. Collector Saturation Region

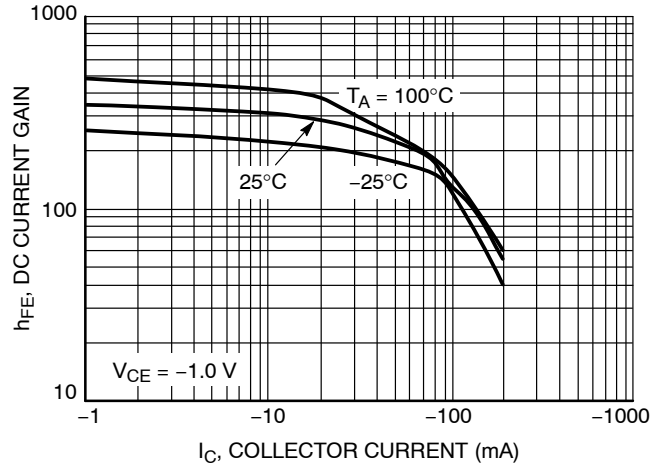


Figure 2. DC Current Gain

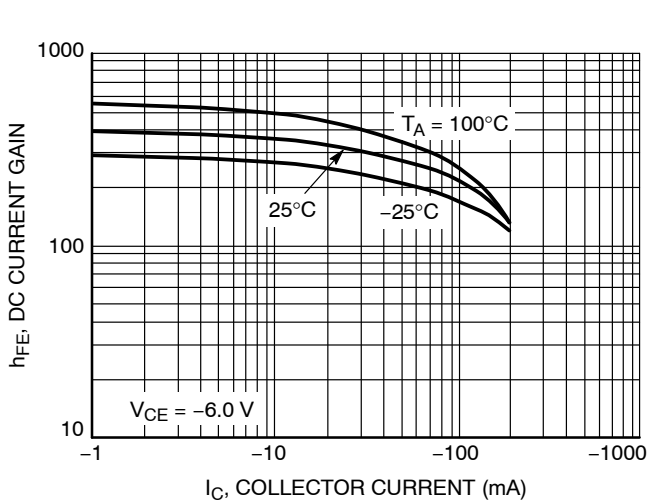


Figure 3. DC Current Gain

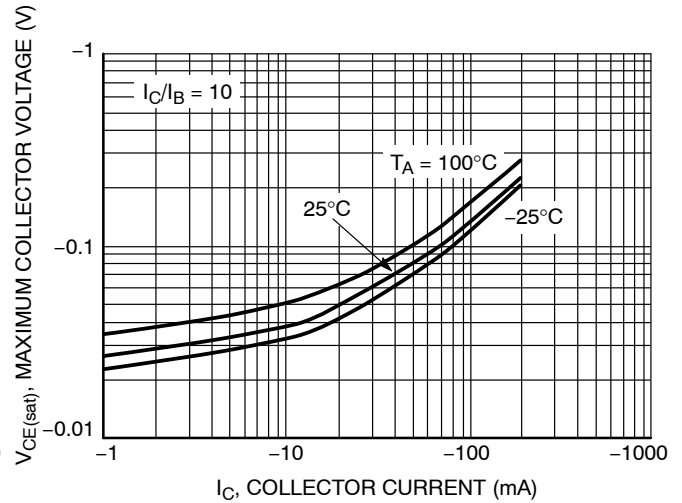


Figure 4.  $V_{CE(sat)}$  versus  $I_C$

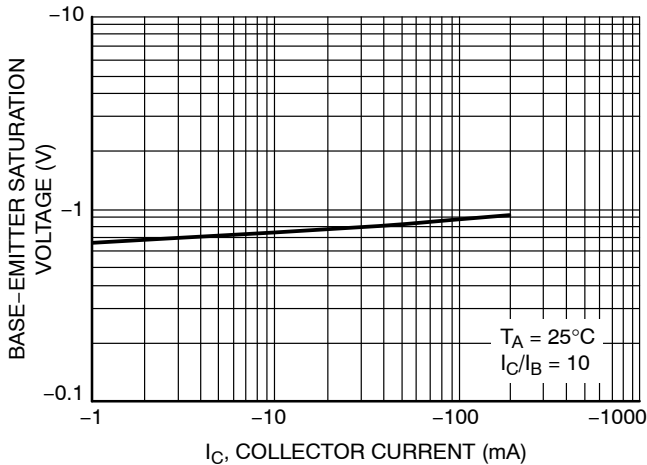


Figure 5.  $V_{BE(sat)}$  versus  $I_C$

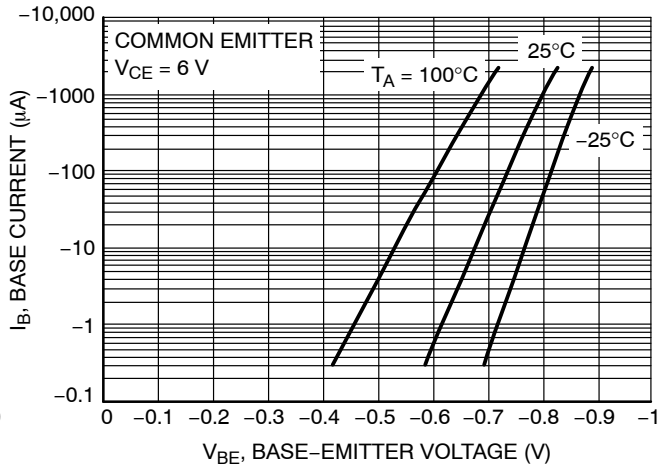
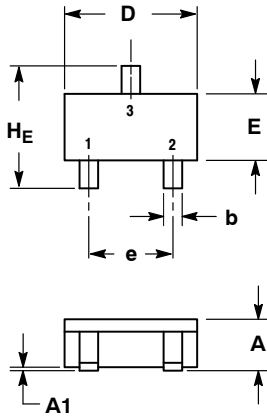


Figure 6. Base-Emitter Voltage

# MSA1162GT1G

## PACKAGE DIMENSIONS

SC-59  
CASE 318D-04  
ISSUE H



NOTES:

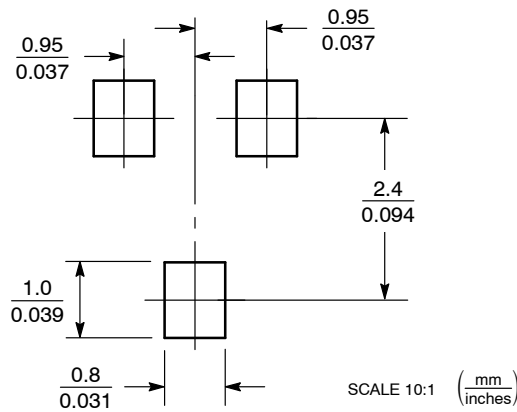
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	1.00	1.15	1.30	0.039	0.045	0.051
A1	0.01	0.06	0.10	0.001	0.002	0.004
b	0.35	0.43	0.50	0.014	0.017	0.020
c	0.09	0.14	0.18	0.003	0.005	0.007
D	2.70	2.90	3.10	0.106	0.114	0.122
E	1.30	1.50	1.70	0.051	0.059	0.067
e	1.70	1.90	2.10	0.067	0.075	0.083
L	0.20	0.40	0.60	0.008	0.016	0.024
HE	2.50	2.80	3.00	0.099	0.110	0.118


STYLE 1:

- PIN 1. BASE
- EMITTER
- COLLECTOR

### SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

ON Semiconductor and  are registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at [www.onsemi.com/site/pdf/Patent-Marking.pdf](http://www.onsemi.com/site/pdf/Patent-Marking.pdf). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

### PUBLICATION ORDERING INFORMATION

**LITERATURE FULFILLMENT:**  
Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** [orderlit@onsemi.com](mailto:orderlit@onsemi.com)

**N. American Technical Support:** 800-282-9855 Toll Free  
USA/Canada  
**Europe, Middle East and Africa Technical Support:**  
Phone: 421 33 790 2910  
**Japan Customer Focus Center**  
Phone: 81-3-5817-1050

**ON Semiconductor Website:** [www.onsemi.com](http://www.onsemi.com)  
**Order Literature:** <http://www.onsemi.com/orderlit>

For additional information, please contact your local Sales Representative