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TOSHIBA Transistor Silicon NPN Epitaxial Planar Type

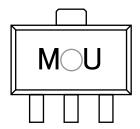
# **MT3S20P**

VHF-UHF Band Low-Noise, Low-Distortion Amplifier Applications

#### **FEATURES**

- Low Noise Figure: NF=1.45dB (typ.) (@f=1GHz)
- High Gain: |S21e|<sup>2</sup>=11dB (typ.) (@f=1GHz)

### Marking



## Absolute Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Collector-base voltage	$V_{CBO}$	20	V
Collector-emitter voltage	V <sub>CEO</sub>	12	V
Emitter-base voltage	V <sub>EBO</sub>	1.5	V
Collector current	IC	80	mA
Base current	ΙΒ	10	mA
Collector power dissipation	PC	400	mW
Collector power dissipation	P <sub>C</sub> (Note1)	1.8	W
Junction temperature	Tj	150	°C
Storage temperature range	T <sub>stg</sub>	−55 to 150	°C

Note.1 : The device is mounted on a ceramic board (25mm X 25mm X 0.8 mm (t))

Note.2 : Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

	Unit: mm
1.5±0.1  1.8ase 2. Collect 3. Emitte	X. 0.4±0.05 11.5±0.1  1.5±0.1  1.5±0.1
PW-Mini	
JEDEC	-
JEITA	SC-62
TOSHIBA	2-5K1A

Weight: 0.05 g (Typ.)



## **Microwave Characteristics (Ta = 25°C)**

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Transition frequency	f <sub>T</sub>	V <sub>CE</sub> = 5V, I <sub>C</sub> = 30mA	5	7	_	GHz
Insertion gain	S21e  <sup>2</sup> (1)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 500MHz	_	16.5	_	dB
	S21e  <sup>2</sup> (2)	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA, f = 1GHz	9	11	_	uв
Noise figure	NF	V <sub>CE</sub> = 5V, I <sub>C</sub> = 20mA, f = 1GHz	_	1.45	2	dB
3 <sup>rd</sup> order intermodulation distortion output intercept point	OIP3	$V_{CE}$ = 5V, $I_{C}$ = 50mA, f = 500MHz, $\triangle$ f=1MHz	27.5	31.5	_	dBmW

## **Electrical Characteristics (Ta = 25°C)**

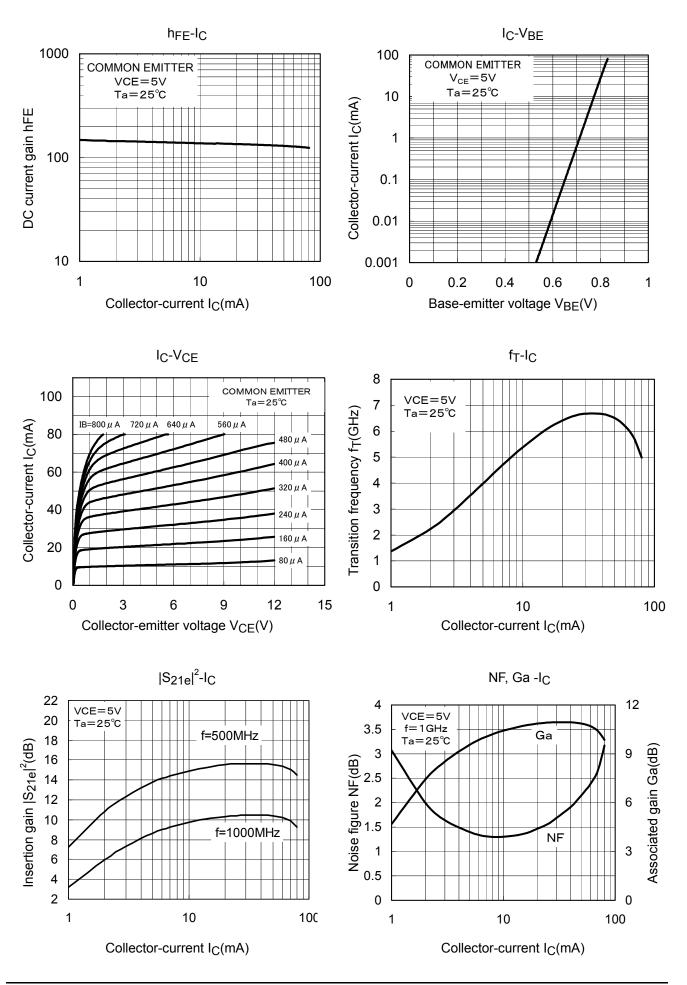
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Collector cut-off current	I <sub>CBO</sub>	$V_{CB} = 10V, I_{E} = 0$	_	_	0.1	μA
Emitter cut-off current	I <sub>EBO</sub>	V <sub>EB</sub> = 1V, I <sub>C</sub> = 0	_	_	0.5	μA
DC current gain	hFE	V <sub>CE</sub> = 5V, I <sub>C</sub> = 50mA	100	150	200	-
Reverse transfer capacitance	C <sub>re</sub>	V <sub>CB</sub> = 5V, I <sub>E</sub> = 0, f = 1MHz (Note3)		0.85	1.1	pF

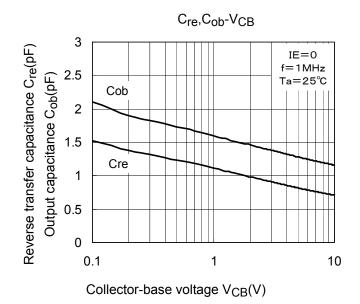
Note.3 : C<sub>re</sub> is measured using a 3-terminal method with capacitance bridge.

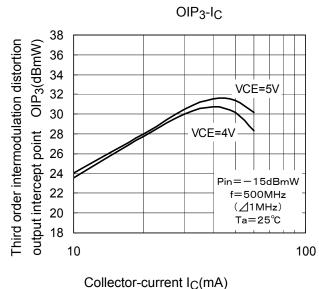
#### Caution:

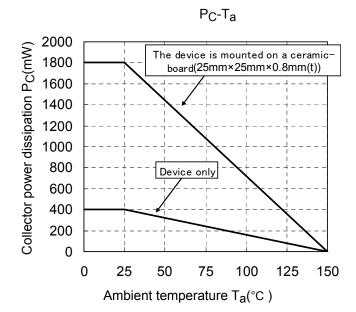
This device is sensitive to electrostatic discharge.

Please make enough tool and equipment earthed when you handle.









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