

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









# Silicon NIR (800nm) APD w/ TIA

**Product No: MTAPD-05-003** 

# NIR enhanced (800nm) Silicon Avalanche Photodiode coupled to a transimpedance amplifier in a hermetic TO-5 package.

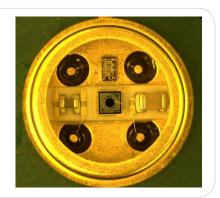
The MTAPD-05-003 is a NIR (800nm) enhanced  $500\mu m$  diameter APD inputing into a high speed TIA to produce a differential output voltage. Positive voltage supply required for the TIA. Positive high voltage supply required for the APD.

#### **FEATURES**

- > TO-5 Metal Can Package
- > High Speed
- > High Reliability

#### **APPLICATIONS**

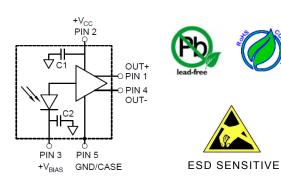
- > LiDAR
- > Free Space Optical
- > Fiber Applications



#### **Absolute Maximum Ratings**

ITEMS	SYMBOL	RATING	UNITS
Supply Voltage	Vcc	+3.0 to +5.5	V
Supply Current	Icc	63	mA
Operating Temperature Range	Topr	-20 to +55	°C
Storage Temperature Range	Tstg	-40 to +100	°С
Soldering Temperature (*1)	Ts	260	°C

<sup>\*1:</sup> Time: 5 sec max.; Position: Up to 3mm from header body.



### Electrical & Optical Characteristics (Ta = 25°C)

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
TIA Supply Voltage		+3.0	+3.3	+5.5	V
TIA Supply Current			34	63	mA
APD Breakdown Voltage	I <sub>R</sub> > 2μA	160		200	V
APD Responsivity Range		450		1050	nm
APD Responsivity @ 800nm	APD V <sub>R</sub> for M=100		50		A/W
Temperature Coefficient of	$TC = -40^{\circ}C$ to $+85^{\circ}C$		0.6		V/°C
APD Sensitivity @ 800nm	APD V <sub>R</sub> for M=100		130		mV/μW
Small Signal Bandwidth	$R_L = 100\Omega$ differential		400		MHz
Low-Frequency Cutoff	-3dB; input < 20μA DC		30		kHz
Output Impedance	Single ended (per side)	48	50	52	Ω

2018-19-06



Optoelectronics

Product No: MTAPD-05-003

#### **APD Information:**

Fig. 1 Typical APD Responsivity @ 0V

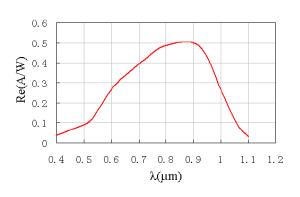


Fig. 3 Typical APD Multiplication (M)

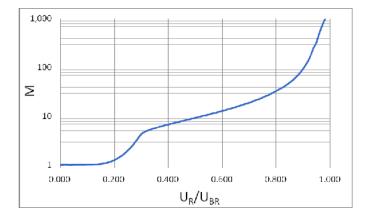


Fig. 2 Typical APD Dark Current

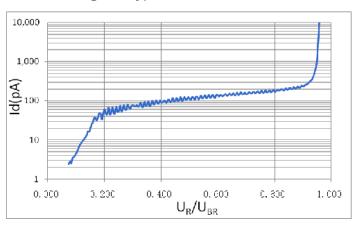
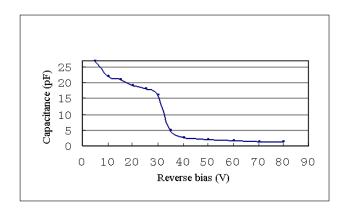
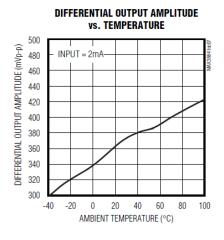
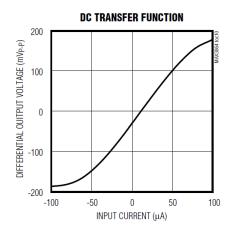


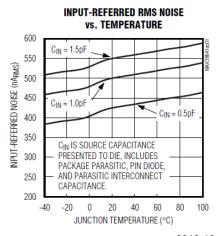
Fig. 4 Typical APD Capacitance



## Amplifier Information:







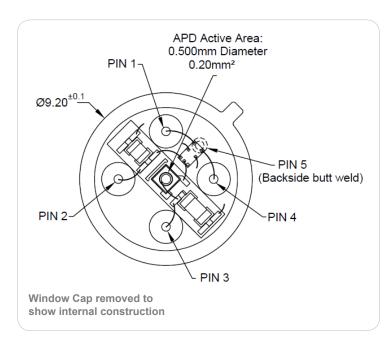
2018-19-06

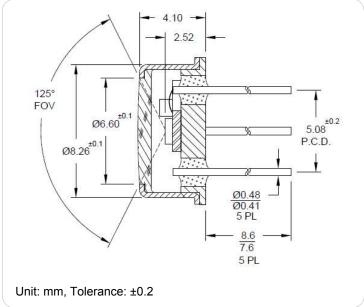


# Silicon NIR APD w/ TIA

Product No: MTAPD-05-003

## Package Information:





## **Application Notes:**

The MTAPD-05-003 is a high speed optical data receiver. It combines a silicon avalanche photodiode with a transimpedance amplifier in a single hermetic TO5 package.

To operate the MTAPD-05-003 requires a +3.0V to +5.5V voltage supply for the TIA and a separate high voltage supply (+100 to +200V) for biasing the APD.

The APD will perform similar to the typical multiplication (gain) curve in Fig. 3. The TIA produces maximum differential output signals just less than  $\pm 200$  millivolts. A current limiting resistor must be placed in series with the APD bias voltage to limit the current into the APD and subsequently into the transimpedance amplifier. Failure to limit this current may result in permanent damage/failure of the device. As the value of this resistor can effect performance, an initial value of  $390 \text{k}\Omega$  is recommended as a starting point with additional experimentation providing the optimum resistance value for the application.

As with all high frequency devices, careful attention must be taken when designing the layout and component placement of the carrier PCB/substrate to maximize performance.

**ESD SENSITIVE** 

2018-19-06