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# Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







Revision. 2

# **Panasonic**

MOS FET

## MTM862270LBF

## MTM862270LBF

#### Silicon N-channel MOSFET

#### For Switching

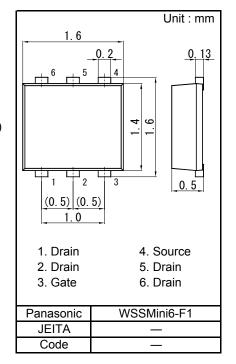
#### ■ Features

- Low drain-source On-state Resistance : RDS(on) typ = 80 m $\Omega$  (VGS = 4.0 V)
- Low drive voltage:1.8V drive
- Halogen-free / RoHS compliant (EU RoHS / UL-94 V-0 / MSL : Level 1 compliant)

#### ■ Marking Symbol : JF

#### ■ Packaging

Embossed type (Thermo-compression sealing): 10 000 pcs / reel (standard)

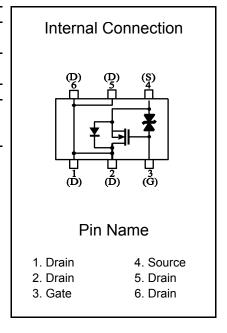


#### ■ Absolute Maximum Ratings Ta = 25 °C

Parameter	Symbol	Rating	Unit
Drain to Source Voltage	VDS	20	V
Gate to Source Voltage	VGS	±10	V
Drain Current	ID	2.2	Α
Drain Current (Pulsed) *1	IDp	8.0	ζ.
Total Power Dissipation*2	PD	540	mW
Channel Temperature	Tch	150	
Operating Ambient Temperature	Topr	-40 to +85	°C
Storage Temperature Range	Tstg	-55 to +150	

Note) \*1 Pulse width  $t \le 10 \mu s$ , Duty cycle  $\le 1 \%$ 

\*2 Measuring on ceramic substrate at 40 mm × 38 mm × 0.2 mm PD absolute maximum rating without a heat shink: 150 mW



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#### ■ Electrical Characteristics Ta = 25 °C ± 3 °C

Parameter	Symbol	Conditions	Min	Тур	Max	Unit
Drain-source Breakdown Voltage	VDSS	ID = 1.0 mA, VGS = 0	20			V
Zero Gate Voltage Drain Current	IDSS	VDS = 20 V, VGS = 0			1.0	μΑ
Gate-source Leakage Current	IGSS	$VGS = \pm 8.0 \text{ V}, VDS = 0$			±10	μΑ
Gate-source Threshold Voltage	Vth	ID = 1.0 mA, VDS = 10 V	0.4	0.85	1.3	V
Drain-source On-state Resistance *1	RDS(on)1	ID = 1.0 A, VGS = 4.0 V		80	105	mΩ
	RDS(on)2	ID = 0.5 A, VGS = 2.5 V		100	150	mΩ
	RDS(on)3	ID = 0.5 A, VGS = 1.8 V		170	300	mΩ
Forward transfer admittance *1	Yfs	ID = 1.0 A, VDS = 10 V	3.0	4.0		S
Input Capacitance	Ciss			280		pF
Output Capacitance	Coss	VDS = 10 V, VGS = 0, f = 1 MHz		18		pF
Reverse Transfer Capacitance	Crss			17		pF
Turn-on time *2	ton	VDD = 10 V, VGS = 0 to 4 V ID = 1.0 A		12		ns
Turn-off time *2	toff	VDD = 10 V, VGS = 4 to 0 V ID = 1.0 A		50		ns

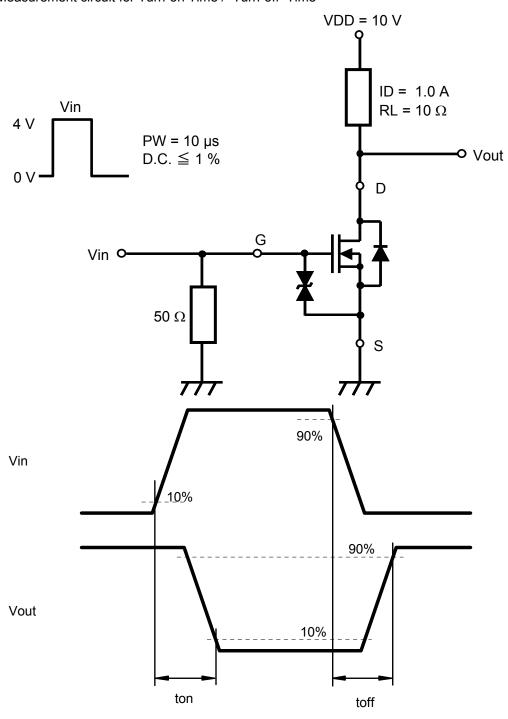
Note) 1. Measuring methods are based on JAPANESE INDUSTRIAL STANDARD JIS C 7030 Measuring methods for transistors.

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<sup>2. \*1</sup> Pulse test

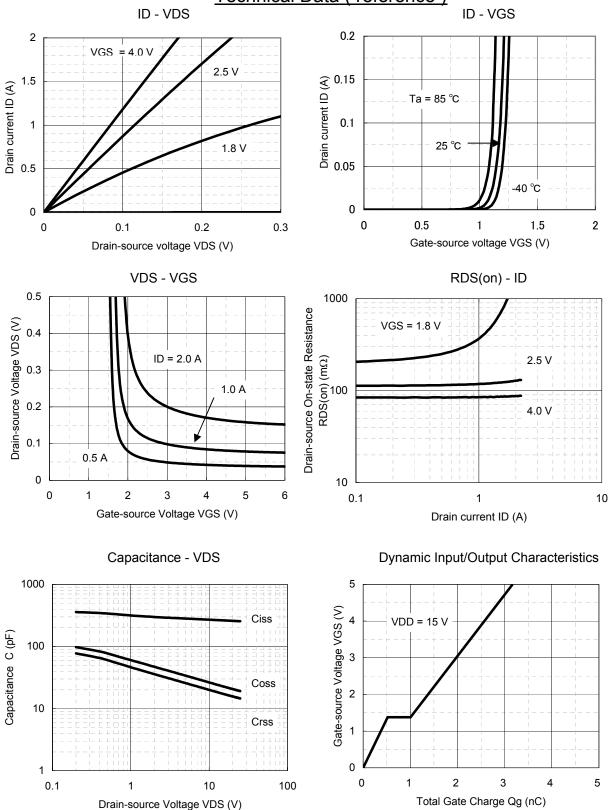
<sup>\*2</sup> Measurement circuit for Turn-on Time / Turn-off Time

\*2 Measurement circuit for Turn-on Time / Turn-off Time



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## Technical Data (reference)

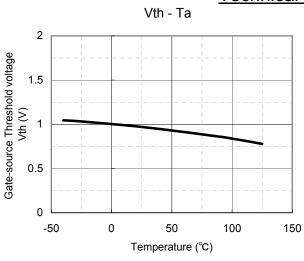


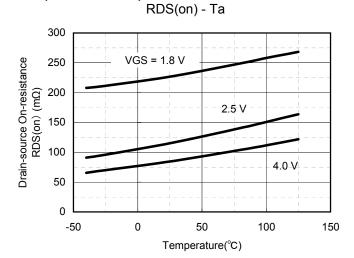
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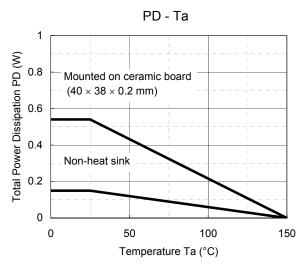
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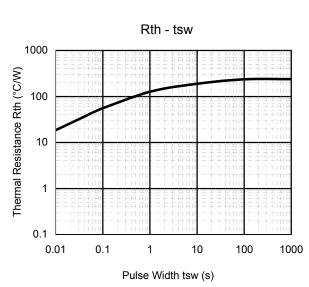
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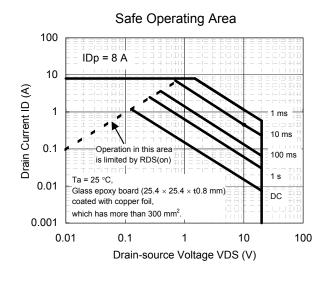
## Technical Data (reference)









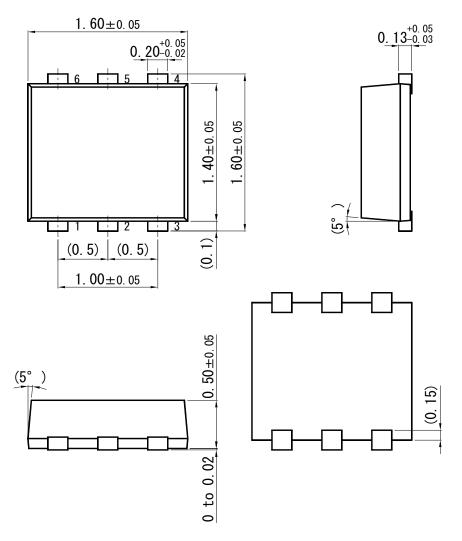


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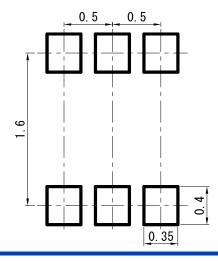
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WSSMini6-F1

Unit: mm



■ Land Pattern (Reference) (Unit : mm)



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