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# Instruction manual for Evaluation Board of TB62262FTAG

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## [Outline]

The TB62262 is a two-phase bipolar stepping motor driver using a PWM chopper. CLK-IN control system and BiCD process are adopted. Rating of 40V and 1.5A is realized.

This evaluation board mounts necessary components to evaluate the IC. Each excitation drive of full step, half step and quarter step can be run with PWM constant current drive. Please sense low noise and low vibration of the stepping motor.

## [Note]

In using, please be careful about thermal condition sufficiently.

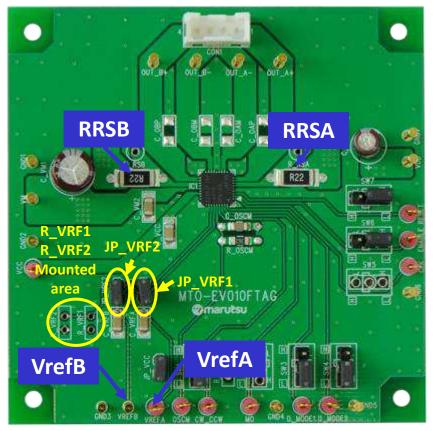
As for each control signal, please refer to the IC specification by accessing to the below URL.

http://toshiba.semicon-storage.com/info/lookup.jsp?pid=TB62262FTAG&region=apc&lang=en Further, the application of this evaluation board is limited to the purpose of evaluating and learning the motor control. Please do not ship them to a market.

			1		<b>@</b> marutsu
Course ations to Evaluations beaud				Two-phase bipolar	
<b>Connection to Evaluation boar</b>			â	type	
Corresponding table				Stepping motor	
(Silk name vs. Si			-		-
(Note) Silk name and signal name on the board are same on the TB62262FTAG.					
are same on the 10022	262FTAG.				
Silk name	Signal name	ı 📕		OUT_B+ OUT_B- OUT_A- OUT_A+	
CLK	CLK				
ENABLE	ENABLE	i 🛛 📕			
		· 🛛	-		
RESET	RESET	Power 🗸			SHT
CW/CCW	CW/CCW	source of			
MO	MO	VM 📗			
DMODE_1	DMODE_1	(10V to 47V)	ND	R VSOU	
DMODE_2	DMODE_2		2 <b>0</b>	aller .	600
				MTO-EV010FTAG Omarutsu	
			<b>o</b>		
				1. A & @ @ @ @	
		V	refB(for B-axis	s) VrefA(for A-axis)	A BORSEL AND A COMPANY
			Pofora	ence voltage for	
				or current set	
				f (0V to 3.6V)	
		l	VIC		

# Setting evaluation board 1

## Setting motor current



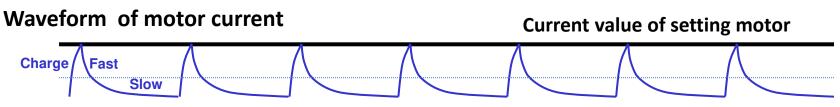
#### Setting motor current

out(max) = VREF(gain) x 
$$\frac{Vref(V)}{Rrs(\Omega)}$$

VREF(gain):Decay ratio of VREF: 1/5.0 (typ.)

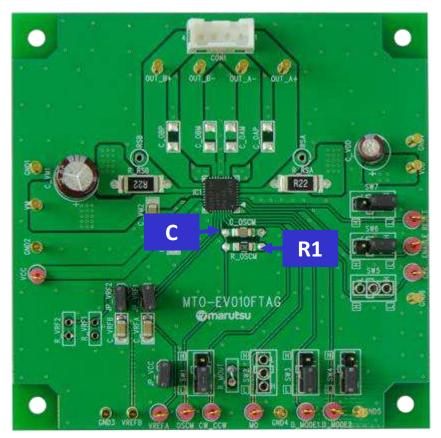
#### RRS=0.22Ω

VREF of A-axis and B-axis are made common by short the jumper (JP\_VRF1) and the both voltages are possible to input from VREFA pin. And Vref can be generated from the internal regulator (VCC) by mounting the divider resistance to R\_VRF1 and R\_VRF2 and shortcircuiting JP\_VRF2.



# Setting evaluation board2

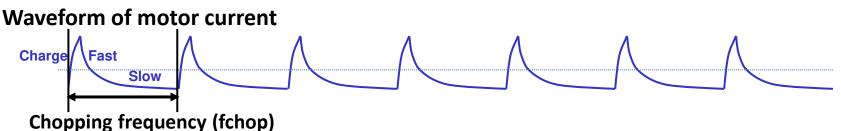
Setting chopping frequency of the constant current of the motor



### Formula of setting chopping frequency

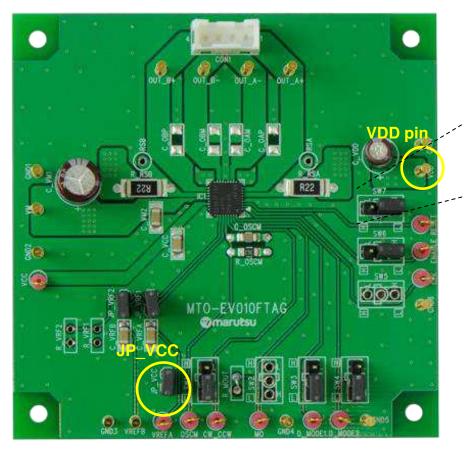
fOSCM = 1 / [0.56 x {C x (R1 + 500)}] fchop = fOSCM / 16

Recommended frequency range: fchop=40kHz to 150kHz 100kHz configuration Mounted parts are as follows; Capacitor (C=270pF), Resistance (R1=3.6k $\Omega$ )



# Setting evaluation board3

## **Setting motor operation**



#### [Enhanced figure of jumper part]



Jumper indicated above is adopted on this evaluation board to set operation of the TB62262FTAG.

To select the function by the jumper, shortcircuit JP\_VCC or supply the voltage of high level by VDD pin.

In above, fixed level of the silk near the jumper is indicated inside the white frame. Please change the short position according to the configuration of the usage function. In case of inputting the signal externally, please remove the short pin.

#### marutsu

