

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



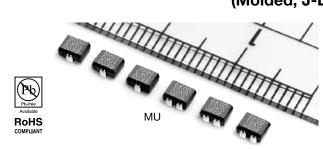




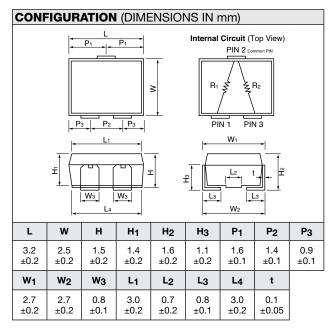


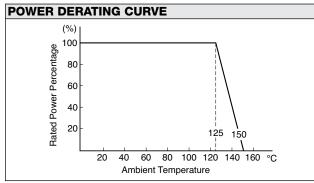
Ultra Precision SMT Resistor 1-2-3 Network

(Molded, J-Lead Terminal)



COMPOSITION OF TYPE NUMBER											
Example:											
MU	1K000/	10	K00	В	Q	L					
1	2		3	4	⑤	6					
	al Resistance Values al Resistance Values	(R1)	4) Tolerance 5) Tolerance 6) Tape & F	e (Mate	ching)	e Being Required					





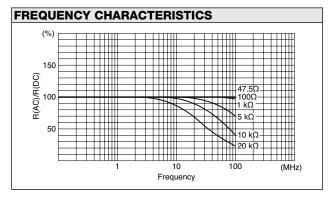
RESISTANCE RANGE, TOLERANCE, RATED POWER											
Туре	Resistance Range	Resistance	Rated Power/ Element								
Type	Element**	Absolute*	Matching*	(W) at 125°C							
	10Ω ≤R <100Ω	±0.1% (B) ±0.5% (D)	±0.05% (A) ±0.1% (B) ±0.5% (D)								
MU	100Ω ≤R <1kΩ	±0.05% (A) ±0.1% (B) ±0.5% (D)	±0.02% (Q) ±0.05% (A) ±0.1% (B) ± 0.5% (D)	0.05							
	1kΩ ≤R ≤20kΩ	±0.02% (Q) ±0.05% (A) ±0.1% (B) ± 0.5% (D)	±0.01% (T) ±0.02% (Q) ±0.05% (A) ±0.1% (B) ±0.5% (D)								

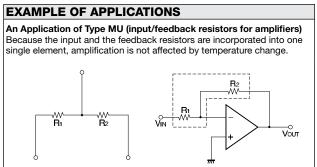
- Symbols in parentheses are for type number composition.
- ** Please contact us for the availability.

ABSOLUTE TCR									
Resistance Range (Ω)	Absolute TCR (ppm/°C) -55C to +125°C								
10Ω ≤R <30Ω	±15								
30Ω ≤R <100Ω	±10								
100Ω ≤R ≤20kΩ	±5								

TCR TRACKING											
Resistance Ratio	TCR Track- ing (ppm/°C) -55°C to +125°C										
Ratio = 1	±1										
1 <ratio td="" ≤10<=""><td>±2</td></ratio>	±2										
10 <ratio td="" ≤100<=""><td>±3</td></ratio>	±3										
100 <ratio< td=""><td>±5</td></ratio<>	±5										

Applicable >50 Ω







PERFORMANCE										
Parameters	Test Condition		PHA ication	ALPHA Typical Test Data						
		ΔR	∆ Ratio	ΔR	∆ Ratio					
Maximum Rated Operating Temperature Working Temperature Range		125°C -65°C to +150°C								
Thermal Shock Overload	-65° C/30 min. \leftrightarrow +150 $^{\circ}$ C/30 min., 5 cycles Rated Voltage x 2.5, 5 sec.	±0.05% ±0.05%	±0.02% ±0.02%	±0.01% ±0.01%	±0.005% ±0.005%					
Low Temperature Storage and Operation Substrate Bending Test	–65°C, No Load, 24 hrs. \rightarrow Rated Voltage, 45 min. 3 mm Bend 60 sec.	±0.05% ±0.05%	±0.02% ±0.02%	±0.01% ±0.01%	±0.005% ±0.005%					
Dielectric Withstanding Voltage Insulation Resistance	Atom. Pres.: AC 200V, 1 min. DC 100V, 1 min.	±0.01% over 10	±0.01% ,000 ΜΩ	±0.005% ±0.00259						
Resistance to Soldering Heat Moisture Resistance	260°C, 10 sec. +65°C to -10°C, 90% to 98% RH, Rated Power, 10 cycles (240 hrs.)	±0.05% ±0.05%	±0.02% ±0.02%	±0.01% ±0.03%	±0.005% ±0.01%					
Shock Vibration, High Frequency	100G, 6 ms, Sawtooth Wave, X, Y, Z, each 10 shocks 20G, 10 Hz to 2,000 Hz to 10 Hz, 20 min., X, Y, Z, each 2.5 hrs.	±0.02% ±0.02%	±0.01% ±0.01%	±0.01% ±0.01%	±0.005% ±0.005%					
Life	125°C, Rated Power, 1.5 hrs. – ON, 0.5 hrs. – OFF, 2,000 hrs.	±0.05%	±0.02%	±0.03%	±0.015%					
Storage Life	15°C to 35°C, 15% RH to 75% RH, No Load, 10,000 hrs.	±0.005%	±0.0025%	±0.0025%	±0.0015%					
High Temperature Exposure	150°C, No Load, 2,000 hrs.	±0.05%	±0.02%	±0.02%	±0.01%					

TAPE AND REEL PACKAGE (BASED ON EIA-481-1) (DIMENSIONS IN mm)																	
Tape Dimensions									Reel Dimensions (Reel capacity: 800 pieces/reel)								
Sprocket Hole J Cavity 0.35 max.											D A	B C	Z W1				
Туре	Α	В	С	D	E	F	G	Н	J	Α	N	В	С	D	W ₁	W ₂	r
MU	3.6	3.1	12.0	5.5	1.75	8.0	2.0	4.0	I	Dia. 178		Dia. 13	Dia. 21	2	12.4	18.4	1.0
	±0.2	±0.2	±0.3	±0.05	±0.1	±0.1	±0.05	±0.1	+0.1-0	±2	min.	±0.5	±0.8	±0.5	+2.0-0	max.	±0.5

PRECAUTION IN USING FACE-BONDED CHIP RESISTOR (DIMENSIONS IN mm)

5 10 20 30 40 50 60 (sec)

Length of contact

1. Storage

Storage condition or environment may adversely affect solderability of the exterior terminals. Do not store in high temperature and humidity. The recommended storage environment is lower than 40°C, has less than 70% RH humidity and is free from harmful gases such as sulphur and chlorine.

2. Caution in Soldering

- Hand Soldering
 Hand soldering is applicable
 as shown at right.
 Recommended
 - Temp. of Iron Tip: 240°C to 270°C
 - Power of Iron: 20W or less
- Diameter of Tip: Dia. 3 mm max.
 Solder Reflew in European
- Solder Reflow in Furnace Recommended
 - Peak Temperature: 250°C +0°C/-5°C
 - Holding time: 10 sec. max.
 - To cool gradually at room temperature
- Dipping in Solder (Wave or Still)
 Recommended
 - Temp. of Solder: 240°C to 250°C
 - Length of Dipping: 3 to 4 seconds
 - To cool gradually at room temperature

Other

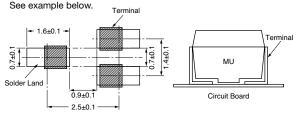
Corrosion-free flux, such as rosin, is recommended. Do not apply pressure to the molded housing immediately after soldering.

3. Cleaning

Use volatile cleaner such as methylalcohol or propylalcohol.

4. Circuit Board Design

The dimensions of solder land must be determined in conformity with the size of resistors and with the soldering method. They are also subject to the mounting machine and the material of the substrate.



When parts are mounted on a board in high density, solder can possibly attach to the resistors in an excessive amount to affect performance or reliability of the resistors. To prevent this effect, the use of solder resist is recommended to isolate solder lands.