Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!

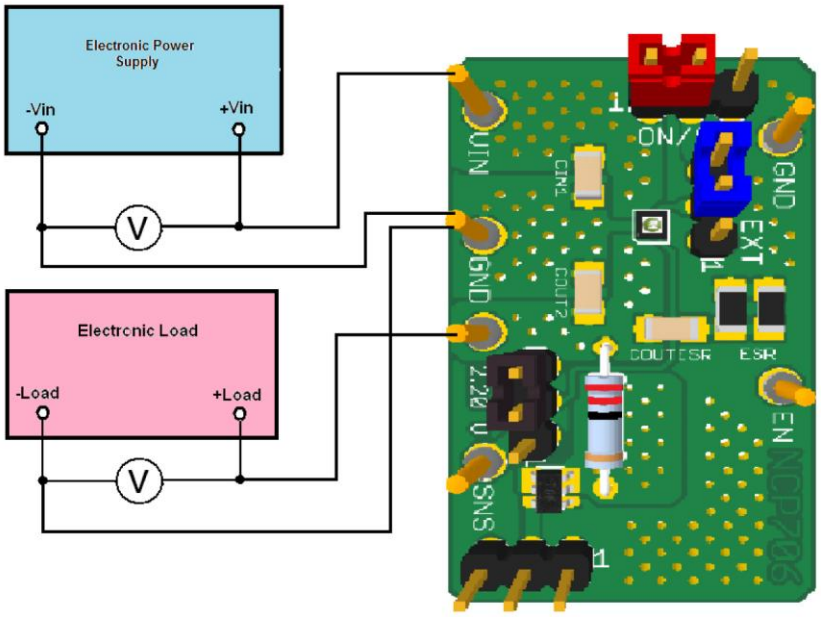
Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832
Email & Skype: info@chipsmall.com Web: www.chipsmall.com
Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China

8/2/2013

ON Semiconductor

Test Procedure for the NCP706MX22TAGEVB Evaluation Board



Test procedure:

1.Check the position of jumpers and correct those if necesary in acordance with the picture above.

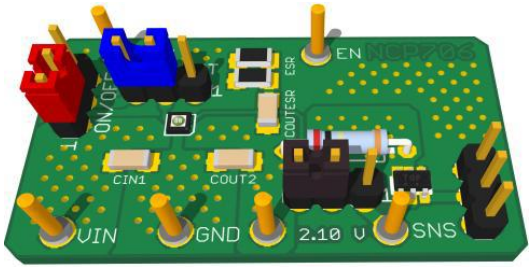
a) ON/OFF **RED**

b) EXT **BLUE**

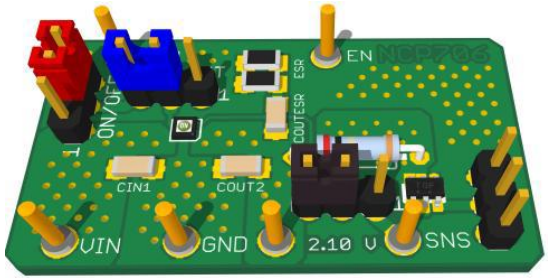
c) SENSE **BLACK**

2. Connect the test setup as shown Figure above
3. Appy an input voltage **Vin = 2.5 V**
4. Appy Iout = 0mA load.
5. Check that Vout is **2.2 V +/-1%**.
6. Increase Iout up to **1A**
7. Check that Vout is **2.2 V +/-1%**.
7. Increase Vin up to **5.5 V** and decrease the load in accordance with **SOA**
8. Power down the Load
9. Power down the Vcc
10. End of test

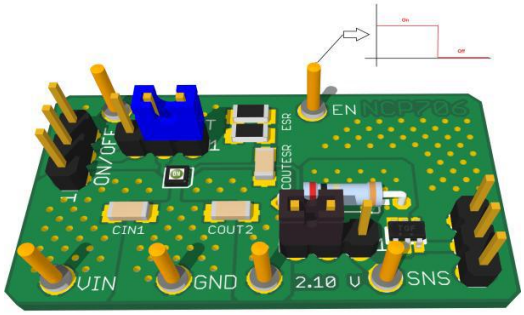
Jumper functions description:



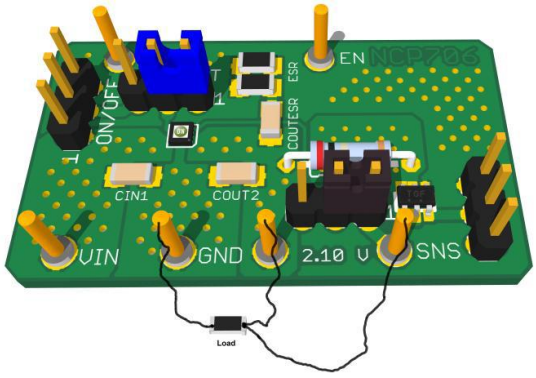
Internal ENABLE On state



Internal ENABLE Off state



External ENABLE Signal



External SNS Signal, connect to positive load node for voltage drop at output and load wiring compensation.