



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



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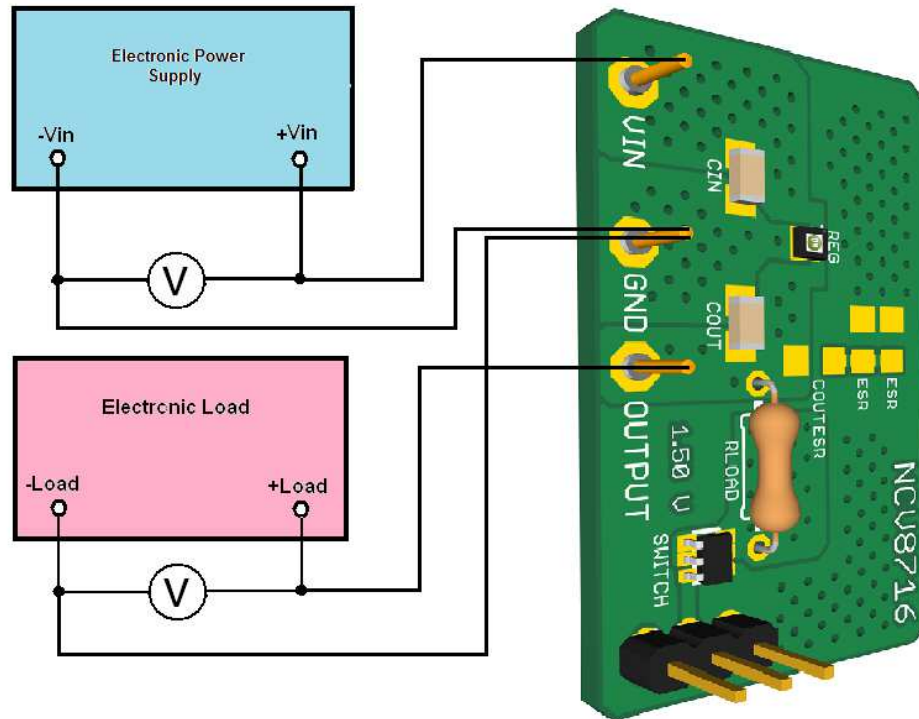
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Test Procedure for the NCV8716MTTBGEVB Evaluation Board



Test procedure:

1. Check the Voltage Version (It is mentioned next to OUTPUT terminal on the demoboard.)
2. Connect the test setup as shown Figure above
3. Apply an input voltage **V_{in} = 6.0 V**
4. Apply I_{out} = 0mA load.
5. Check that V_{out} is:
 - 1.5 V +/-2% for 1.5 V voltage version**
 - 1.8 V +/-2% for 1.8 V voltage version**
 - 2.5 V +/-2% for 2.5 V voltage version**
 - 3.0 V +/-2% for 3.0 V voltage version**
 - 3.3 V +/-2% for 3.3 V voltage version**
 - 5.0 V +/-2% for 5.0 V voltage version**
6. Increase I_{out} up to **80 mA**



7. Check that V_{out} is :

- 1.5 V $\pm 2\%$ for 1.5 V voltage version**
- 1.8 V $\pm 2\%$ for 1.8 V voltage version**
- 2.5 V $\pm 2\%$ for 2.5 V voltage version**
- 3.0 V $\pm 2\%$ for 3.0 V voltage version**
- 3.3 V $\pm 2\%$ for 3.3 V voltage version**
- 5.0 V $\pm 2\%$ for 5.0 V voltage version**

8. Increase V_{in} up to **24.0 V** and decrease the load in accordance with **Safe Operation Area**

9. Power down the Load

10. Power down the V_{cc}

11. End of test