



Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from,Europe,America and south Asia,supplying obsolete and hard-to-find components to meet their specific needs.

With the principle of “Quality Parts,Customers Priority,Honest Operation,and Considerate Service”,our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip,ALPS,ROHM,Xilinx,Pulse,ON,Everlight and Freescale. Main products comprise IC,Modules,Potentiometer,IC Socket,Relay,Connector.Our parts cover such applications as commercial,industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



## Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: [info@chipsmall.com](mailto:info@chipsmall.com) Web: [www.chipsmall.com](http://www.chipsmall.com)

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China



# NCX2202

Low voltage comparator; open-drain output

Rev. 5 — 30 October 2012

Product data sheet

## 1. General description

---

The NCX2202 is a single low voltage, low power, comparator with open-drain output.

The NCX2202 has a very low supply current of 6  $\mu\text{A}$  and is guaranteed to operate at a low voltage of 1.3 V and is fully operational up to 5.5 V. These characteristics make the device convenient for use in both 3.0 V and 5.0 V systems.

## 2. Features and benefits

---

- Wide supply voltage range from 1.3 V to 5.5 V (functional operating range)
- Rail-to-rail input/output performance
- Very low supply current of 6  $\mu\text{A}$  (typical)
- Very low-power consumption
- No phase inversion with overdriven input signals
- Internal hysteresis
- Propagation delay of 0.8  $\mu\text{s}$  (typical)
- ESD protection:
  - ◆ HBM JESD22-A114F Class 1C exceeds 1500 V
  - ◆ CDM JESD22-C101E exceeds 1000 V
- Multiple package options
- Specified from  $-40\text{ }^{\circ}\text{C}$  to  $+85\text{ }^{\circ}\text{C}$

## 3. Applications

---

- Cellular telephones
- Alarm and security systems
- Personal Digital assistants



4. Ordering information

Table 1. Ordering information

Type number	Package			Version
	Temperature range	Name	Description	
NCX2202GW	−40 °C to +85 °C	TSSOP5	plastic thin shrink small outline package; 5 leads; body width 1.25 mm	SOT353-1
NCX2202GM	−40 °C to +85 °C	XSON6	plastic extremely thin small outline package; no leads; 6 terminals; body 1 × 1.45 × 0.5 mm	SOT886

5. Marking

Table 2. Marking codes

Type number	Marking <sup>[1]</sup>
NCX2202GW	qa
NCX2202GM	qa

[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

6. Functional diagram

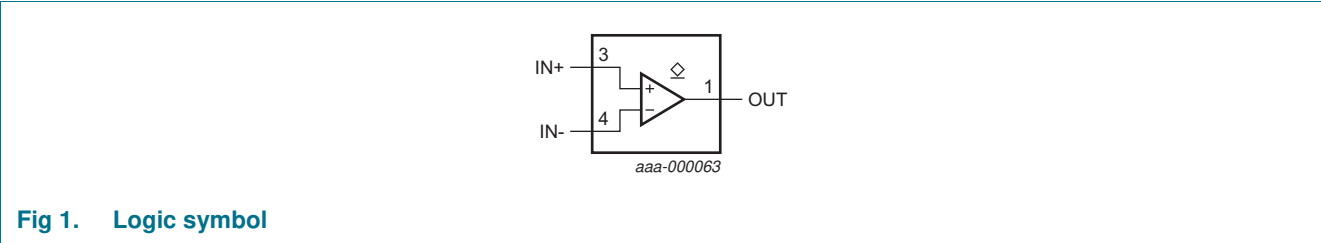
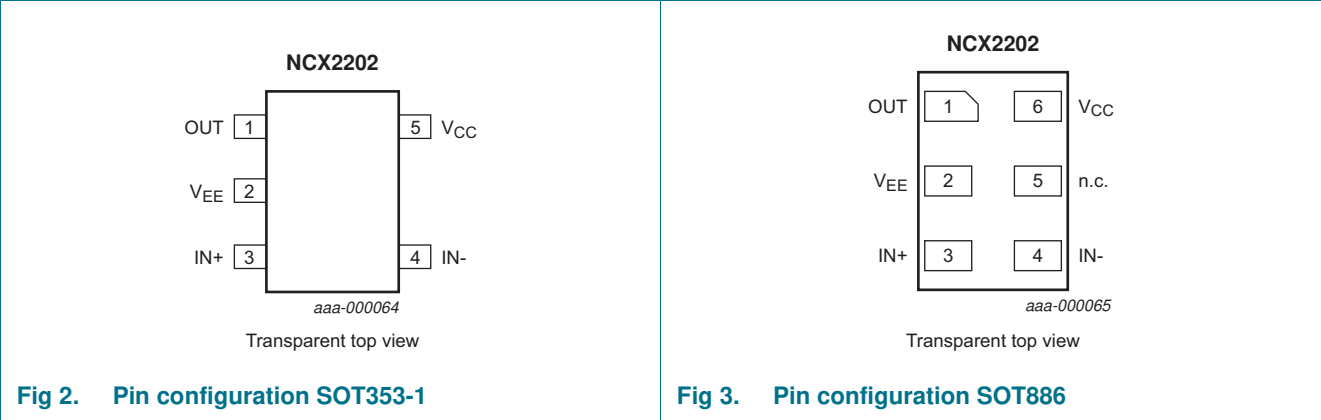


Fig 1. Logic symbol

7. Pinning information

7.1 Pinning



## 7.2 Pin description

**Table 3.** Pin description

Symbol	Pin		Description
	SOT353-1	SOT886	
OUT	1	1	comparator output (open-drain)
V <sub>EE</sub>	2	2	supply voltage
IN+	3	3	comparator input (positive)
IN–	4	4	comparator input (negative)
n.c.	-	5	not connected
V <sub>CC</sub>	5	6	supply voltage

## 8. Limiting values

**Table 4.** Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to V<sub>EE</sub>.

Symbol	Parameter	Conditions	Min	Max	Unit
V <sub>CC</sub>	supply voltage		-	7.0	V
V <sub>I</sub>	input voltage	IN–, IN+ inputs	–0.5	V <sub>CC</sub> + 0.5	V
V <sub>O</sub>	output voltage		V <sub>EE</sub> – 0.5	7.0	V
t <sub>sc(o)</sub>	output short-circuit time	[1]	-	indefinite	s
T <sub>j(max)</sub>	maximum junction temperature		-	+150	°C
T <sub>stg</sub>	storage temperature		–65	+150	°C
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = –40 °C to +85 °C	-	250	mW

[1] The maximum total power dissipation must not be exceeded.

## 9. Recommended operating conditions

**Table 5.** Recommended operating conditions

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V <sub>CC</sub>	supply voltage	V <sub>CC</sub> to V <sub>EE</sub>				
		full spec operating range	1.6	-	5.5	V
		functional operating range	1.3	-	5.5	V
V <sub>I</sub>	input voltage		V <sub>EE</sub>	-	V <sub>CC</sub>	V
V <sub>O</sub>	output voltage		V <sub>EE</sub>	-	5.5	V
T <sub>amb</sub>	ambient temperature		–40	-	+85	°C

## 10. Static characteristics

**Table 6. Static characteristics**

At recommended operating conditions.  $V_{CC} = 1.6\text{ V to }5.5\text{ V}$ ,  $V_{EE} = 0\text{ V}$ ;  $V_{CM} = 0.5V_{CC}$  unless otherwise specified.

Symbol	Parameter	Conditions	25 °C			–40 °C to +85 °C		Unit
			Min	Typ	Max	Min	Max	
$V_H$	hysteresis voltage		6	9	13	-	-	mV
		$V_{CC} = 1.3\text{ V}$	-	20	-	-	-	mV
$V_{I(\text{offset})}$	offset input voltage		[1] -30	0.5	+30	-30	+30	mV
		$V_{CC} = 1.3\text{ V}$	[1] -	3	-	-	-	mV
$V_{OL}$	LOW-level output voltage	$I_O = 0.5\text{ mA}$ ; $V_{CC} = 1.3\text{ V}$	-	0.05	-	-	-	V
		$I_O = 0.5\text{ mA}$ ; $V_{CC} = 1.6\text{ V}$	-	0.04	-	-	0.25	V
		$I_O = 3\text{ mA}$ ; $V_{CC} = 3.0\text{ V}$	-	0.14	-	-	0.3	V
		$I_O = 5\text{ mA}$ ; $V_{CC} = 5.5\text{ V}$	-	0.20	-	-	0.3	V
$I_{OZ}$	OFF-state output current	$I_{N-} = V_{EE}$ ; $I_{N+} = V_{CC}$ ; $V_O = 5.5\text{ V}$	-	3	-	-	-	nA
$V_{CM}$	common-mode voltage	$V_{CC} = 1.3\text{ V to }5.5\text{ V}$	-	$V_{EE}\text{ to }V_{CC}$	-	-	-	V
$I_{OS}$	output short-circuit current	$V_{CC} = 5.5\text{ V}$ ; $V_O = V_{CC}$	-	68	-	-	-	mA
CMRR	common-mode rejection ratio	$\Delta V_{CM} = V_{CC}$	-	70	-	-	-	dB
PSRR	power supply rejection ratio	$\Delta V_{CC} = 1.95\text{ V}$	45	80	-	-	-	dB
$I_{IB}$	input bias current		-	1.0	-	-	-	pA
$I_{CC}$	supply current		-	6.0	-	-	9.0	$\mu\text{A}$

[1] Differential input switching level is guaranteed at the minimum or maximum offset voltage, minus or plus half the maximum hysteresis voltage.



11. Dynamic characteristics

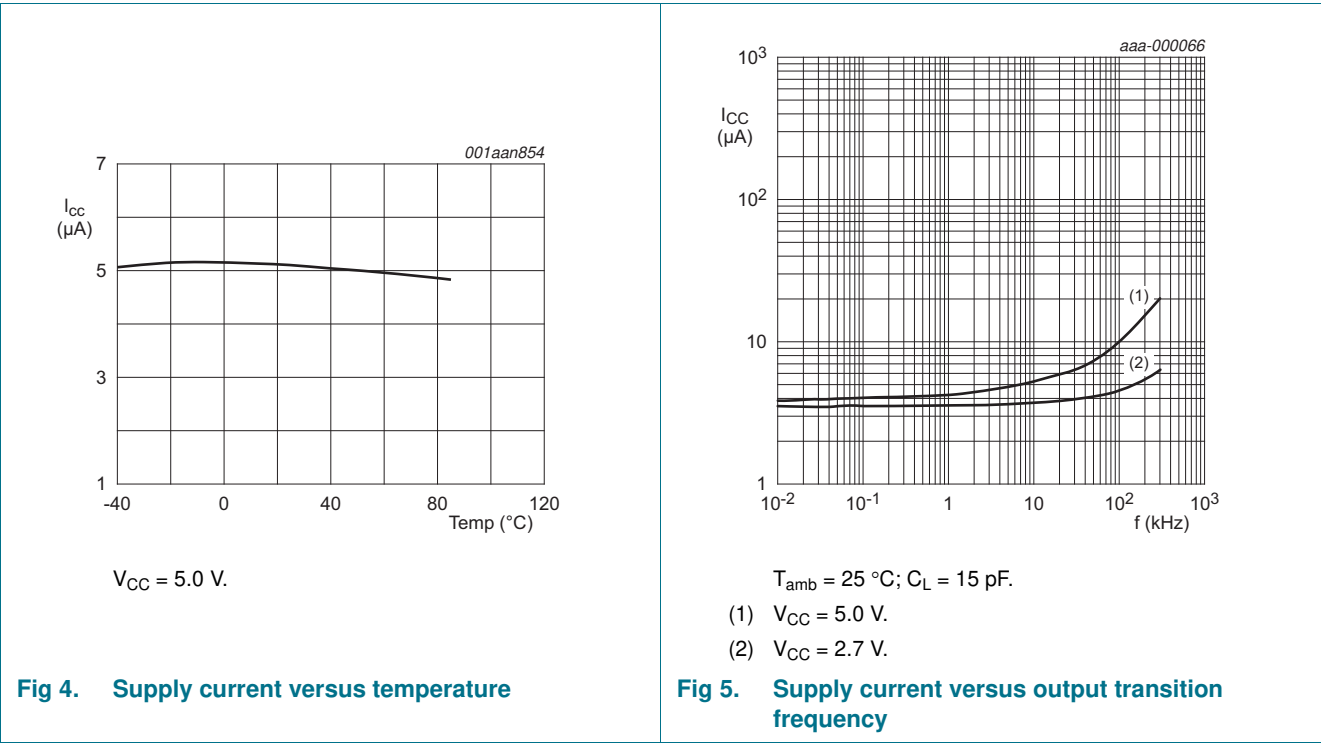
Table 7. Dynamic characteristics

Voltages are referenced to  $V_{EE}$  ( $V_{EE} = 0\text{ V}$ );  $V_{CC} = 1.6\text{ V to }5.5\text{ V}$ ;  $V_{CM} = 0.5V_{CC}$  unless otherwise specified.

Symbol	Parameter	Conditions	25 °C			Unit	
			Min	Typ	Max		
t <sub>pd</sub>	propagation delay	20 mV overdrive; C <sub>L</sub> = 15 pF	[1]	-	0.8	-	μs
t <sub>t</sub>	transition time	HIGH to LOW; V <sub>CC</sub> = 5.5 V; C <sub>L</sub> = 50 pF	[2]	-	10	-	ns

- [1]  $t_{pd}$  is the same as  $t_{PLZ}$  and  $t_{PZL}$ ;  $t_{PLZ}$  is the time that the output is disabled.  
[2] Input signal: 1 kHz, square wave signal with 10 ns edge rate.

12. Graphs



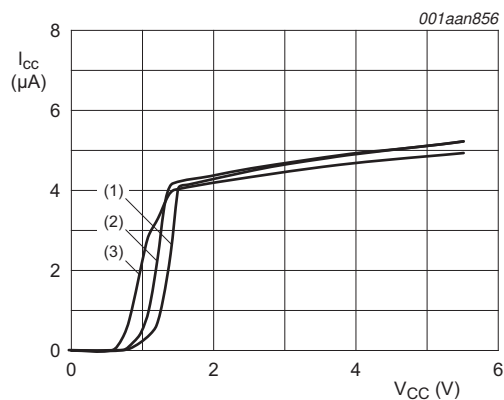


Fig 6. Supply current versus supply voltage

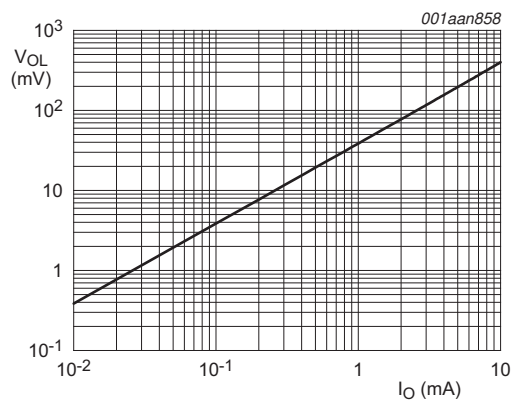


Fig 7. LOW-level output voltage versus output current

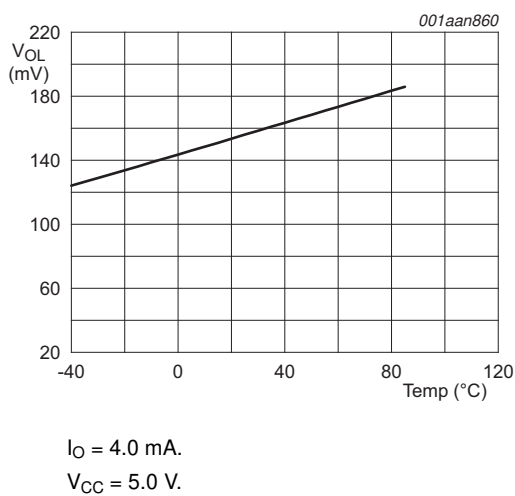


Fig 8. LOW-level output voltage versus temperature

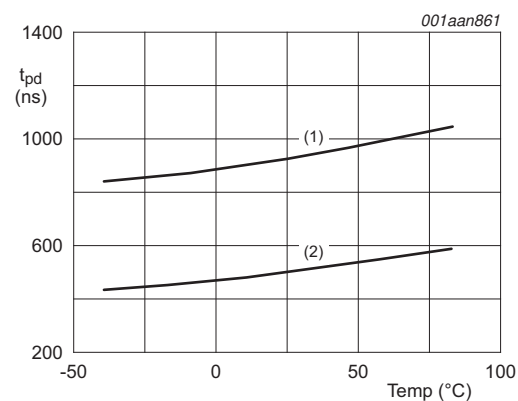
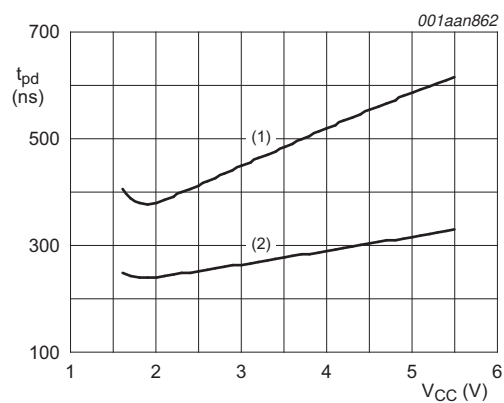


Fig 9. Propagation delay versus temperature



$T_{\text{amb}} = 25\text{ }^{\circ}\text{C}$ ; input overdrive = 100 mV.

(1)  $t_{\text{PLZ}}$ .

(2)  $t_{\text{PZL}}$ .

**Fig 10. Propagation delay versus supply voltage.**



## 13. Application information

### 13.1 Operating description

The NCX2202 is a single low voltage, low power, comparator with open-drain output. This device is designed for use with a pull-up resistor to define the output switching levels. This device consumes only 6  $\mu\text{A}$  of supply current while achieving a typical propagation delay of 0.8  $\mu\text{s}$  at a 20 mV input overdrive. [Figure 9](#) and [Figure 10](#) show propagation delay with various input overdrives. This comparator is guaranteed to operate at a low voltage of 1.3 V up to 5.5 V. The common-mode input voltage range extends 0.1 V beyond the upper and lower rail without phase inversion or other adverse effects. This device has a typical internal hysteresis of 9.0 mV. This allows for greater noise immunity and clean output switching.

### 13.2 Output stage

The NCX2202 has an N-channel output stage that has capability of sinking the output to  $V_{EE}$  with a load ranging up to 5.0 mA. See [Figure 11](#)

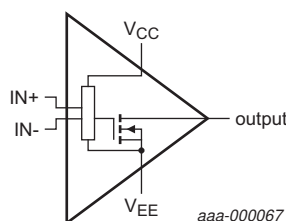


Fig 11. NCX2202 output configuration

### 13.3 Zero-crossing detector

[Figure 12](#) shows the NCX2202 configured as a zero-crossing detector.

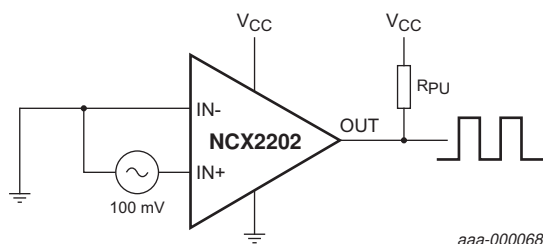
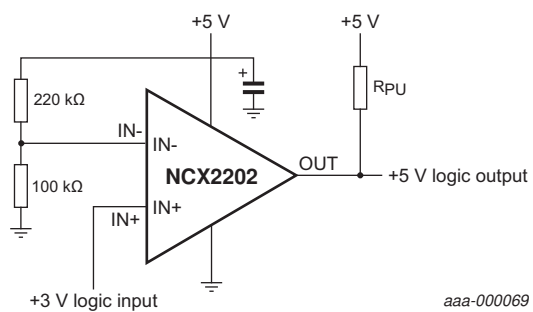


Fig 12. Zero-crossing detector

### 13.4 Logic level translator

[Figure 13](#) shows the NCX2202 configured as a logic level translator.



This circuit converts 3 V logic to 5 V logic.

**Fig 13. Logic level translator**

# 14. Package outline

TSSOP5: plastic thin shrink small outline package; 5 leads; body width 1.25 mm

SOT353-1

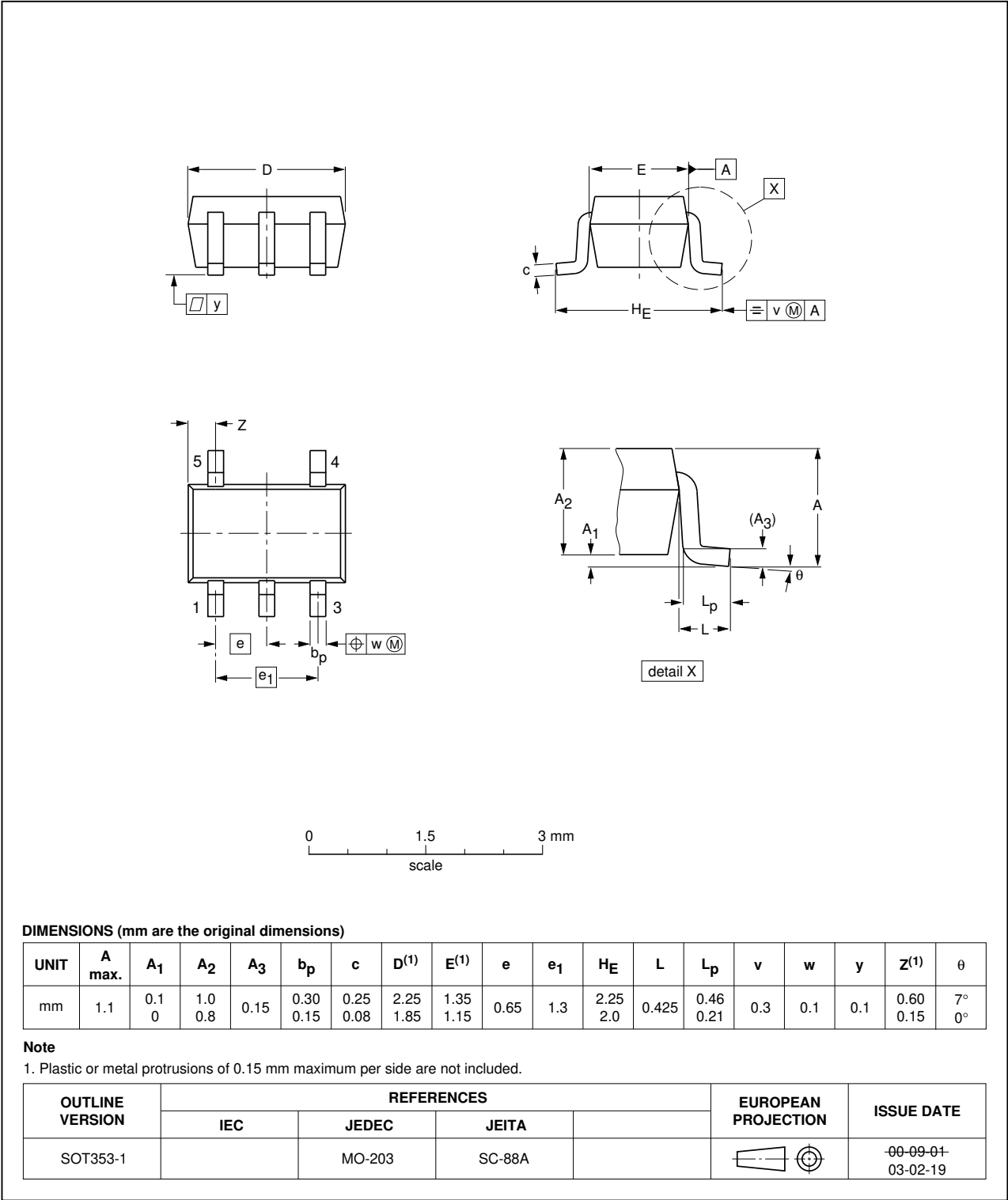


Fig 14. Package outline SOT353-1 (TSSOP5)

XSON6: plastic extremely thin small outline package; no leads; 6 terminals; body 1 x 1.45 x 0.5 mm

SOT886

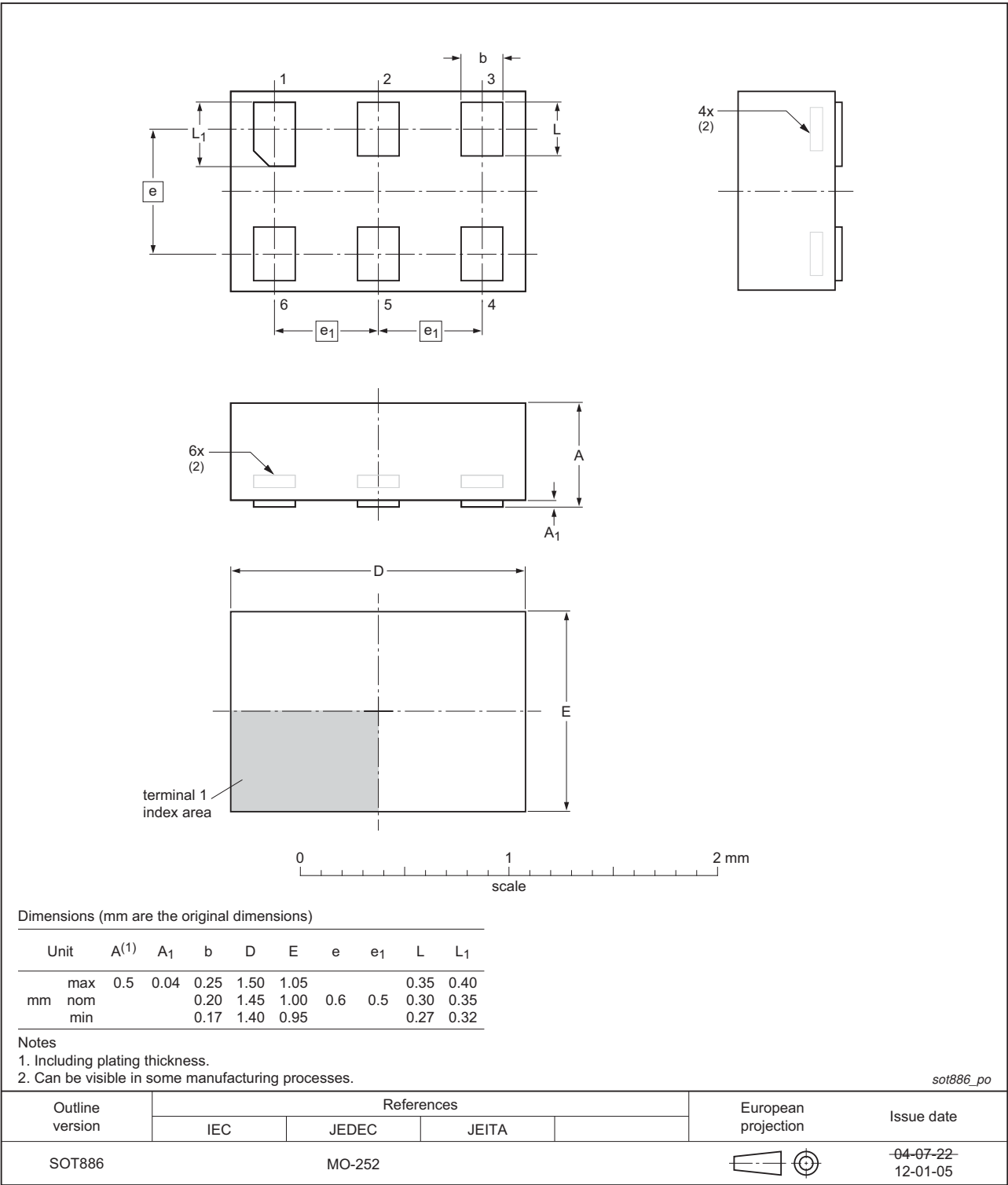


Fig 15. Package outline SOT886 (XSON6)

## 15. Abbreviations

Table 8. Abbreviations

Acronym	Description
CDM	Charged Device Model
ESD	ElectroStatic Discharge
HBM	Human Body Model

## 16. Revision history

Table 9. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
NCX2202 v.5	20121030	Product data sheet	-	NCX2202 v.4
Modifications:	• Class 3A changed into Class 1C (errata) in <a href="#">Section 2</a> .			
NCX2202 v.4	20120806	Product data sheet	-	NCX2202 v.3
Modifications:	• Package outline drawing of SOT886 ( <a href="#">Figure 15</a> ) modified.			
NCX2202 v.3	20111110	Product data sheet	-	NCX2202 v.2
Modifications:	• Legal pages updated.			
NCX2202 v.2	20111020	Product data sheet	-	NCX2202 v.1
NCX2202 v.1	20110720	Product data sheet	-	-

## 17. Legal information

### 17.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the Internet at URL <http://www.nxp.com>.

### 17.2 Definitions

**Draft** — The document is a draft version only. The content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included herein and shall have no liability for the consequences of use of such information.

**Short data sheet** — A short data sheet is an extract from a full data sheet with the same product type number(s) and title. A short data sheet is intended for quick reference only and should not be relied upon to contain detailed and full information. For detailed and full information see the relevant full data sheet, which is available on request via the local NXP Semiconductors sales office. In case of any inconsistency or conflict with the short data sheet, the full data sheet shall prevail.

**Product specification** — The information and data provided in a Product data sheet shall define the specification of the product as agreed between NXP Semiconductors and its customer, unless NXP Semiconductors and customer have explicitly agreed otherwise in writing. In no event however, shall an agreement be valid in which the NXP Semiconductors product is deemed to offer functions and qualities beyond those described in the Product data sheet.

### 17.3 Disclaimers

**Limited warranty and liability** — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential damages (including - without limitation - lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the *Terms and conditions of commercial sale* of NXP Semiconductors.

**Right to make changes** — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

**Suitability for use** — NXP Semiconductors products are not designed, authorized or warranted to be suitable for use in life support, life-critical or safety-critical systems or equipment, nor in applications where failure or malfunction of an NXP Semiconductors product can reasonably be expected to result in personal injury, death or severe property or environmental damage. NXP Semiconductors and its suppliers accept no liability for inclusion and/or use of NXP Semiconductors products in such equipment or applications and therefore such inclusion and/or use is at the customer's own risk.

**Applications** — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification.

Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

**Limiting values** — Stress above one or more limiting values (as defined in the Absolute Maximum Ratings System of IEC 60134) will cause permanent damage to the device. Limiting values are stress ratings only and (proper) operation of the device at these or any other conditions above those given in the Recommended operating conditions section (if present) or the Characteristics sections of this document is not warranted. Constant or repeated exposure to limiting values will permanently and irreversibly affect the quality and reliability of the device.

**Terms and conditions of commercial sale** — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

**No offer to sell or license** — Nothing in this document may be interpreted or construed as an offer to sell products that is open for acceptance or the grant, conveyance or implication of any license under any copyrights, patents or other industrial or intellectual property rights.



**Export control** — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

**Non-automotive qualified products** — Unless this data sheet expressly states that this specific NXP Semiconductors product is automotive qualified, the product is not suitable for automotive use. It is neither qualified nor tested in accordance with automotive testing or application requirements. NXP Semiconductors accepts no liability for inclusion and/or use of non-automotive qualified products in automotive equipment or applications.

In the event that customer uses the product for design-in and use in automotive applications to automotive specifications and standards, customer (a) shall use the product without NXP Semiconductors' warranty of the product for such automotive applications, use and specifications, and (b) whenever customer uses the product for automotive applications beyond

NXP Semiconductors' specifications such use shall be solely at customer's own risk, and (c) customer fully indemnifies NXP Semiconductors for any liability, damages or failed product claims resulting from customer design and use of the product for automotive applications beyond NXP Semiconductors' standard warranty and NXP Semiconductors' product specifications.

**Translations** — A non-English (translated) version of a document is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

## 17.4 Trademarks

Notice: All referenced brands, product names, service names and trademarks are the property of their respective owners.

## 18. Contact information

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

## 19. Contents

<b>1</b>	<b>General description</b> .....	<b>1</b>
<b>2</b>	<b>Features and benefits</b> .....	<b>1</b>
<b>3</b>	<b>Applications</b> .....	<b>1</b>
<b>4</b>	<b>Ordering information</b> .....	<b>2</b>
<b>5</b>	<b>Marking</b> .....	<b>2</b>
<b>6</b>	<b>Functional diagram</b> .....	<b>2</b>
<b>7</b>	<b>Pinning information</b> .....	<b>2</b>
7.1	Pinning .....	2
7.2	Pin description .....	3
<b>8</b>	<b>Limiting values</b> .....	<b>3</b>
<b>9</b>	<b>Recommended operating conditions</b> .....	<b>3</b>
<b>10</b>	<b>Static characteristics</b> .....	<b>4</b>
<b>11</b>	<b>Dynamic characteristics</b> .....	<b>5</b>
<b>12</b>	<b>Graphs</b> .....	<b>5</b>
<b>13</b>	<b>Application information</b> .....	<b>8</b>
13.1	Operating description .....	8
13.2	Output stage .....	8
13.3	Zero-crossing detector .....	8
13.4	Logic level translator .....	8
<b>14</b>	<b>Package outline</b> .....	<b>10</b>
<b>15</b>	<b>Abbreviations</b> .....	<b>12</b>
<b>16</b>	<b>Revision history</b> .....	<b>12</b>
<b>17</b>	<b>Legal information</b> .....	<b>13</b>
17.1	Data sheet status .....	13
17.2	Definitions .....	13
17.3	Disclaimers .....	13
17.4	Trademarks .....	14
<b>18</b>	<b>Contact information</b> .....	<b>14</b>
<b>19</b>	<b>Contents</b> .....	<b>15</b>

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© NXP B.V. 2012.

All rights reserved.

For more information, please visit: <http://www.nxp.com>

For sales office addresses, please send an email to: [salesaddresses@nxp.com](mailto:salesaddresses@nxp.com)

Date of release: 30 October 2012

Document identifier: NCX2202