# imall

Chipsmall Limited consists of a professional team with an average of over 10 year of expertise in the distribution of electronic components. Based in Hongkong, we have already established firm and mutual-benefit business relationships with customers from, Europe, America and south Asia, supplying obsolete and hard-to-find components to meet their specific needs.

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## **Ignition IGBT** 20 A, 350 V, N–Channel D<sup>2</sup>PAK

This Logic Level Insulated Gate Bipolar Transistor (IGBT) features monolithic circuitry integrating ESD and Overvoltage clamped protection for use in inductive coil drivers applications. Primary uses include Ignition, Direct Fuel Injection, or wherever high voltage and high current switching is required.

### Features

- Ideal for Coil-on-Plug and Driver-on-Coil Applications
- Gate–Emitter ESD Protection
- Temperature Compensated Gate–Collector Voltage Clamp Limits Stress Applied to Load
- Integrated ESD Diode Protection
- Low Threshold Voltage for Interfacing Power Loads to Logic or Microprocessor Devices
- Low Saturation Voltage
- High Pulsed Current Capability
- These are Pb-Free Devices

### Applications

• Ignition Systems

**MAXIMUM RATINGS** (T<sub>J</sub> =  $25^{\circ}$ C unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V <sub>CES</sub>	390	V
Collector-Gate Voltage	V <sub>CER</sub>	390	V
Gate-Emitter Voltage	V <sub>GE</sub>	±15	V
Collector Current–Continuous @ $T_C = 25^{\circ}C - Pulsed$	Ι <sub>C</sub>	20 50	A <sub>DC</sub> A <sub>AC</sub>
Continuous Gate Current	۱ <sub>G</sub>	1.0	mA
Transient Gate Current (t $\leq$ 2 ms, f $\leq$ 100 Hz)	I <sub>G</sub>	20	mA
ESD (Charged-Device Model)	ESD	2.0	kV
ESD (Human Body Model) R = 1500 $\Omega$ , C = 100 pF	ESD	8.0	kV
ESD (Machine Model) R = 0 $\Omega$ , C = 200 pF	ESD	500	V
Total Power Dissipation @ T <sub>C</sub> = 25°C Derate above 25°C	PD	150 1.0	W W/°C
Operating & Storage Temperature Range	T <sub>J</sub> , T <sub>stg</sub>	–55 to +175	°C

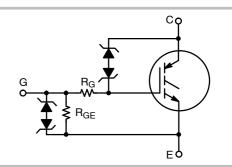
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

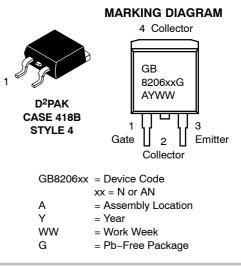


### **ON Semiconductor®**

http://onsemi.com

20 AMPS, 350 VOLTS V<sub>CE(on)</sub> = 1.3 V @ I<sub>C</sub> = 10 A, V<sub>GE</sub> ≥ 4.5 V





### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 7 of this data sheet.

### UNCLAMPED COLLECTOR-TO-EMITTER AVALANCHE CHARACTERISTICS (-55° $\leq T_J \leq 175^{\circ}C)$

Characteristic	Symbol	Value	Unit
$ \begin{array}{l} \mbox{Single Pulse Collector-to-Emitter Avalanche Energy} \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 16.7 \mbox{ A, } L = 1.8 \mbox{ mH}, \mbox{ R}_g = 1 \mbox{ k}\Omega \mbox{ Starting } T_J = 25^\circ C \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 14.9 \mbox{ A, } L = 1.8 \mbox{ mH}, \mbox{ R}_g = 1 \mbox{ k}\Omega \mbox{ Starting } T_J = 150^\circ C \\ V_{CC} = 50 \mbox{ V, } V_{GE} = 5.0 \mbox{ V, Pk } I_L = 14.1 \mbox{ A, } L = 1.8 \mbox{ mH}, \mbox{ R}_g = 1 \mbox{ k}\Omega \mbox{ Starting } T_J = 175^\circ C \\ \end{array} $	E <sub>AS</sub>	250 200 180	mJ
Reverse Avalanche Energy V <sub>CC</sub> = 100 V, V <sub>GE</sub> = 20 V, Pk I <sub>L</sub> = 25.8 A, L = 6.0 mH, Starting T <sub>J</sub> = 25°C	E <sub>AS(R)</sub>	2000	mJ

#### THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.0	°C/W
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	62.5	°C/W
Maximum Temperature for Soldering Purposes, 0.125 in from case for 5 seconds (Note 2)	ΤL	275	°C

When surface mounted to an FR4 board using the minimum recommended pad size.
For further details, see Soldering and Mounting Techniques Reference Manual: SOLDERRM/D.

#### **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Test Conditions	Temperature	Min	Тур	Max	Unit
OFF CHARACTERISTICS			-				
Collector-Emitter Clamp Voltage	BV <sub>CES</sub>	I <sub>C</sub> = 2.0 mA	$T_J = -40^{\circ}C$ to $175^{\circ}C$	325	350	375	V
	İ	I <sub>C</sub> = 10 mA	$T_J = -40^{\circ}C$ to $175^{\circ}C$	340	365	390	
Zero Gate Voltage Collector Current	ICES	V <sub>CE</sub> = 15 V, V <sub>GE</sub> = 0 V	$T_J = 25^{\circ}C$		0.1	1.0	μΑ
			$T_{\rm J} = 25^{\circ} \rm C$	0.5	1.5	10	
		V <sub>CE</sub> = 175 V, V <sub>GE</sub> = 0 V	$T_J = 175^{\circ}C$	1.0	25	100*	
			$T_J = -40^{\circ}C$	0.4	0.8	5.0	
Reverse Collector-Emitter Clamp	B <sub>VCES(R)</sub>		$T_{\rm J} = 25^{\circ} \rm C$	30	35	39	V
Voltage		I <sub>C</sub> = –75 mA – NGB8206	T <sub>J</sub> = 175°C	35	39	45*	
	NGD0200	$T_J = -40^{\circ}C$	30	33	37		
			$T_J = 25^{\circ}C$	30	35	39	
		I <sub>C</sub> = -75 mA - NGB8206A	$T_J = 175^{\circ}C$	32	37	42	
			$T_J = -40^{\circ}C$	29	32	37	
Reverse Collector-Emitter Leakage Cur-	I <sub>CES(R)</sub>		$T_{\rm J} = 25^{\circ} \rm C$	0.05	0.25	0.5	mA
rent		V <sub>CE</sub> = -24 V - NGB8206	T <sub>J</sub> = 175°C	1.0	12.5	25	
		NGBOLOG	$T_J = -40^{\circ}C$	0.005	0.03	0.25	
			T <sub>J</sub> = 25°C	0.05	0.25	1.0	
		V <sub>CE</sub> = -24 V - NGB8206A	T <sub>J</sub> = 175°C	1.0	12.5	25	
			$T_J = -40^{\circ}C$	0.005	0.03	0.25	
Gate-Emitter Clamp Voltage	BV <sub>GES</sub>	$I_{G} = \pm 5.0 \text{ mA}$	$T_J = -40^{\circ}C$ to $175^{\circ}C$	12	12.5	14	V
Gate-Emitter Leakage Current	I <sub>GES</sub>	$V_{GE}$ = ±5.0 V	$T_J = -40^{\circ}C$ to $175^{\circ}C$	200	300	350*	μΑ
Gate Resistor	R <sub>G</sub>		$T_J = -40^{\circ}C$ to $175^{\circ}C$		70		Ω
Gate-Emitter Resistor	R <sub>GE</sub>		$T_J = -40^{\circ}C$ to $175^{\circ}C$	14.25	16	25	kΩ

**ON CHARACTERISTICS** (Note 3)

Gate Threshold Voltage	V <sub>GE(th)</sub>		$T_{\rm J} = 25^{\circ}C$	1.5	1.8	2.1	V
		I <sub>C</sub> = 1.0 mA, V <sub>GE</sub> = V <sub>CE</sub>	T <sub>J</sub> = 175°C	0.7	1.0	1.3	
			$T_J = -40^{\circ}C$	1.7	2.0	2.3*	

\*Maximum Value of Characteristic across Temperature Range. 3. Pulse Test: Pulse Width  $\leq$  300  $\mu S,$  Duty Cycle  $\leq$  2%.

### **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Test Conditions	Temperature	Min	Тур	Max	Unit
ON CHARACTERISTICS (Note 3)							
Threshold Temperature Coefficient (Neg- ative)				3.8	4.6	6.0	mV/°C
Collector-to-Emitter On-Voltage	V <sub>CE(on)</sub>		T <sub>J</sub> = 25°C	0.95	1.15	1.35	V
		I <sub>C</sub> = 6.5 A, V <sub>GE</sub> = 3.7 V	T <sub>J</sub> = 175°C	0.70	0.95	1.15	
		•GE = 0.7 •	$T_J = -40^{\circ}C$	1.0	1.30	1.40	
			$T_J = 25^{\circ}C$	0.95	1.25	1.45	-
		I <sub>C</sub> = 9.0 A, V <sub>GE</sub> = 3.9 V	T <sub>J</sub> = 175°C	0.8	1.05	1.25	-
	$I_{C} = 7.5 \text{ A},$ $V_{GE} = 4.5 \text{ V}$ $I_{C} = 10 \text{ A},$ $V_{GE} = 4.5 \text{ V}$		$T_J = -40^{\circ}C$	1.1	1.4	1.50	-
			$T_J = 25^{\circ}C$	0.85	1.15	1.4	
			T <sub>J</sub> = 175°C	0.7	0.95	1.2	
		$T_J = -40^{\circ}C$	1.0	1.3	1.6*		
		$T_J = 25^{\circ}C$	1.0	1.3	1.6		
		$V_{GE} = 4.5 V$ NGB8206	T <sub>J</sub> = 175°C	0.8	1.05	1.4	-
			$T_J = -40^{\circ}C$	1.1	1.4	1.7*	
			$T_J = 25^{\circ}C$	0.9	1.2	1.6	
		V <sub>GE</sub> = 4.5 V	T <sub>J</sub> = 175°C	0.8	1.05	1.4	
		NGB8206A	$T_J = -40^{\circ}C$	1.0	1.2	1.7*	
		L- 15 A	$T_J = 25^{\circ}C$	1.15	1.45	1.7	
		I <sub>C</sub> = 15 A, V <sub>GE</sub> = 4.5 V	T <sub>J</sub> = 175°C	1.0	1.3	1.55	
		NGB8206	$T_J = -40^{\circ}C$	1.25	1.55	1.8*	
		I <sub>C</sub> = 15 A,	$T_J = 25^{\circ}C$	1.0	1.3	1.7	
		V <sub>GE</sub> = 4.5 V	T <sub>J</sub> = 175°C	1.0	1.3	1.55	
		NGB8206A	$T_J = -40^{\circ}C$	1.1	1.35	1.8*	
			$T_J = 25^{\circ}C$	1.3	1.6	1.9	]
		I <sub>C</sub> = 20 A, V <sub>GE</sub> = 4.5 V	T <sub>J</sub> = 175°C	1.2	1.5	1.8	]
			$T_J = -40^{\circ}C$	1.4	1.75	2.0*	]
Forward Transconductance	gfs	I <sub>C</sub> = 6.0 A, V <sub>CE</sub> = 5.0 V	$T_J = 25^{\circ}C$	10	18	25	Mhos

### DYNAMIC CHARACTERISTICS

Input Capacitance	C <sub>ISS</sub>			1100	1300	1500	pF
Output Capacitance	C <sub>OSS</sub>	f = 10 kHz, $V_{CE}$ = 25 V	$T_J = 25^{\circ}C$	70	80	90	
Transfer Capacitance	C <sub>RSS</sub>			18	20	22	

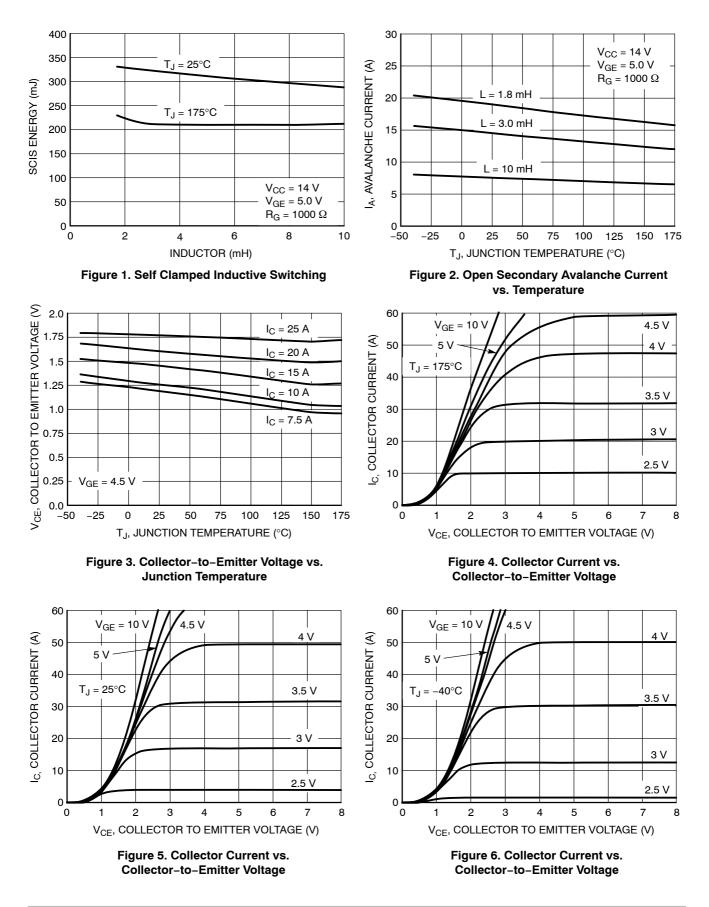
\*Maximum Value of Characteristic across Temperature Range. 3. Pulse Test: Pulse Width  $\leq$  300  $\mu$ S, Duty Cycle  $\leq$  2%.

### **ELECTRICAL CHARACTERISTICS**

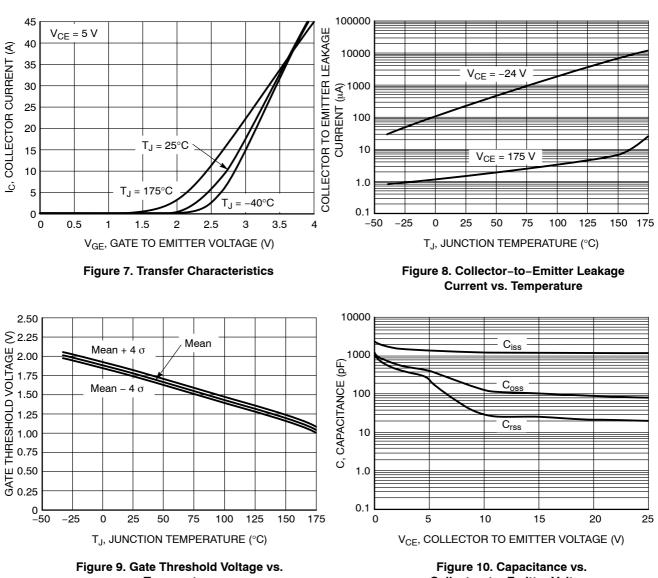
Characteristic	Symbol	Test Conditions	Temperature	Min	Тур	Max	Unit
SWITCHING CHARACTERISTICS	•				•		
Turn-Off Delay Time (Resistive)	t <sub>d(off)</sub>	V <sub>CC</sub> = 300 V, I <sub>C</sub> = 9.0	$T_J = 25^{\circ}C$	6.0	8.0	10	μSec
		A	$T_{\rm J} = 175^{\circ}C$	6.0	8.0	10	
Fall Time (Resistive)	t <sub>f</sub>	R <sub>G</sub> = 1.0 kΩ, R <sub>L</sub> = 33 Ω	$T_J = 25^{\circ}C$	4.0	6.0	8.0	
		V <sub>GE</sub> = 5 V	$T_{\rm J}$ = 175°C	8.0	10.5	14	
Turn-Off Delay Time (Inductive)	t <sub>d(off)</sub>	V <sub>CC</sub> = 300 V, I <sub>C</sub> = 9.0	$T_J = 25^{\circ}C$	3.0	5.0	7.0	
		A	$T_{\rm J}$ = 175°C	5.0	7.0	9.0	
Fall Time (Inductive)	t <sub>f</sub>	R <sub>G</sub> = 1.0 kΩ, L = 300 μH V <sub>GE</sub> = 5 V	$T_J = 25^{\circ}C$	1.5	3.0	4.5	
		V <sub>GE</sub> = 5 V	$T_J = 175^{\circ}C$	5.0	7.0	10	
Turn-On Delay Time	t <sub>d(on)</sub>		$T_J = 25^{\circ}C$	1.0	1.5	2.0	
		$V_{CC}$ = 14 V, I <sub>C</sub> = 9.0 A R <sub>G</sub> = 1.0 kΩ, R <sub>L</sub> = 1.5	$T_{\rm J}$ = 175°C	1.0	1.5	2.0	
Rise Time	t <sub>r</sub>	$\Omega$ V <sub>GE</sub> = 5 V	$T_J = 25^{\circ}C$	4.0	6.0	8.0	
		VGE - 5 V	$T_{\rm J} = 175^{\circ}C$	3.0	5.0	7.0	1

\*Maximum Value of Characteristic across Temperature Range. 3. Pulse Test: Pulse Width  $\leq$  300  $\mu$ S, Duty Cycle  $\leq$  2%.

### **TYPICAL ELECTRICAL CHARACTERISTICS**



### **TYPICAL ELECTRICAL CHARACTERISTICS**



Temperature

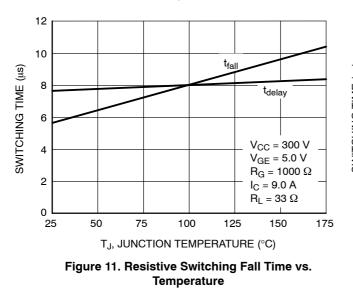
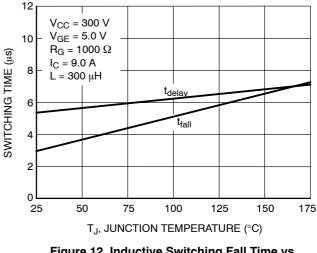
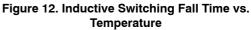


Figure 10. Capacitance vs. Collector-to-Emitter Voltage





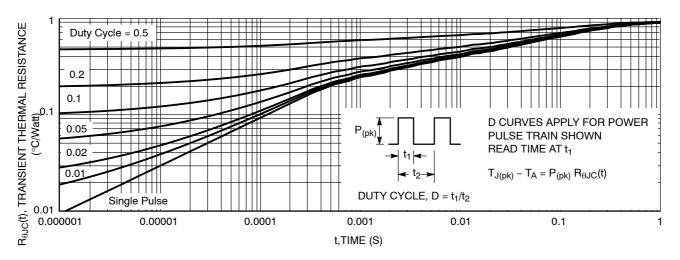


Figure 13. Best Case Transient Thermal Resistance (Non-normalized Junction-to-Case Mounted on Cold Plate)

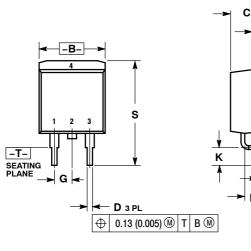
#### **ORDERING INFORMATION**

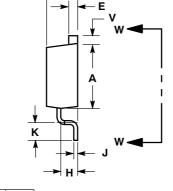
Device	Package	Shipping <sup>†</sup>
NGB8206NG	D <sup>2</sup> PAK (Pb-Free)	50 Units / Rail
NGB8206NT4G	D <sup>2</sup> PAK (Pb-Free)	800 / Tape & Reel
NGB8206ANT4G	D <sup>2</sup> PAK (Pb-Free)	800 / Tape & Reel
NGB8206ANTF4G	D <sup>2</sup> PAK (Pb-Free)	700 / Tape & Reel
NGB8206ANSL3G	D <sup>2</sup> PAK (Pb-Free)	50 Units / Rail

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

#### PACKAGE DIMENSIONS

D<sup>2</sup>PAK 3 CASE 418B-04 **ISSUE K** 





NOTES:

DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

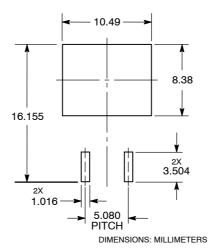
CONTROLLING DIMENSION: INCH. 418B-01 THRU 418B-03 OBSOLETE, 2 З.

NEW STANDARD 418B-04.

	INC	HES	MILLIN	IETERS		
DIM	MIN	MAX	MIN	MAX		
Α	0.340	0.380	8.64	9.65		
в	0.380	0.405	9.65	10.29		
С	0.160	0.190	4.06	4.83		
D	0.020	0.035	0.51	0.89		
Е	0.045	0.055	1.14	1.40		
F	0.310	0.350	7.87	8.89		
G	0.100 BSC		2.54	BSC		
н	0.080	0.110	2.03	2.79		
J	0.018	0.025	0.46	0.64		
κ	0.090	0.110	2.29	2.79		
L	0.052	0.072	1.32	1.83		
М	0.280	0.320	7.11	8.13		
Ν	0.197	' REF	5.00	REF		
Р	0.079	0.079 REF 2.00		0.079 REF 2.00 RI		REF
R	0.039	REF	0.99	REF		
S	0.575	0.625	14.60	15.88		
V	0.045	0.055	1.14	1.40		

U Μ VIEW W-W

#### **SOLDERING FOOTPRINT\***



STYLE 4: PIN 1. GATE

2. COLLECTOR 3. EMITTER

COLLECTOR

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\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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