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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

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IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on-state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications. Incorporated into the device is a rugged co-packaged free wheeling diode with a low forward voltage.

Features

- Low Saturation Voltage using Trench with Fieldstop Technology
- Low Switching Loss Reduces System Power Dissipation
- Low Gate Charge
- 5 µs Short Circuit Capability
- These are Pb-Free Devices

Typical Applications

- Inverter Welding Machines
- Microwave Ovens
- Industrial Switching
- Motor Control Inverter

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V _{CES}	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	lc	40 20	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	200	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	l _F	40 20	Α
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	200	Α
Gate-emitter voltage	V_{GE}	±20	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	192 77	W
Short Circuit Withstand Time $V_{GE} = 15 \text{ V}, V_{CE} = 600 \text{ V}, T_J \le 150^{\circ}\text{C}$	T _{SC}	5	μs
Operating junction temperature range	TJ	–55 to +150	°C
Storage temperature range	T _{stg}	-55 to +150	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

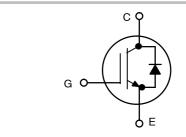
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

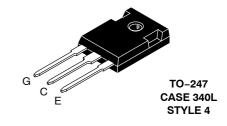


ON Semiconductor®

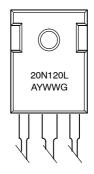
http://onsemi.com

20 A, 1200 V V_{CEsat} = 1.80 V E_{off} = 0.7 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB20N120LWG	TO-247 (Pb-Free)	30 Units / Rail

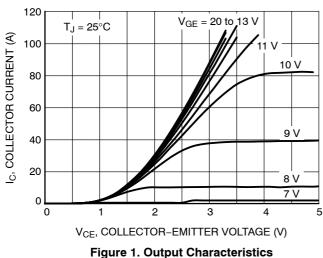
THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ hetaJC}$	0.65	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ hetaJC}$	1.5	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	•					
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	V _{(BR)CES}	1200	=	_	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 20 A V _{GE} = 15 V, I _C = 20 A, T _J = 150°C	V _{CEsat}	-	1.80 2.0	2.2 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 1200 V V _{GE} = 0 V, V _{CE} = 1200 V, T _{J =} 150°C	I _{CES}	<u>-</u> -	- -	0.5 2.0	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	-	_	100	nA
DYNAMIC CHARACTERISTIC						
Input capacitance		C _{ies}	-	4700	_	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{oes}	_	155	-	
Reverse transfer capacitance	1	C _{res}	_	100	-	
Gate charge total		Q_g		200		nC
Gate to emitter charge	$V_{CE} = 600 \text{ V}, I_{C} = 20 \text{ A}, V_{GE} = 15 \text{ V}$	Q _{ge}		36		
Gate to collector charge		Q _{gc}		98		
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t _{d(on)}		86		ns
Rise time		t _r		26		
Turn-off delay time	$T_J = 25^{\circ}C$ $V_{CC} = 600 \text{ V, } I_C = 20 \text{ A}$	t _{d(off)}		235		
Fall time	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V} / 15 \text{ V}$	t _f		180		
Turn-on switching loss	GE - 0 V/ 10 V	E _{on}		3.1		mJ
Turn-off switching loss		E _{off}		0.7		
Turn-on delay time		t _{d(on)}		84		ns
Rise time]	t _r		26		
Turn-off delay time	$T_J = 125^{\circ}\text{C}$ $V_{CC} = 600 \text{ V, } I_C = 20 \text{ A}$ $R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t _{d(off)}		235		
Fall time		t _f		250		
Turn-on switching loss		E _{on}		3.9		mJ
Turn-off switching loss		E _{off}		1.3		
DIODE CHARACTERISTIC			_			
Forward voltage	V _{GE} = 0 V, I _F = 20 A V _{GE} = 0 V, I _F = 20 A, T _J = 150°C	V _F		1.55 1.65	1.75	V

TYPICAL CHARACTERISTICS



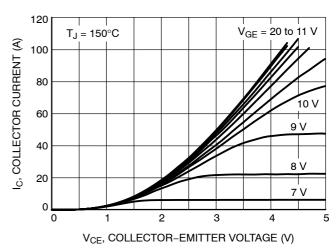


Figure 2. Output Characteristics

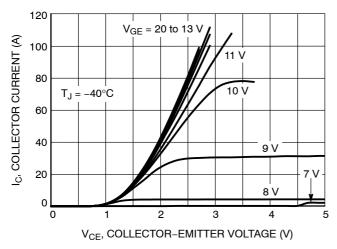


Figure 3. Output Characteristics

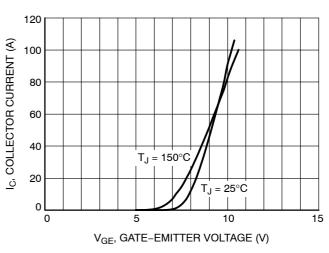


Figure 4. Typical Transfer Characteristics

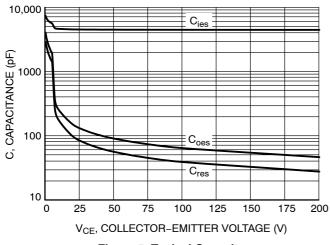


Figure 5. Typical Capacitance

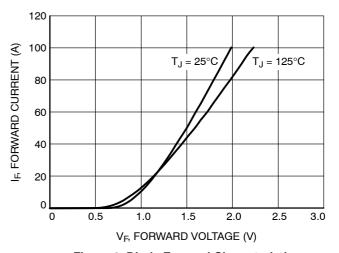


Figure 6. Diode Forward Characteristics

TYPICAL CHARACTERISTICS

SWITCHING ENERGY (mJ)

ENERGY (mJ)

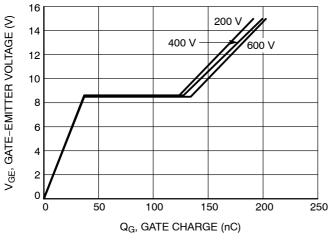


Figure 7. Typical Gate Charge

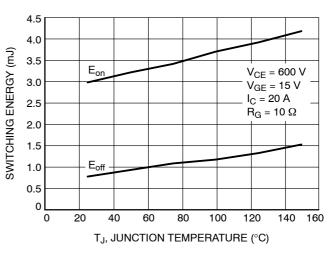


Figure 8. Energy Loss vs. Temperature

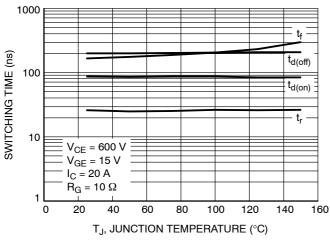


Figure 9. Switching Time vs. Temperature

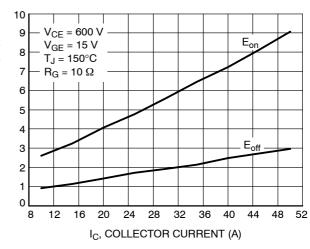


Figure 10. Energy Loss vs. I_C

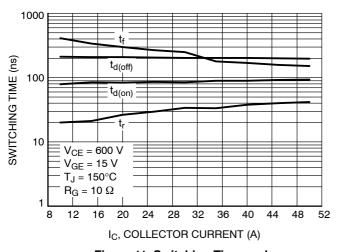


Figure 11. Switching Time vs. I_{C}

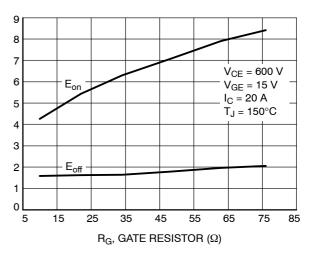


Figure 12. Energy Loss vs. R_G

TYPICAL CHARACTERISTICS

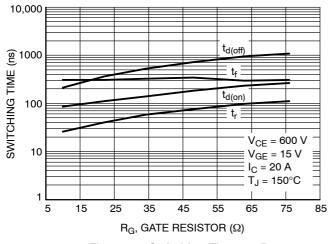


Figure 13. Switching Time vs. $R_{\mbox{\scriptsize G}}$

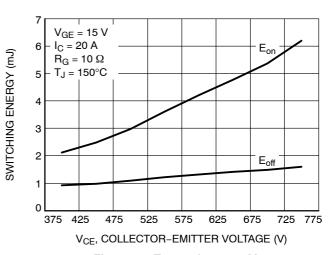


Figure 14. Energy Loss vs. V_{CE}

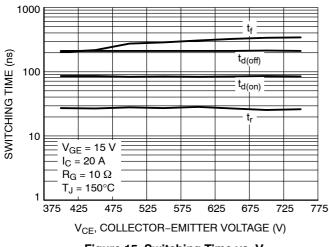


Figure 15. Switching Time vs. V_{CE}

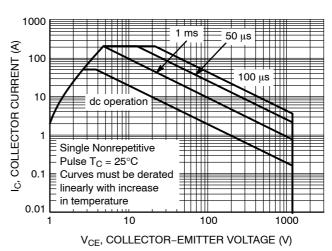


Figure 16. Safe Operating Area

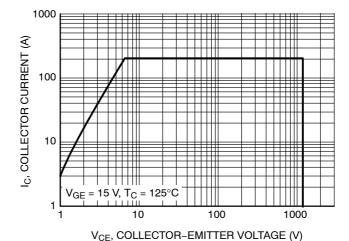


Figure 17. Reverse Bias Safe Operating Area

TYPICAL CHARACTERISTICS

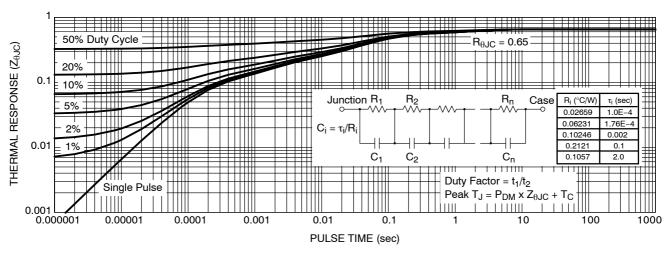


Figure 18. IGBT Transient Thermal Impedance

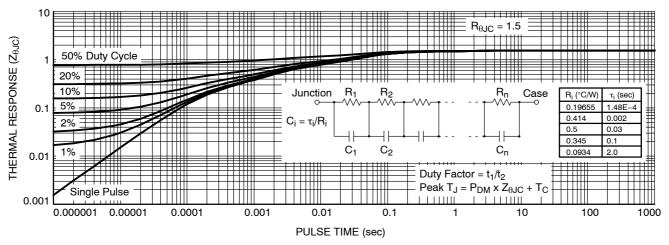


Figure 19. Diode Transient Thermal Impedance

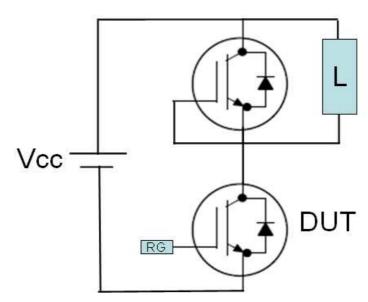


Figure 20. Test Circuit for Switching Characteristics

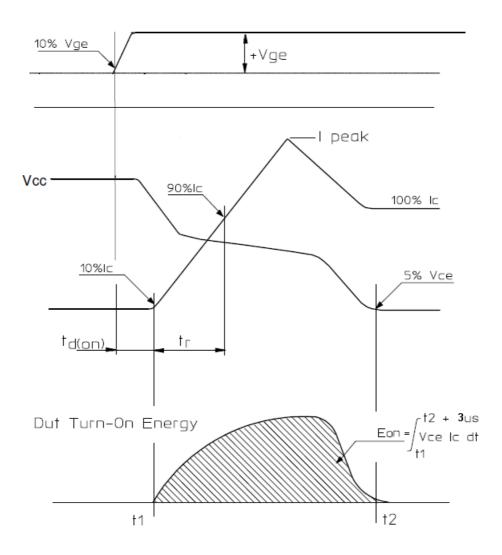


Figure 21. Definition of Turn On Waveform

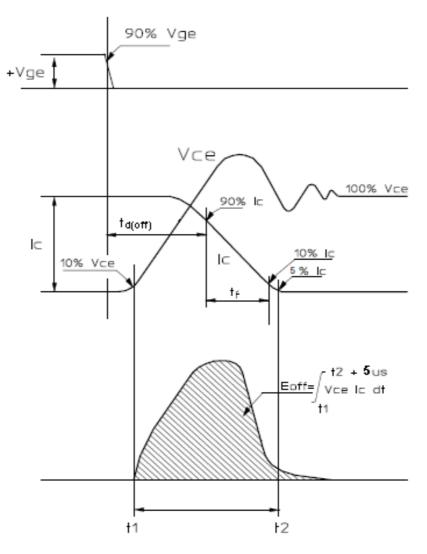
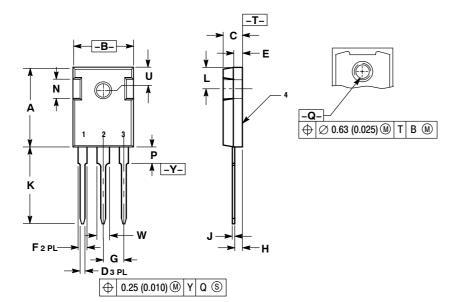


Figure 22. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247 CASE 340L-02 **ISSUE E**



NOTES:

- 1. DIMENSIONING AND TOLERANCING PER ANSI
- CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS		INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	20.32	21.08	0.800	8.30	
В	15.75	16.26	0.620	0.640	
С	4.70	5.30	0.185	0.209	
D	1.00	1.40	0.040	0.055	
Е	1.90	2.60	0.075	0.102	
F	1.65	2.13	0.065	0.084	
G	5.45 BSC		0.215	BSC	
Н	1.50	2.49	0.059	0.098	
J	0.40	0.80	0.016	0.031	
K	19.81	20.83	0.780	0.820	
L	5.40	6.20	0.212	0.244	
N	4.32	5.49	0.170	0.216	
P		4.50		0.177	
Q	3.55	3.65	0.140	0.144	
J	6.15 BSC 0.242 BS		BSC		
W	2.87	3.12	0.113	0.123	

STYLE 4: PIN 1. GATE

- 2. COLLECTOR 3. EMITTER
- 4. COLLECTOR

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