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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







IGBT - Field Stop II / 4 Lead

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop II Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss. In addition, this new device is packaged in a TO-247-4L package that provides significant reduction in E_{on} Losses compared to standard TO-247-3L package. The IGBT is well suited for UPS and solar applications. Incorporated into the device is a soft and fast co-packaged free wheeling diode with a low forward voltage.

Features

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Improved Gate Control Lowers Switching Losses
- Separate Emitter Drive Pin
- TO-247-4L for Minimal E_{on} Losses
- Optimized for High Speed Switching
- These are Pb-Free Devices

Typical Applications

- Solar Inverter
- Uninterruptible Power Inverter Supplies (UPS)
- Neutral Point Clamp Topology

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V _{CES}	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	I _C	160 40	A
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	160	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	I _F	160 40	Α
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	160	Α
Gate-emitter voltage Transient gate-emitter voltage $(T_{pulse} = 5 \mu s, D < 0.10)$	V _{GE}	±20 ±30	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	536 268	W
Operating junction temperature range	T_J	-55 to +175	°C
Storage temperature range	T _{stg}	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

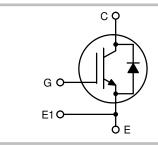
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

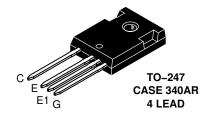


ON Semiconductor®

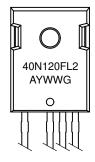
www.onsemi.com

40 A, 1200 V $V_{CEsat} = 2.1 V$ $E_{on} = 1.7 \text{ mJ}$





MARKING DIAGRAM



40N120FL2 = Specific Device Code

= Assembly Location Α

Υ = Year ww = Work Week = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB40N120FL2WAG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{\theta JC}$	0.28	°C/W
Thermal resistance junction-to-case, for Diode	$R_{\theta JC}$	0.50	°C/W
Thermal resistance junction-to-ambient		40	°C/W

ELECTRICAL CHARACTERISTICS (T_{.1} = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu A$	V _{(BR)CES}	1200	_	-	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 40 A V _{GE} = 15 V, I _C = 40 A, T _J = 175°C	V _{CEsat}	_ _	2.1 2.4	2.4 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}$, $I_C = 400 \mu A$	V _{GE(th)}	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 1200 V V _{GE} = 0 V, V _{CE} = 1200 V, T _{J =} 175°C	I _{CES}	_ _	- 4.0	0.4	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V , V _{CE} = 0 V	I _{GES}	-	_	200	nA
Input capacitance		C _{ies}	_	7500	_	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{oes}	-	136	_	'
Reverse transfer capacitance		C _{res}	_	230	_	
Gate charge total		Qg	_	313	_	nC
Gate to emitter charge	$V_{CE} = 600 \text{ V}, I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V}$	Q _{ge}	-	61	_	
Gate to collector charge		Q _{gc}	-	151	-	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t _{d(on)}	-	30	_	ns
Rise time	1	t _r	-	33	-	
Turn-off delay time	T _J = 25°C	t _{d(off)}	-	145	-	
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 40 \text{ A}$ $R_{g} = 10 \Omega$ $V_{GE} = 15 \text{ V}$	t _f	-	95	-	
Turn-on switching loss		E _{on}	-	1.7	-	mJ
Turn-off switching loss		E _{off}	_	1.1	_	
Total switching loss		E _{ts}	_	2.8	_	1
Turn-on delay time		t _{d(on)}	-	28	-	ns
Rise time		t _r	-	37	-	
Turn-off delay time	T _J = 175°C	t _{d(off)}	-	165	-	
Fall time	$V_{CC} = \tilde{6}00 \text{ V, } I_{C} = 40 \text{ A}$ $R_{c} = 10 \Omega$	t _f	-	195	-	
Turn-on switching loss	$R_g = 10 \Omega$ $V_{GE} = 15 V$	E _{on}	-	2.5	-	mJ
Turn-off switching loss		E _{off}	-	2.5	-	
Total switching loss		E _{ts}	-	5.0	-	
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V}, I_F = 40 \text{ A}$ $V_{GE} = 0 \text{ V}, I_F = 40 \text{ A}, T_J = 175^{\circ}\text{C}$	V _F	1 1	2.00 2.30	2.40	V
Reverse recovery time	$T_J = 25^{\circ}\text{C}$ $I_F = 40 \text{ A}, V_R = 400 \text{ V}$ $di_F/dt = 200 \text{ A/}\mu\text{s}$	t _{rr}	-	240	-	ns
Reverse recovery charge		Q _{rr}	-	2.5	-	μC
Reverse recovery current		I _{rrm}	-	18	-	Α
Reverse recovery time	T _J = 175°C I _F = 40 A, V _R = 400 V	t _{rr}	-	392	-	ns
Reverse recovery charge		Q _{rr}	_	5.4	-	μC
Reverse recovery current	di _F /dt = 200 A/μs	I _{rrm}	_	26	_	Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

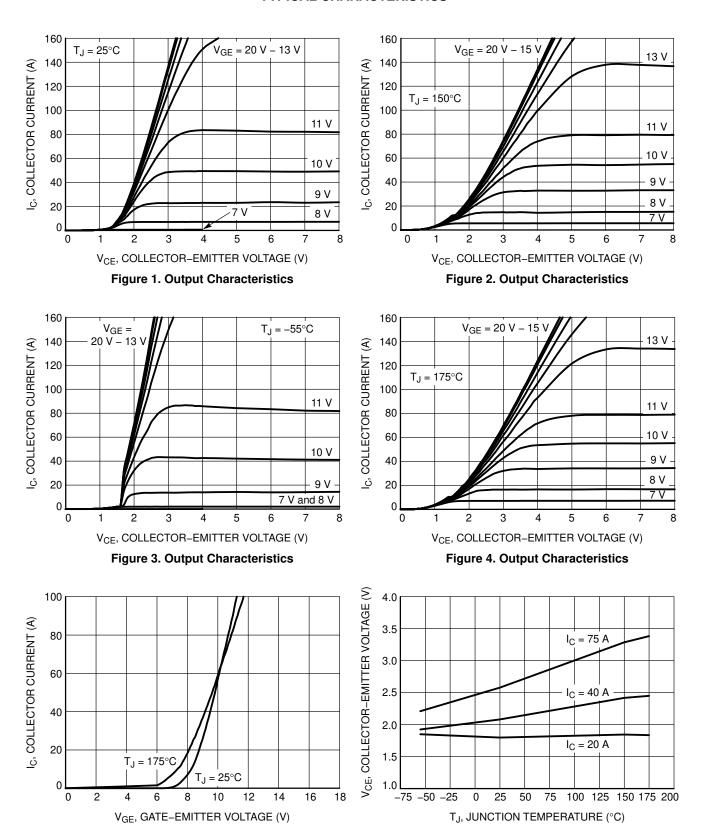


Figure 6. V_{CE(sat)} vs. T_J

Figure 5. Typical Transfer Characteristics

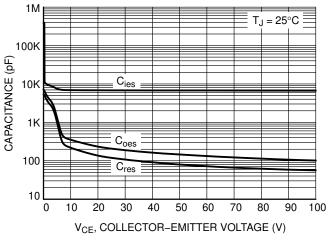


Figure 7. Typical Capacitance

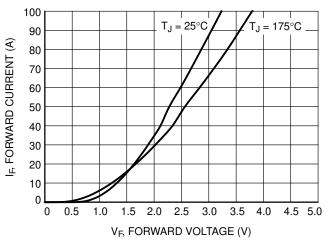


Figure 8. Diode Forward Characteristics

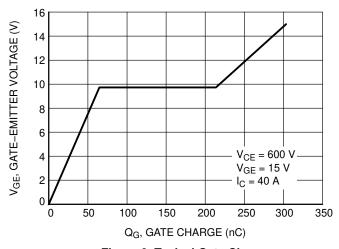


Figure 9. Typical Gate Charge

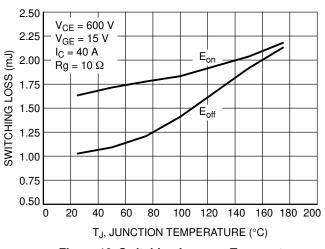


Figure 10. Switching Loss vs. Temperature

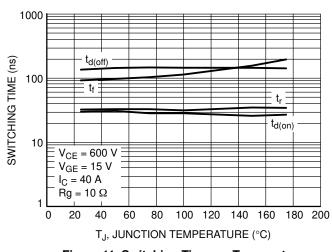


Figure 11. Switching Time vs. Temperature

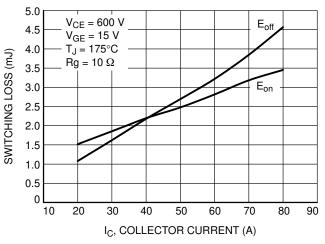


Figure 12. Switching Loss vs. IC

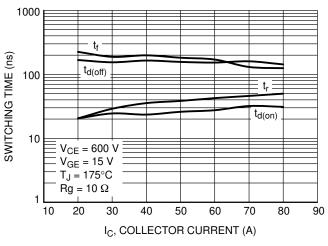


Figure 13. Switching Time vs. IC

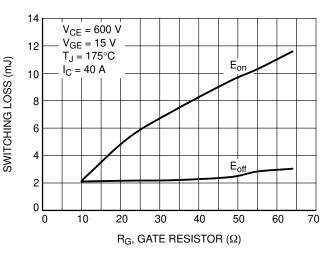


Figure 14. Switching Loss vs. R_G

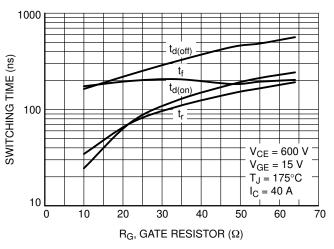


Figure 15. Switching Time vs. R_G

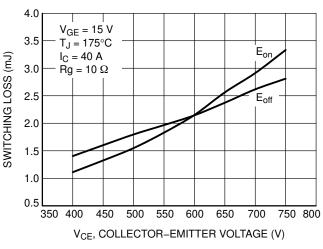


Figure 16. Switching Loss vs. V_{CE}

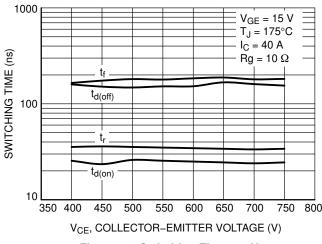


Figure 17. Switching Time vs. V_{CE}

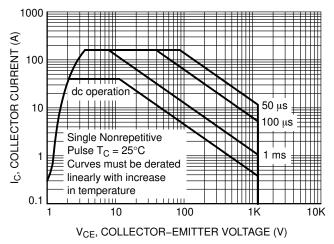


Figure 18. Safe Operating Area

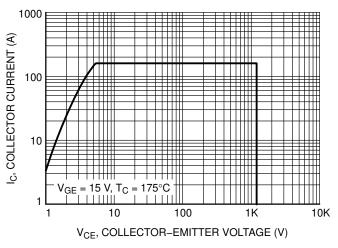


Figure 19. Reverse Bias Safe Operating Area

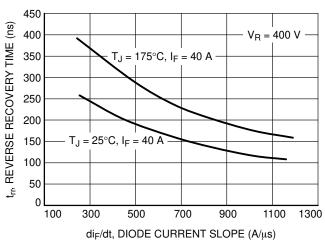


Figure 20. t_{rr} vs. di_F/dt

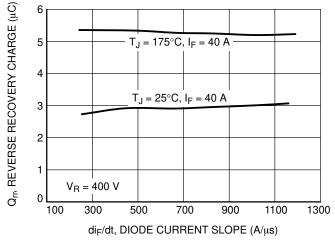


Figure 21. Q_{rr} vs. di_F/dt

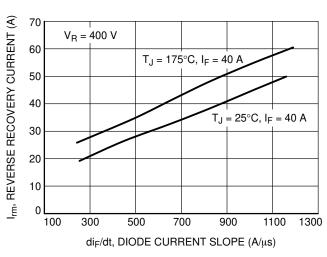


Figure 22. I_{rm} vs. di_F/dt

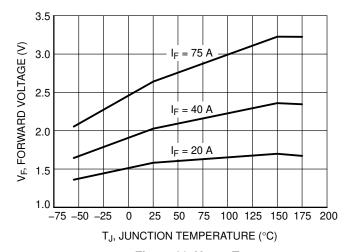


Figure 23. V_F vs. T_J

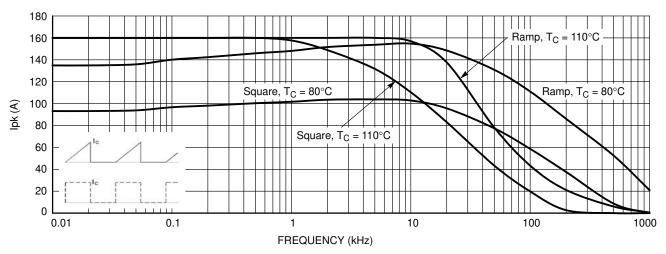


Figure 24. Collector Current vs. Switching Frequency

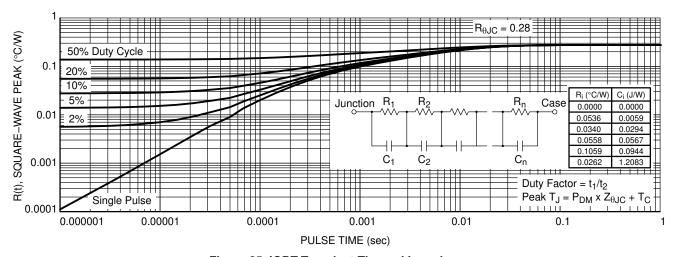


Figure 25. IGBT Transient Thermal Impedance

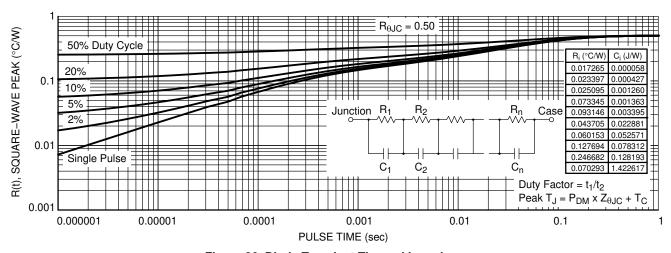


Figure 26. Diode Transient Thermal Impedance

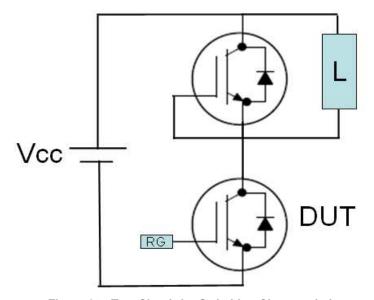


Figure 27. Test Circuit for Switching Characteristics

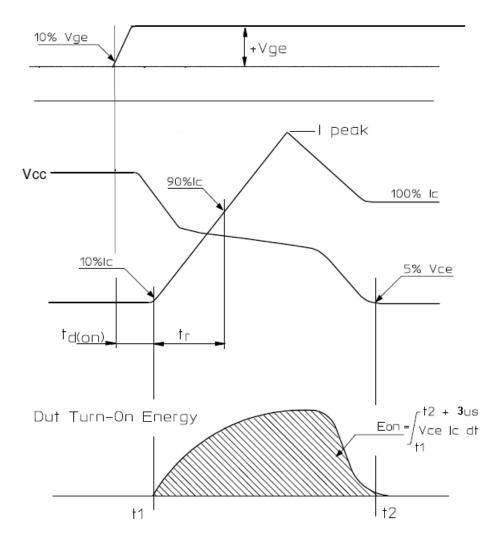


Figure 28. Definition of Turn On Waveform

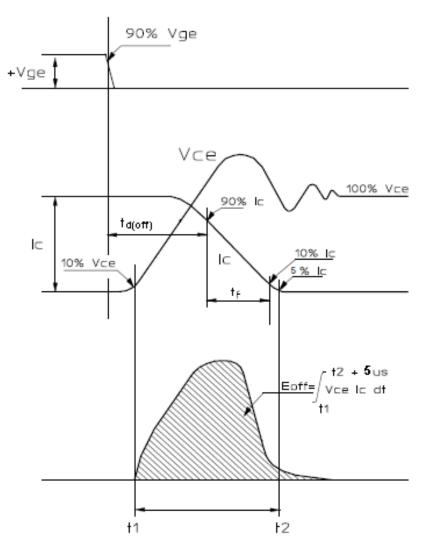
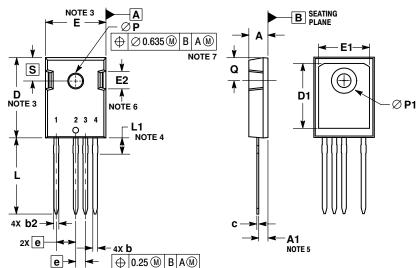


Figure 29. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247 4-LEAD CASE 340AR **ISSUE O**



NOTES

- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.

 LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY L1.

 DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED BY L1.

 NOTCHES ARE REQUIRED BUT THEIR SHAPE IS OPTIONAL.

- DIAMETER ØP SHALL HAVE A MAXIMUM DRAFT ANGLE OF 3.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 4.20.

THE TOP OF THE PART W			
	MILLIMETERS		
DIM	MIN	MAX	
Α	4.83	5.21	
A1	2.29	2.54	
b	1.10	1.30	
b2	1.30	1.50	
С	0.50	0.70	
D	20.80	21.10	
D1	16.25	17.65	
Е	15.75	16.13	
E1	13.06	13.46	
E2	4.32	4.83	
е	2.54 BSC		
L	19.90	20.30	
L1	4.00	4.40	
Р	3.50	3.70	
P1	7.00	7.40	
Q	5.59	6.20	
S	6.15 BSC		

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