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# **Product Preview**

# **IGBT - Ultra Field Stop**

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Ultra Field Stop Trench construction, and provides superior performance in demanding switching applications, offering low switching losses. The IGBT is well suited for applications that require fast switching IGBT with low  $V_F$  diodes, e.g. phase–shifted full bridge, etc. Incorporated into the device is a free wheeling diode with a low forward voltage.

#### **Features**

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Low V<sub>F</sub> Reverse Diode
- Optimized for High Speed Switching
- These are Pb-Free Devices

#### **Typical Applications**

- Welding
- Uninterruptible Power Inverter Supplies (UPS)
- Motor Control

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CES</sub>	1200	V
Collector current @ Tc = 25°C @ Tc = 100°C	I <sub>C</sub>	160 40	А
Pulsed collector current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>CM</sub>	160	Α
Diode forward current @ Tc = 25°C @ Tc = 100°C	l <sub>F</sub>	160 40	А
Diode pulsed current, T <sub>pulse</sub> limited by T <sub>Jmax</sub>	I <sub>FM</sub>	160	Α
Gate-emitter voltage Transient gate-emitter voltage (T <sub>pulse</sub> = 5 μs, D < 0.10)	V <sub>GE</sub>	±20 ±30	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P <sub>D</sub>	454 227	W
Operating junction temperature range	$T_J$	-55 to +175	°C
Storage temperature range	T <sub>stg</sub>	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

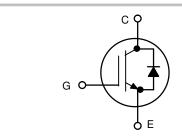
This document contains information on a product under development. ON Semiconductor reserves the right to change or discontinue this product without notice.

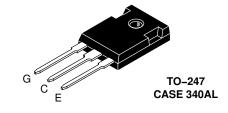


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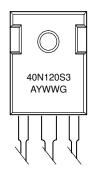
www.onsemi.com

40 A, 1200 V V<sub>CEsat</sub> = 1.7 V E<sub>off</sub> = 1.1 mJ





#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB40N120S3WG	TO-247 (Pb-Free)	30 Units / Rail

#### THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{\theta JC}$	0.34	°C/W
Thermal resistance junction-to-case, for Diode	$R_{\theta JC}$	0.5	°C/W
Thermal resistance junction-to-ambient		40	°C/W

#### **ELECTRICAL CHARACTERISTICS** (T<sub>1</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	•	•				
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500  \mu\text{A}$	V <sub>(BR)CES</sub>	1200	_	-	V
Collector-emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A, T <sub>J</sub> = 175°C	V <sub>CEsat</sub>	-	1.7 2.3	1.95 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}$ , $I_C = 400 \mu A$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J =</sub> 175°C	I <sub>CES</sub>	_ _	_ 0.5	0.4	mA
Gate leakage current, collector-emitter short-circuited	V <sub>GE</sub> = 20 V , V <sub>CE</sub> = 0 V	I <sub>GES</sub>	_	_	200	nA
Input capacitance		C <sub>ies</sub>	_	4912	_	pF
Output capacitance	$V_{CE} = 20 \text{ V}, V_{GE} = 0 \text{ V}, f = 1 \text{ MHz}$	C <sub>oes</sub>	_	140	_	
Reverse transfer capacitance		C <sub>res</sub>	_	80	_	
Gate charge total		$Q_g$	-	212	-	nC
Gate to emitter charge	$V_{CE} = 600 \text{ V}, I_{C} = 40 \text{ A}, V_{GE} = 15 \text{ V}$	Q <sub>ge</sub>	-	43	-	
Gate to collector charge		$Q_{gc}$	-	102	-	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-on delay time		t <sub>d(on)</sub>	-	12	-	ns
Rise time	1	t <sub>r</sub>	-	25	-	
Turn-off delay time	T <sub>J</sub> = 25°C	t <sub>d(off)</sub>	-	145	-	
Fall time	$V_{CC} = 600 \text{ V}, I_{C} = 40 \text{ A}$ $R_{g} = 10 \Omega$	t <sub>f</sub>	-	107	_	
Turn-on switching loss	V <sub>GE</sub> = 15V	E <sub>on</sub>	-	2.2	-	mJ
Turn-off switching loss	1	E <sub>off</sub>	-	1.1	-	
Total switching loss	1	E <sub>ts</sub>	-	3.3	-	
Turn-on delay time		t <sub>d(on)</sub>	-	13	-	ns
Rise time	1	t <sub>r</sub>	-	24	-	
Turn-off delay time	T <sub>J</sub> = 175°C	t <sub>d(off)</sub>	-	153	-	
Fall time	$V_{CC} = 600 \text{ V, } I_{C} = 40 \text{ A}$ $R_{a} = 10 \Omega$	t <sub>f</sub>	-	173	-	
Turn-on switching loss	$R_g = 10 \Omega$ $V_{GE} = 15 V$	E <sub>on</sub>	-	2.8	-	mJ
Turn-off switching loss	1	E <sub>off</sub>	-	1.6	-	
Total switching loss		E <sub>ts</sub>	-	4.4	-	1
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V, } I_F = 40 \text{ A}$ $V_{GE} = 0 \text{ V, } I_F = 40 \text{ A, } T_J = 175^{\circ}\text{C}$	V <sub>F</sub>	_ _	2.0 2.55	2.6 -	V
Reverse recovery time		t <sub>rr</sub>	-	163	_	ns
Reverse recovery charge	T <sub>J</sub> = 25°C	Q <sub>rr</sub>	-	2.9	_	μС
Reverse recovery current	$I_F = 40 \text{ A}, V_R = 400 \text{ V}$	I <sub>rrm</sub>	-	30	_	Α
Diode peak rate of fall of reverse recovery current during tb		dl <sub>rrm</sub> /dt	-	137	-	A/μs

### **ELECTRICAL CHARACTERISTICS** (T<sub>J</sub> = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
DIODE CHARACTERISTIC						
Reverse recovery time	$T_{J} = 175^{\circ} C$ $I_{F} = 40 \text{ A, V}_{R} = 400 \text{ V}$ $di_{F}/dt = 500 \text{ A/}\mu\text{s}$	t <sub>rr</sub>	-	250	_	ns
Reverse recovery charge		Q <sub>rr</sub>	-	5.3	-	μς
Reverse recovery current		I <sub>rrm</sub>	-	40	-	Α
Diode peak rate of fall of reverse recovery current during tb		dl <sub>rrm</sub> /dt	-	482	-	A/μs

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

#### TYPICAL CHARACTERISTICS

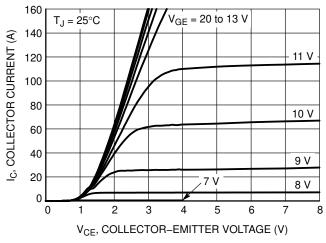


Figure 1. Output Characteristics

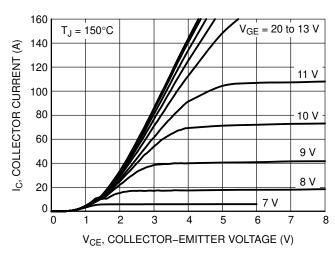


Figure 2. Output Characteristics

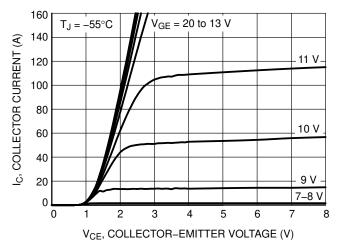


Figure 3. Output Characteristics

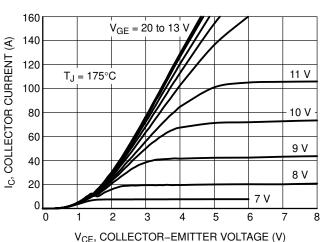


Figure 4. Output Characteristics

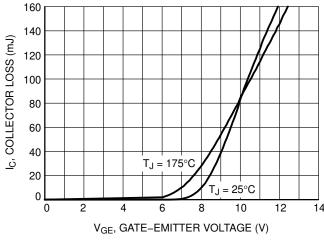


Figure 5. Typical Transfer Characteristics

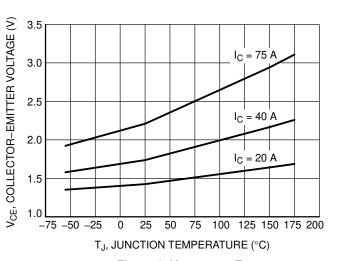


Figure 6. V<sub>CE(sat)</sub> vs. T<sub>J</sub>

#### TYPICAL CHARACTERISTICS

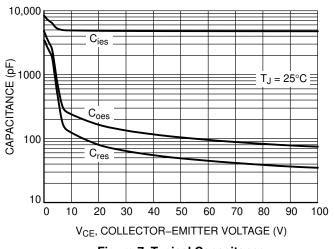


Figure 7. Typical Capacitance

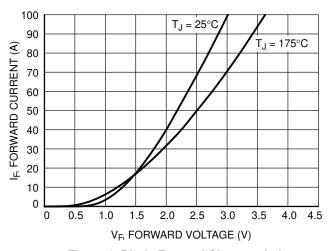


Figure 8. Diode Forward Characteristics

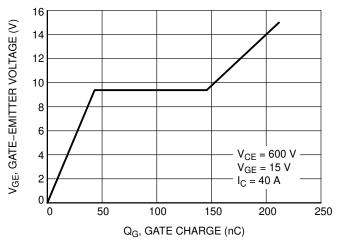


Figure 9. Typical Gate Charge

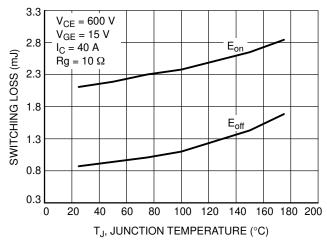


Figure 10. Switching Loss vs. Temperature

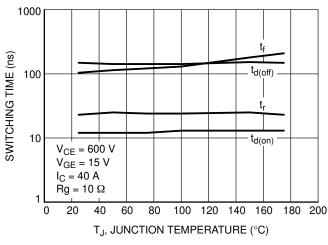


Figure 11. Switching Loss vs. Temperature

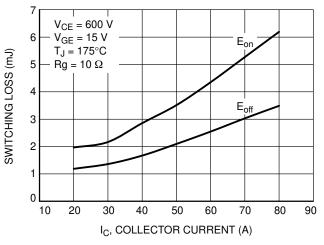


Figure 12. Switching Loss vs. I<sub>C</sub>

#### TYPICAL CHARACTERISTICS

SWITCHING LOSS (mJ)

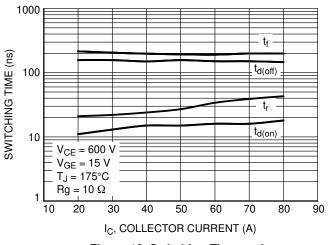


Figure 13. Switching Time vs. I<sub>C</sub>

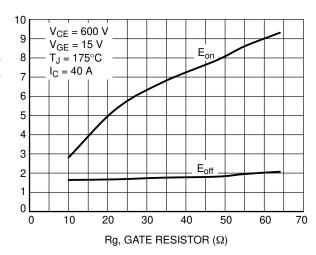


Figure 14. Switching Loss vs. R<sub>G</sub>

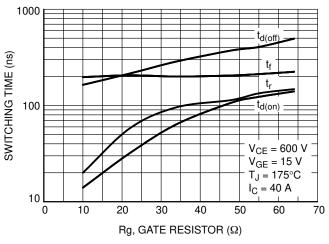


Figure 15. Switching Time vs. R<sub>G</sub>

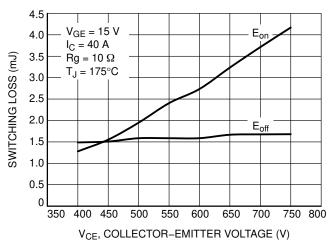


Figure 16. Switching Loss vs. V<sub>CE</sub>

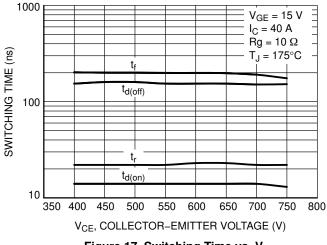


Figure 17. Switching Time vs. V<sub>CE</sub>

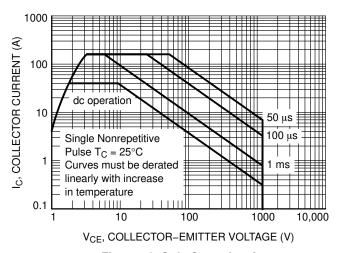
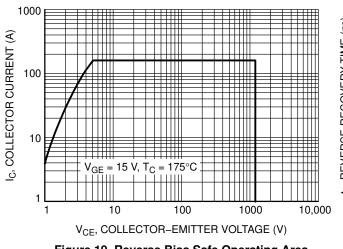


Figure 18. Safe Operating Area

#### **TYPICAL CHARACTERISTICS**

400

350

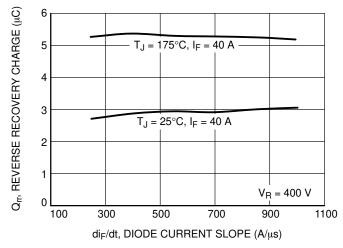


REVERSE RECOVERY TIME (ns)  $T_J = 175^{\circ}C, I_F = 40 A$ 300 250 200  $T_J = 25^{\circ}C$ ,  $I_F = 40 \text{ A}$ 150 100 50 ₹ 100 300 500 700 1100

 $V_{R} = 400 \ V$ 

Figure 19. Reverse Bias Safe Operating Area

 $di_F/dt$ , DIODE CURRENT SLOPE (A/ $\mu$ s) Figure 20. t<sub>rr</sub> vs. di<sub>F</sub>/dt



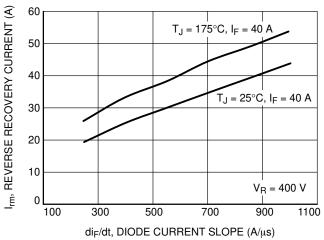


Figure 21. Q<sub>rr</sub> vs. di<sub>F</sub>/dt

Figure 22. I<sub>rm</sub> vs. di<sub>F</sub>/dt

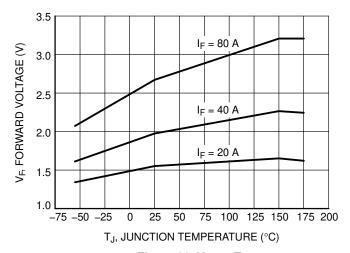


Figure 23. V<sub>F</sub> vs. T<sub>J</sub>

#### **TYPICAL CHARACTERISTICS**

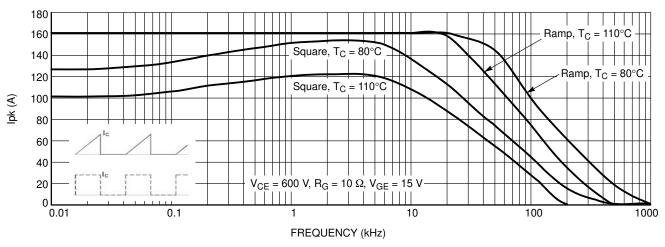


Figure 24. Collector Current vs. Switching Frequency

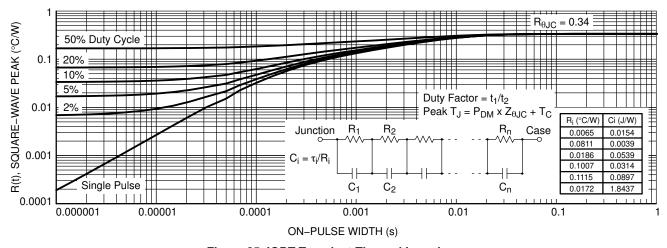


Figure 25. IGBT Transient Thermal Impedance

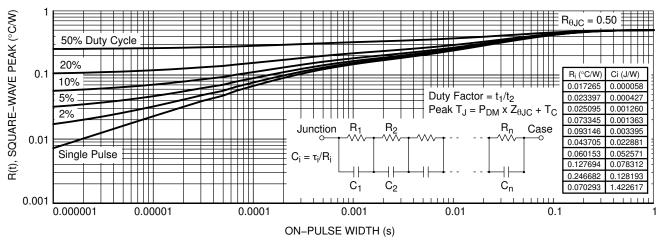


Figure 26. Diode Transient Thermal Impedance

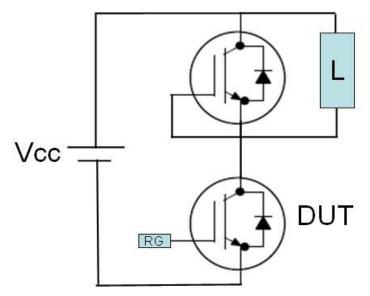


Figure 27. Test Circuit for Switching Characteristics

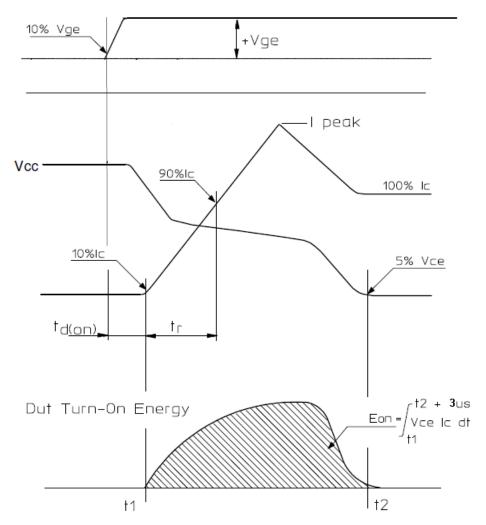


Figure 28. Definition of Turn On Waveform

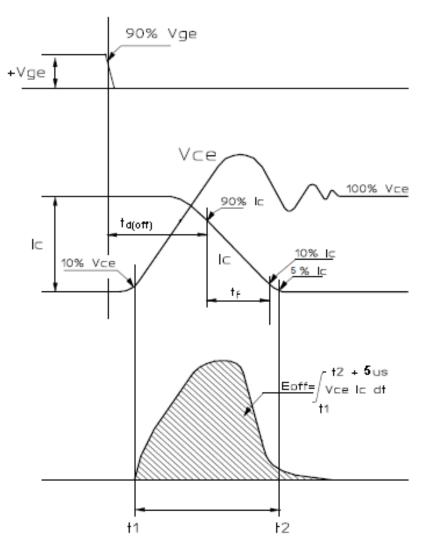
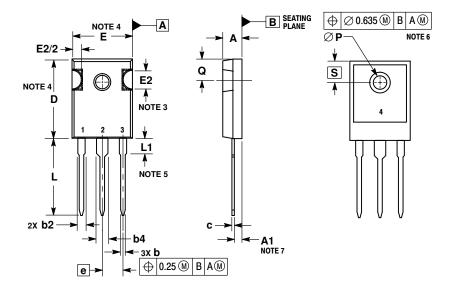


Figure 29. Definition of Turn Off Waveform

#### PACKAGE DIMENSIONS

TO-247 CASE 340AL **ISSUE B** 



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
   CONTROLLING DIMENSION: MILLIMETERS.
- SLOT REQUIRED, NOTCH MAY BE ROUNDED.
  DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH.
- MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY
- LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ØP SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.

  DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED
- BY L1.

	MILLIMETERS			
DIM	MIN	MAX		
Α	4.70	5.30		
A1	2.20	2.60		
b	1.00	1.40		
b2	1.65	2.35		
b4	2.60	3.40		
С	0.40	0.80		
D	20.80	21.34		
Е	15.50	16.25		
E2	4.32	5.49		
е	5.45	5.45 BSC		
L	19.80	20.80		
L1	3.81	4.32		
Р	3.55	3.65		
Q	5.40	6.20		
S	6.15 BSC			

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