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# IGBT with Monolithic Reverse Conducting Diode

This Insulated Gate Bipolar Transistor (IGBT) features robust and cost effective Field Stop (FS2) trench construction with a monolithic RC Diode. It provides a cost effective Solution for applications where diode losses are minimal. The IGBT is optimized for low conduction losses (low  $V_{CEsat}$ ) and is well suited for resonant or soft switching applications.

#### **Features**

- Extremely Efficient Trench with Fieldstop Technology
- Low Conduction Design for Soft Switching Application
- Reduced Power Dissipation in Inducting Heating Application
- Reliable and Cost Effective Single Die Solution
- This is a Pb-Free Device

#### **Typical Applications**

- Inductive Heating
- Air Conditioning PFC
- Welding

#### **ABSOLUTE MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Collector-emitter voltage	V <sub>CES</sub>	650	٧
Collector current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	lc	80 40	A
Pulsed collector current, $t_{pulse}$ limited by $T_{Jmax}$ , 10 $\mu s$ pulse, $V_{GE} = 15 \text{ V}$	I <sub>CM</sub>	160	Α
Diode forward current @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	I <sub>F</sub>	80 40	Α
Diode pulsed current, $t_{pulse}$ limited by $T_{Jmax}$ , 10 $\mu s$ pulse, $V_{GE} = 0$ V	I <sub>FM</sub>	160	Α
Gate-emitter voltage Transient Gate-emitter voltage ( $t_{pulse} = 5 \mu s$ , D < 0.10)	V <sub>GE</sub>	±20 ±25	V
Power Dissipation @ T <sub>C</sub> = 25°C @ T <sub>C</sub> = 100°C	P <sub>D</sub>	405 202	W
Operating junction temperature range	TJ	-40 to +175	°C
Storage temperature range	T <sub>stg</sub>	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T <sub>SLD</sub>	260	°C

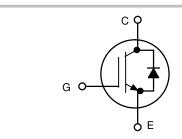
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

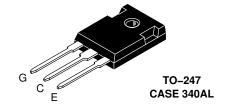


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40 A, 650 V V<sub>CEsat</sub> = 1.55 V E<sub>off</sub> = 0.42 mJ





#### **MARKING DIAGRAM**



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

#### **ORDERING INFORMATION**

Device	Package	Shipping
NGTB40N65IHRWG	TO-247 (Pb-Free)	30 Units / Rail

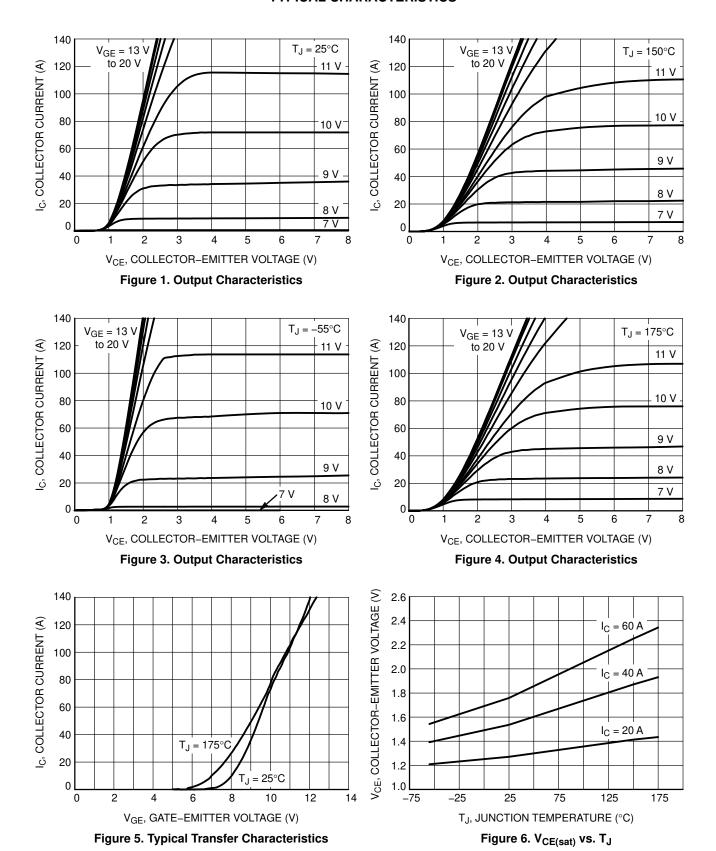
#### THERMAL CHARACTERISTICS

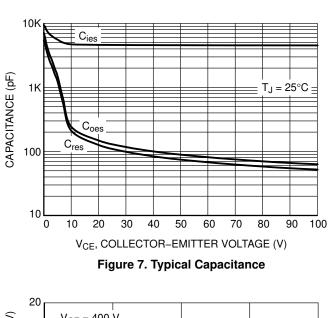
Rating	Symbol	Value	Unit
Thermal resistance junction-to-case	$R_{ heta JC}$	0.37	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

#### **ELECTRICAL CHARACTERISTICS** ( $T_J = 25^{\circ}C$ unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC	•	•		•		•
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu A$	V <sub>(BR)CES</sub>	650	-	-	V
Collector–emitter saturation voltage	V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A V <sub>GE</sub> = 15 V, I <sub>C</sub> = 40 A, T <sub>J</sub> = 175°C	V <sub>CEsat</sub>	<u> </u>	1.55 1.95	1.7 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 350 \mu A$	V <sub>GE(th)</sub>	4.5	5.5	6.5	٧
Collector–emitter cut–off current, gate– emitter short–circuited	V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 650 V V <sub>GE</sub> = 0 V, V <sub>CE</sub> = 1200 V, T <sub>J</sub> = 175°C	I <sub>CES</sub>	- -	_ 1.0	0.3 -	mA
Gate leakage current, collector–emitter short–circuited	V <sub>GE</sub> = 20 V, V <sub>CE</sub> = 0 V	I <sub>GES</sub>	-	-	100	nA
DYNAMIC CHARACTERISTIC	•	•		•	•	
Input capacitance	V <sub>CE</sub> = 20 V, V <sub>GE</sub> = 0 V, f = 1 MHz	C <sub>ies</sub>	-	4628	_	pF
Output capacitance		C <sub>oes</sub>	-	148	_	
Reverse transfer capacitance	1	C <sub>res</sub>	-	126	-	
Gate charge total		$Q_g$	_	190	_	nC
Gate to emitter charge	V <sub>CE</sub> = 400 V, I <sub>C</sub> = 40 A, V <sub>GE</sub> = 15 V	$Q_{ge}$	_	38	_	
Gate to collector charge	1	$Q_{gc}$	_	90	_	
SWITCHING CHARACTERISTIC, INDUC	TIVE LOAD					
Turn-off delay time	$T_{J} = 25^{\circ}C$ $V_{CC} = 400 \text{ V, } I_{C} = 40 \text{ A}$ $R_{g} = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t <sub>d(off)</sub>	_	197	_	ns
Fall time		t <sub>f</sub>	_	74	_	
Turn-off switching loss		E <sub>off</sub>	-	0.42	-	mJ
Turn-off delay time	$T_{J} = 175^{\circ}C$ $V_{CC} = 400 \text{ V, } I_{C} = 40 \text{ A}$ $R_{g} = 10 \Omega$	t <sub>d(off)</sub>	-	210	-	ns
Fall time		t <sub>f</sub>	-	106	-	
Turn-off switching loss	$V_{GE} = 0 \text{ V} / 15 \text{V}$	E <sub>off</sub>	-	0.7	_	mJ
DIODE CHARACTERISTIC						
Forward voltage	V <sub>GE</sub> = 0 V, I <sub>F</sub> = 40 A V <sub>GE</sub> = 0 V, I <sub>F</sub> = 40 A, T <sub>J</sub> = 175°C	V <sub>F</sub>	_ _	1.50 1.70	1.80 -	V

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.





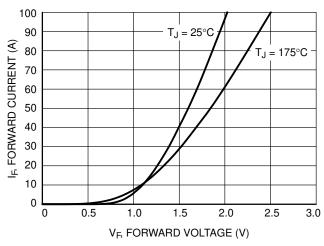
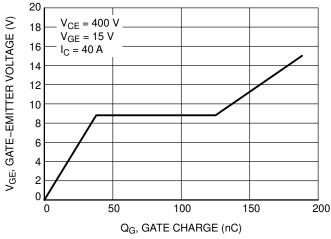


Figure 8. Diode Forward Characteristics



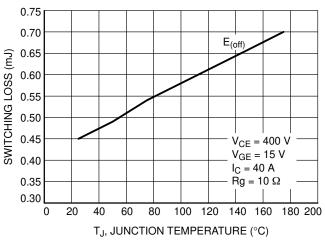
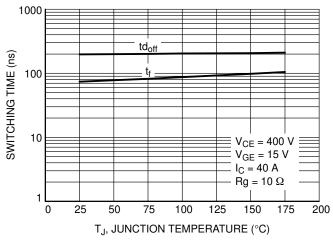


Figure 9. Typical Gate Charge

Figure 10. Switching Loss vs. Temperature



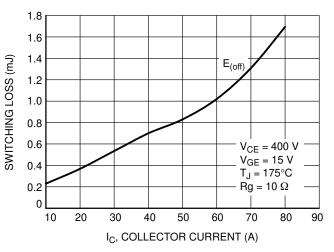


Figure 11. Switching Time vs. Temperature

Figure 12. Switching Loss vs. I<sub>C</sub>

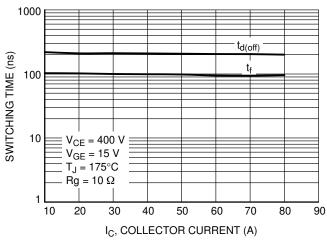


Figure 13. Switching Time vs. I<sub>C</sub>

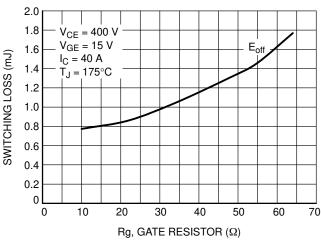


Figure 14. Switching Loss vs. Rg

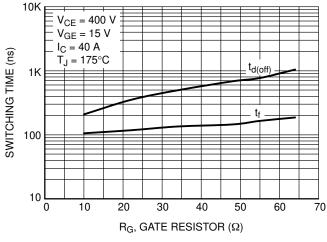


Figure 15. Switching Time vs. Rg

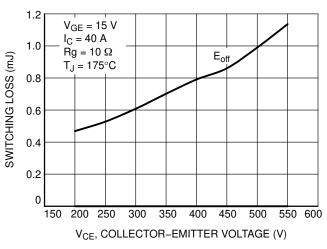


Figure 16. Switching Loss vs. V<sub>CE</sub>

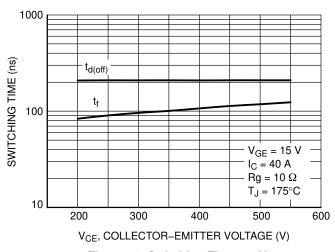


Figure 17. Switching Time vs. V<sub>CE</sub>

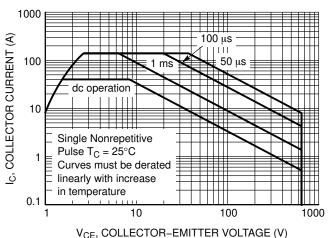


Figure 18. Safe Operating Area

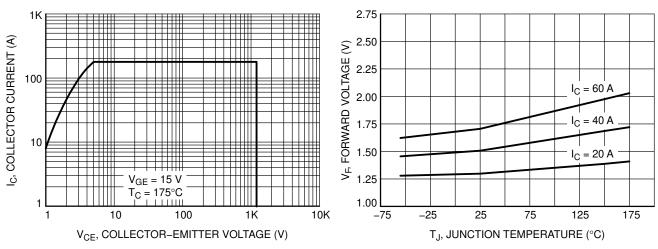


Figure 19. Reverse Bias Safe Operating Area

Figure 20. Forward Voltage vs. Junction Temperature

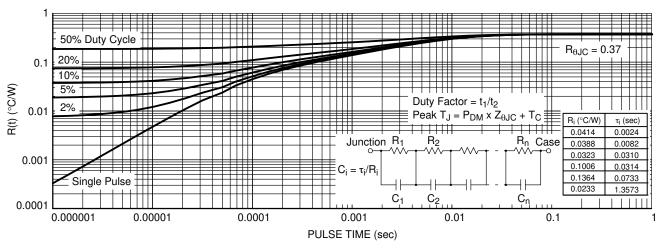


Figure 21. IGBT Transient Thermal Impedance

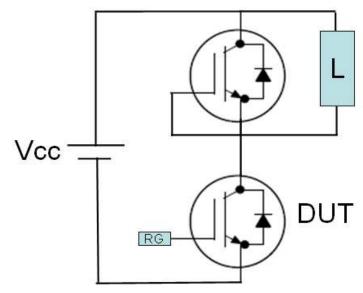


Figure 22. Test Circuit for Switching Characteristics

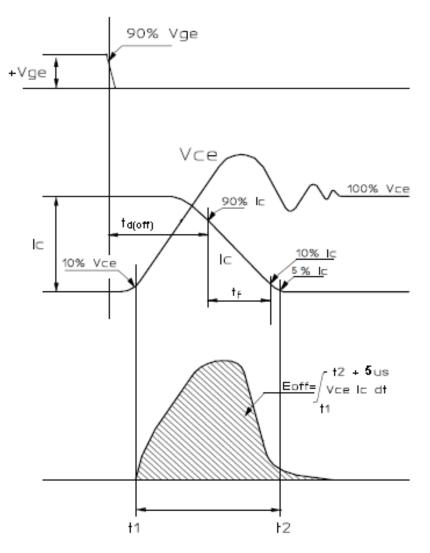
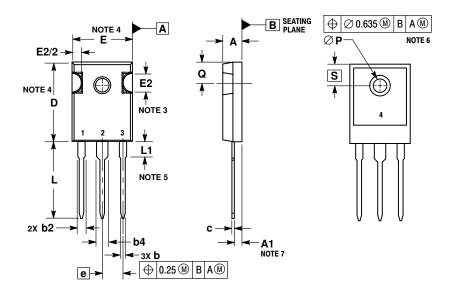


Figure 23. Definition of Turn Off Waveform

#### PACKAGE DIMENSIONS

#### TO-247 CASE 340AL **ISSUE A**



#### NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
  SLOT REQUIRED. NOTCH MAY BE ROUNDED.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
- LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ØP SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.00	1.40	
b2	1.65	2.35	
b4	2.60	3.40	
С	0.40	0.80	
D	20.30	21.40	
Е	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
L	19.80	20.80	
L1	3.50	4.50	
P	3.55	3.65	
Q	5.40	6.20	
S	6.15 BSC		

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