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With the principle of "Quality Parts, Customers Priority, Honest Operation, and Considerate Service", our business mainly focus on the distribution of electronic components. Line cards we deal with include Microchip, ALPS, ROHM, Xilinx, Pulse, ON, Everlight and Freescale. Main products comprise IC, Modules, Potentiometer, IC Socket, Relay, Connector. Our parts cover such applications as commercial, industrial, and automotives areas.

We are looking forward to setting up business relationship with you and hope to provide you with the best service and solution. Let us make a better world for our industry!



Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

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Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China







IGBT

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on state voltage and minimal switching loss.

Features

- Extremely Efficient Trench with Field Stop Technology
- $T_{Jmax} = 175^{\circ}C$
- Soft Fast Reverse Recovery Diode
- Optimized for High Speed Switching
- 5 µs Short-Circuit Capability
- These are Pb-Free Devices

Typical Applications

- Solar Inverters
- Uninterruptible Power Supplies (UPS)
- Welding

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-emitter voltage	V_{CES}	650	V
Collector current @ Tc = 25°C @ Tc = 100°C	I _C	100 75	A
Diode Forward Current @ Tc = 25°C @ Tc = 100°C	I _F	100 75	А
Diode Pulsed Current T _{PULSE} Limited by T _J Max	I _{FM}	200	Α
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	200	Α
Short–circuit withstand time $V_{GE} = 15 \text{ V}, V_{CE} = 400 \text{ V}, $ $T_{J} \le +150 ^{\circ}\text{C}$	t _{SC}	5	μS
Gate-emitter voltage	V_{GE}	±20	V
Transient gate-emitter voltage (T _{PULSE} = 5 μ s, D < 0.10)		±30	V
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	595 265	W
Operating junction temperature range	TJ	–55 to +175	°C
Storage temperature range	T _{stg}	-55 to +175	°C
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C

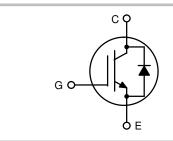
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

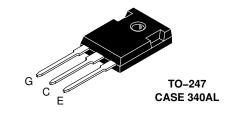


ON Semiconductor®

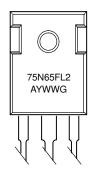
www.onsemi.com

75 A, 650 V V_{CEsat} = 1.70 V E_{OFF} = 1.0 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

Device	Package	Shipping
NGTB75N65FL2WG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case, for IGBT	$R_{ heta JC}$	0.28	°C/W
Thermal resistance junction-to-case, for Diode	$R_{ heta JC}$	0.62	°C/W
Thermal resistance junction-to-ambient	$R_{ heta JA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC		•				
Collector–emitter breakdown voltage, gate–emitter short–circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	V _{(BR)CES}	650	_	-	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 75 A V _{GE} = 15 V, I _C = 75 A, T _J = 175°C	V _{CEsat}	1.50 -	1.75 2.30	2.00	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 350 \mu A$	$V_{GE(th)}$	4.5	5.5	6.5	V
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 650 V V _{GE} = 0 V, V _{CE} = 650 V, T _{J =} 175°C	I _{CES}	_ _	- -	0.1 4.0	mA
Gate leakage current, collector–emitter short–circuited	V _{GE} = 20 V , V _{CE} = 0 V	I _{GES}	-	-	200	nA
DYNAMIC CHARACTERISTIC		•				
Input capacitance		C _{ies}	_	7500	_	pF
Output capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	C _{oes}	_	300	-	
Reverse transfer capacitance	1	C _{res}	_	190	-	
Gate charge total		Q _q	_	310	-	nC
Gate to emitter charge	V _{CE} = 480 V, I _C = 50 A, V _{GE} = 15 V	Q _{ge}	_	60	-	
Gate to collector charge	1	Q _{gc}	_	150	_	
SWITCHING CHARACTERISTIC, INDUCT	TIVE LOAD	90				<u>l</u>
Turn-on delay time		t _{d(on)}	_	110	_	ns
Rise time	1	t _r	_	48	-	
Turn-off delay time	T _J = 25°C	t _{d(off)}	_	270	_	
Fall time	$V_{CC} = 400 \text{ V}, I_{C} = 75 \text{ A}$	t _f	_	70	_	
Turn–on switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	E _{on}	_	2.2	_	mJ
Turn-off switching loss	GL	E _{off}	_	1.1	_	
Total switching loss		E _{ts}	_	3.3	_	
Turn-on delay time		t _{d(on)}	_	100	-	ns
Rise time	1	t _r	_	50	_	
Turn-off delay time	$T_{J} = 150^{\circ}\text{C}$ $V_{CC} = 400 \text{ V, } I_{C} = 75 \text{ A}$ $R_{g} = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t _{d(off)}	_	280	_	
Fall time		t _f	_	100	_	
Turn-on switching loss		E _{on}	_	2.8	_	mJ
Turn-off switching loss	1	E _{off}	_	1.6	_	
Total switching loss	1	E _{ts}	_	4.4	-	
DIODE CHARACTERISTIC						
Forward voltage	$V_{GE} = 0 \text{ V, } I_F = 75 \text{ A}$ $V_{GE} = 0 \text{ V, } I_F = 75 \text{ A, } T_J = 175^{\circ}\text{C}$	V _F	1.50 -	2.20 2.40	2.90 -	V
Reverse recovery time	T _{.1} = 25°C	t _{rr}	_	80	-	ns
Reverse recovery charge	$I_J = 25^{\circ}C$ $I_F = 75 \text{ A, } V_R = 400 \text{ V}$ $di_F/dt = 200 \text{ A/}\mu\text{s}$	Q _{rr}	_	0.40	-	μC
Reverse recovery current		I _{rrm}	_	8	-	Α
Reverse recovery time	T _J = 175°C	t _{rr}	_	143	-	ns
Reverse recovery charge	$I_F = 75 \text{ A}, V_R = 400 \text{ V}$	Q _{rr}	_	1.45	-	μC
Reverse recovery current	di _F /dt = 200 A/μs	I _{rrm}	_	16	_	Α

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

TYPICAL CHARACTERISTICS

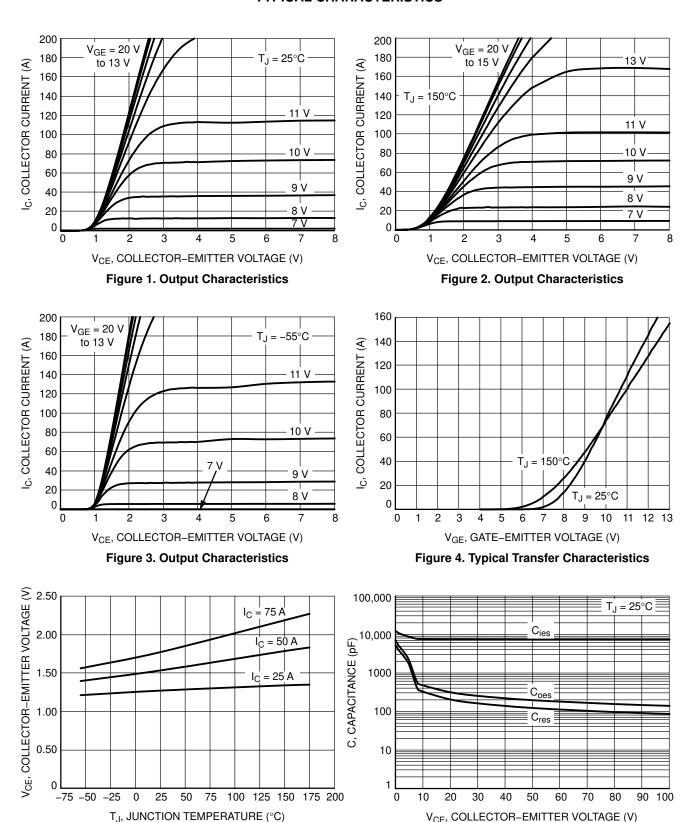


Figure 6. Typical Capacitance

Figure 5. V_{CE(sat)} vs. T_J

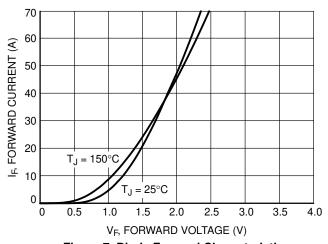


Figure 7. Diode Forward Characteristics

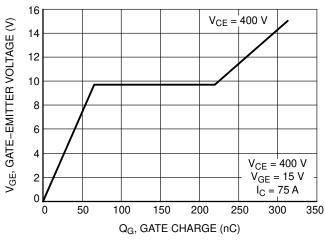


Figure 8. Typical Gate Charge

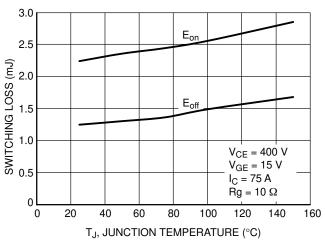


Figure 9. Switching Loss vs. Temperature

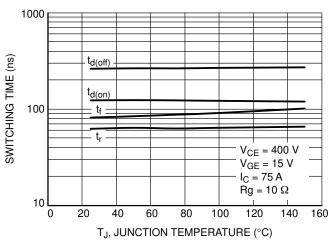


Figure 10. Switching Time vs. Temperature

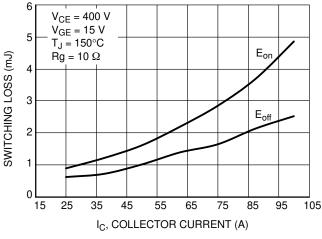


Figure 11. Switching Loss vs. I_C

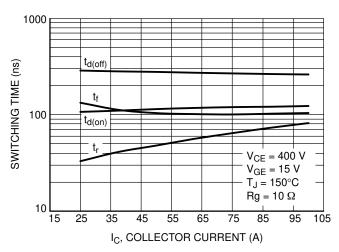


Figure 12. Switching Time vs. I_C

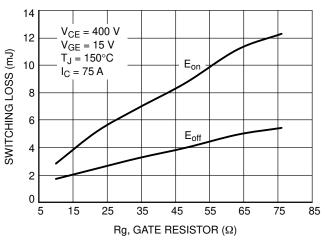


Figure 13. Switching Loss vs. Rg

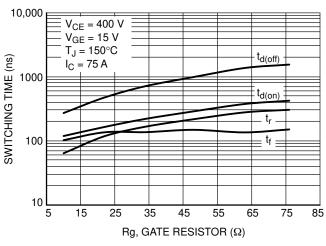


Figure 14. Switching Time vs. Rg

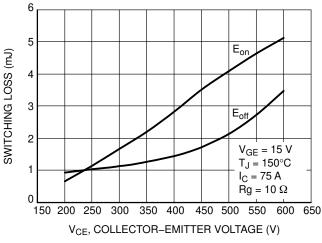


Figure 15. Switching Loss vs. V_{CE}

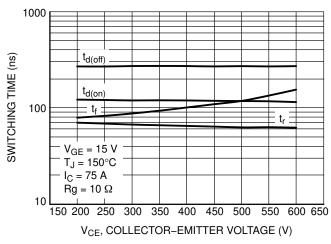


Figure 16. Switching Time vs. V_{CE}

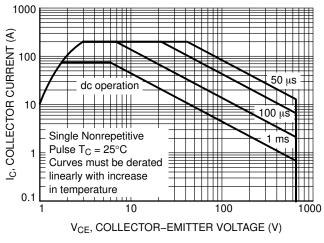


Figure 17. Safe Operating Area

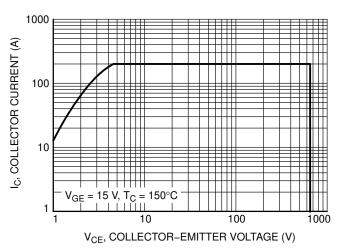
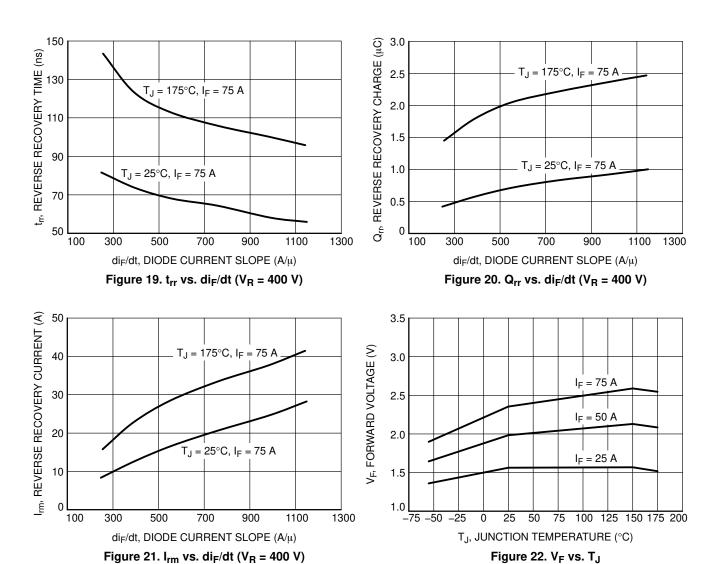


Figure 18. Reverse Bias Safe Operating Area



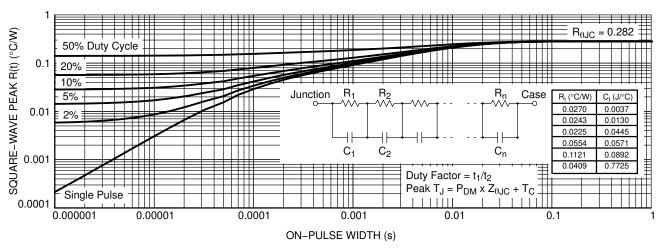


Figure 23. IGBT Transient Thermal Impedance

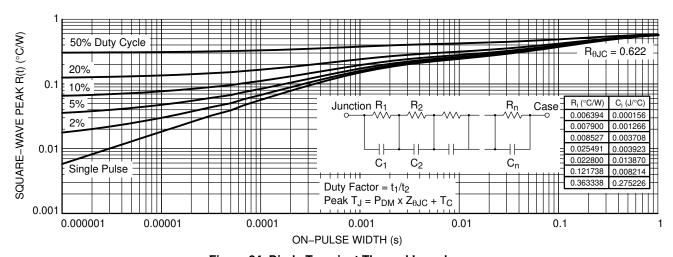
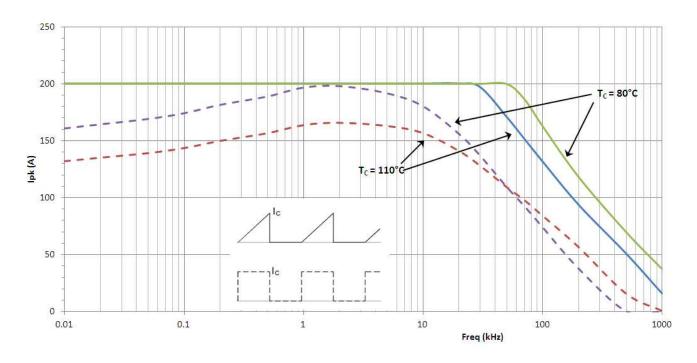


Figure 24. Diode Transient Thermal Impedance



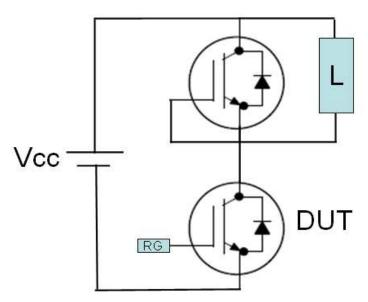


Figure 25. Test Circuit for Switching Characteristics

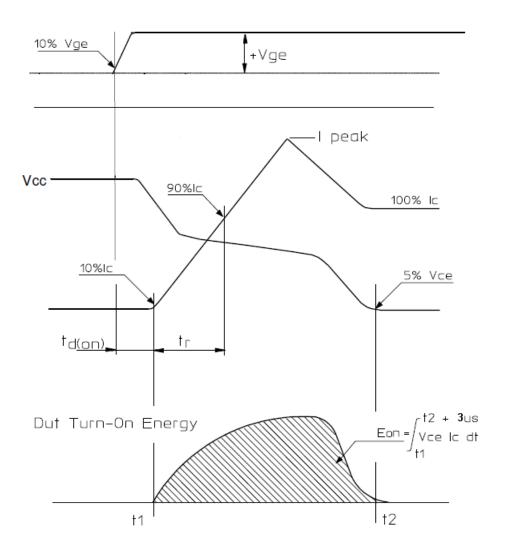


Figure 26. Definition of Turn On Waveform

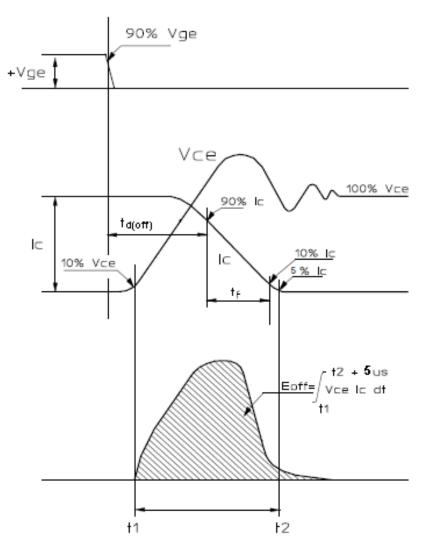
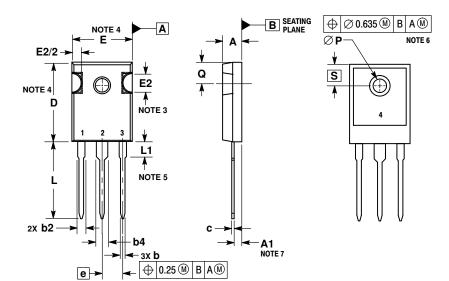


Figure 27. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247 CASE 340AL **ISSUE B**



- DIMENSIONING AND TOLERANCING PER ASME Y14.5M. 1994.
- CONTROLLING DIMENSION: MILLIMETERS
- SLOT REQUIRED, NOTCH MAY BE ROUNDED. DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
 LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- \varnothing P SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91.
 DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.00	1.40	
b2	1.65	2.35	
b4	2.60	3.40	
C	0.40	0.80	
D	20.80	21.34	
Е	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
L	19.80	20.80	
L1	3.81	4.32	
Р	3.55	3.65	
Q	5.40	6.20	
S	6.15 BSC		

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