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NHD-3.5-320240MF-ASXV#

TFT (Thin-Film-Transistor) Color Liquid Crystal Display Module

NHD- Newhaven Display
3.5- 3.5" Diagonal
320240- 320xRGBx240 Pixels

MF- Model

A- Built-in Driver / No Controller

S- High Brightness, White LED Backlight

X- TFT

V- MVA, Wide Temperature

RoHS Compliant

Newhaven Display International, Inc.

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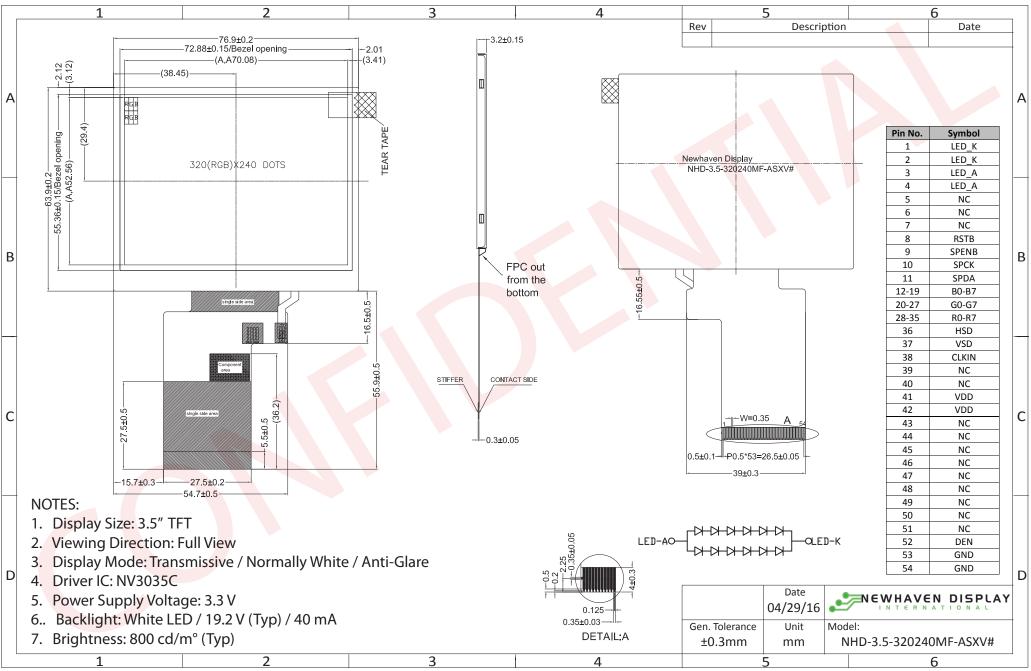
Document Revision History

Revision	Date	Description	Changed by
0	4/29/16	Initial Release	SB
1	6/30/16	Added Chromaticity	SB
2	9/23/16	Updated I _{LED} , I _{DD} , and Chromaticity Values	SB

Functions and Features

- 320xRGBx240 resolution
- LED backlight
- 3.3V power supply
- 24-bit Parallel digital RGB interface (6.4MHz)
- Premium high brightness display

Mechanical Drawing



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Pin Description

Pin No.	Symbol	External	Function Description				
PIII NO.	Syllibol	Connection	Function Description				
1	LED K	Power Supply	Backlight Cathode (Ground)				
2	LED_K	Power Supply Power Supply	Backlight Cathode (Ground)				
3	LED_K	Power Supply Power Supply	Backlight Anode (40mA @ 19.2V)				
4	-		, , , , , , , , , , , , , , , , , , , ,				
5	LED_A NC	Power Supply	Backlight Anode (40mA @ 19.2V) No Connect				
6		-					
7	NC NC	-	No Connect				
	NC	-	No Connect				
8	RSTB	MPU	Active LOW Reset signal				
9	SPENB	MPU	Active LOW Serial Chip Select signal				
10	SPCK	MPU	Serial Clock signal				
11	SPDA	MPU	Serial Data signal				
12-19	B0-B7	MPU	Blue Data signals				
20-27	G0-G7	MPU	Green Data signals				
28-35	R0-R7	MPU	Red Data signals				
36	HSD	MPU	Horizontal (Line) Sync signal				
37	VSD	MPU	Vertical (Frame) Sync signal				
38	CLKIN	MPU	Dot Clock signal				
39	NC	-	No Connect				
40	NC	-	No Connect				
41	VDD	Power Supply	Supply Voltage for LCD and logic (3.3V)				
42	VDD	Power Supply	Supply Voltage for LCD and logic (3.3V)				
43	NC	-	No Connect				
44	NC	-	No Connect				
45	NC	-	No Connect				
46	NC	-	No Connect				
47	NC	-	No Connect				
48	NC	-	No Connect				
49	NC	-	No Connect				
50	NC	-	No Connect				
51	NC	-	No Connect				
52	DEN	-	Data Enable signal (No Connect)				
53	GND	Power Supply	Ground				
54	GND	Power Supply	Ground				

Recommended connector: 54pin, 0.5mm pitch, FFC connector. Molex P/N 51296-5494

Electrical Characteristics

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	T _{OP}	Absolute Max	-20	-	+70	°C
Storage Temperature Range	T _{ST}	Absolute Max	-30	-	+80	°C
Digital Supply Voltage	V_{DD}	ı	3.0	3.3	3.6	V
Supply Current	I _{DD}	$V_{DD}=3.3V$	5	10	20	mA
"H" Level input	V _{IH}	-	$0.8*V_{DD}$	-	V_{DD}	V
"L" Level input	V _{IL}	-	V_{SS}	-	0.2*V _{DD}	V
"H" Level output	V _{OH}	-	V_{DD} -0.4	-	V_{DD}	V
"L" Level output	V _{OL}	ı	V_{SS}	-	V _{SS} +0.4	V
Backlight Supply Voltage	V_{LED}	ı	17.4	19.2	19.8	V
Backlight Supply Current	I _{LED}	V _{LED} =19.2V	30	40	50	mA
Backlight Lifetime*	-	$I_{LED} = 40 \text{ mA}$ $T_{OP} = 25^{\circ} \text{ C}$	20,000	50,000	-	Hrs.

^{*}Backlight lifetime is rated as Hours until half-brightness, under normal operating conditions.

Optical Characteristics

	lte	em	Symbol	Condition	Min.	Тур.	Max.	Unit
Optimal	Тор		φΥ+		-	70	-	0
	Bott	tom	φΥ-	CR ≥ 10	-	70	-	0
Viewing Angles	Left		θХ-		-	70	-	0
Aligies	Righ	nt	θХ+		-	70	-	0
Contrast Rati	Contrast Ratio			-	200	350	-	-
Luminance	Luminance			I _{LED} = 40 mA	640	800	-	cd/m ²
Dosnonso Ti	ina	Rise	T _R	T - 25° C	-	25	40	ms
Response Ti	ime	Fall	T _F	T _{OP} = 25° C	-	25	40	ms
		Dod	X _R	-	0.554	0.604	0.654	-
		Red	Y_R	-	0.298	0.345	0.395	-
		Croon	X _G	-	0.263	0.313	0.363	-
Chromatic	:+.,	Green	Y_{G}	-	0.552	0.602	0.652	-
Chromatic	ity	Dlug	X _B	-	0.094	0.144	0.194	-
		Blue	Y _G	-	0.056	0.106	0.156	-
		White	X _W	-	0.229	0.279	0.329	-
		wille	Y _W	-	0.271	0.321	0.371	-

Driver Information

Built-in NV3035C driver. No controller.

Please download specification at http://www.newhavendisplay.com/app_notes/NV3035C.pdf

Note: To achieve optimum VCOM and VGL settings, the SPI interface may be used to set the following registers:

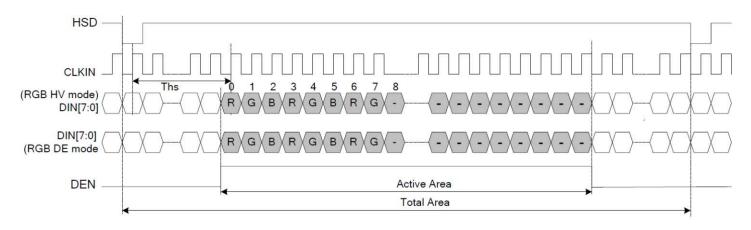
ROEh = 6BhROFh = 24h

Timing Characteristics

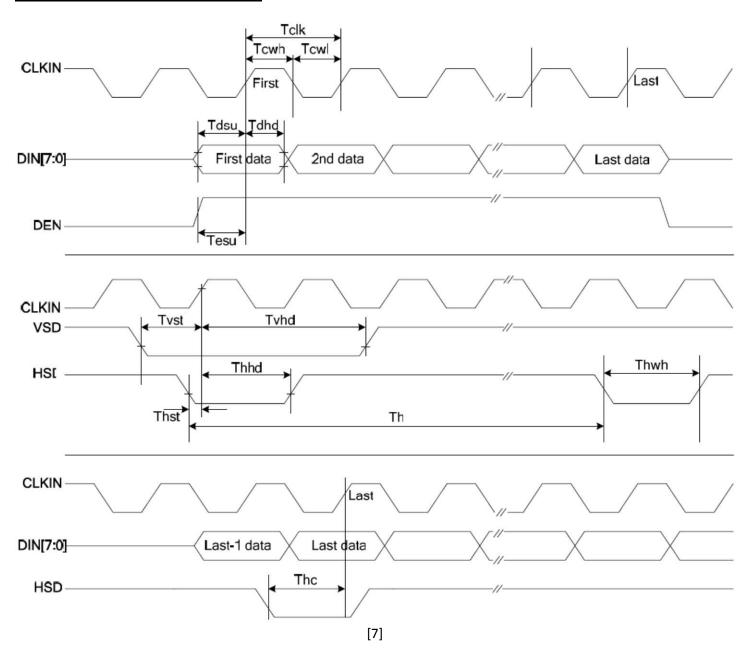
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
System Operation Timing						
VDD power source slew time	TPOR			1000	us	From 0V to 90% VDD
RSTB active pulse width	T _{RSTB}	40			us	VDD=3.3V
Input Output Timing	3 Access 5 Access 6	Alt.				
CLKIN clock time	Telk	-		35.7	ns	Please refer to timing table(P25)
HSD to CLKIN	The	-	-	1	CLKIN	
HSD width	Thwh	1	-	-	CLKIN	
VSD width	Tvwh	1	-	-	Th	
HSD period time	Th	60	63.56	67	us	
VSD setup time	Tvst	12		-	ns	
VSD hold time	Tvhd	12	-	-	ns	
HSD setup time	Thst	12	-	-	ns	
HSD hold time	Thhd	12	1	-	ns	
Data set-up time	Tdsu	12	-	-	ns	DIN[23:0] to CLKIN
Data hold time	Tdhd	12	-	-	ns	DIN[23:0] to CLKIN
DEN setup time	Tesd	12	-		ns	DEN to CLKIN
Time that VSD to 1st line data						@CIR601/8bit RGB HV mode
The second secon	Tvs	2	13	127	Th	Control by HDLY[6:0] setting
input						Tvs=HDLY[6:0]
Time that CCIR_V to 1st line	Tvs	12	20	28	Th	@CCIR656 NTSC mode Control by
data input	1 15	12	20	20	111	HDLY[6:0] setting Tvs=HDLY[6:0]
Time that CCIR_V to 1st line	Tvs	17	25	33	Th	@CCIR656 PAL mode Control by
data input	1 13	17	23	55	111	HDLY[6:0] setting Tvs=HDLY[6:0]
Time that VSD to 1st line data	Tvs	2	13	127	Th	@24bit RGB HV mode Control by
input		-			111	HDLY[6:0] setting Tvs=HDLY[6:0]
Source output stable time 1	Tst	-2	25	30	us	96% final, CL=30pF, RL=2K
Gate output stable time	Tgst	-	500	1000	ns	96% final, CL=40pF
VCOMOUT output stable time	Test	-	4 .	8	us	96% final, CL=33nF, RL=100ohm
3-wire serial communication AC					,	
Serial clock	Tspck	320	-	-	ns	
SPCK pulse duty	Tscdut	40	50	60	%	Tckh/Tspck
Serial data setup time	Tisu	120	11-21	-	ns	
Serial data hold time	Tihd	120	-		ns	
Serial clock high/low	Tssw	120			ns	
Chip select distinguish	Tcd	1	12	-	us	
SPENA to VSD	Tev	1	1.	-	us	
SPENB input setup time	Teck	150			Ns	
SPENB input hold time	Tcke	150	- 1	-	ns	

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
CLKIN frequency	Fclk	6.1	6.4	8.0	MHz	VDD=3.0~3.6V
CLKIN cycle time	Telk	125	156	164	ns	
CLKIN pulse duty	Tewh	40	50	60	%	Telk
Time that HSD to 1 st data input(NTSC)	Ths	40	70	255	CLKIN	DDLY=70,Offset=0(fixed)

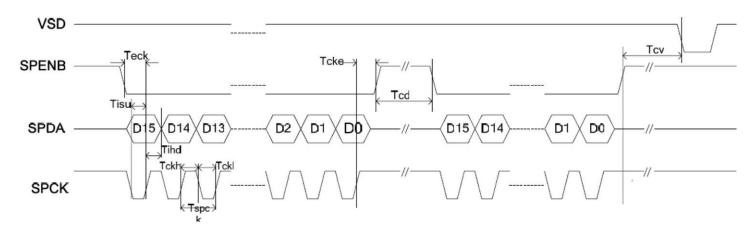
Input Data Format



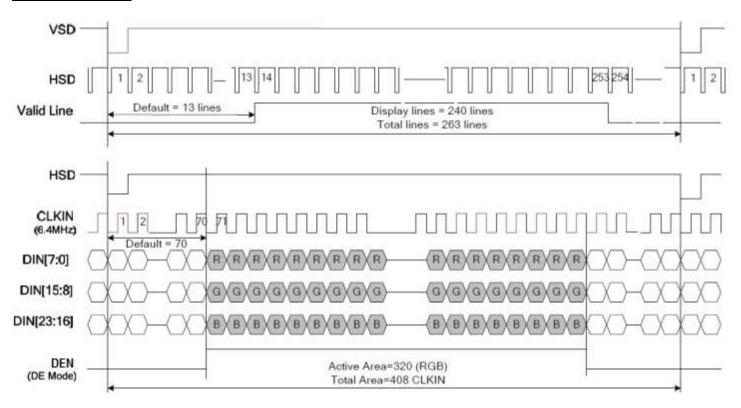
Clock and Data Input Timing Diagram



3-wire Timing Diagram



Input Data Timing



Quality Information

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	+70°C , 240hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C , 240hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.	+60°C , 240hrs	2
Low Temperature Operation	Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.	-20°C , 240hrs	1,2
High Temperature / Humidity Operation	Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time.	+60°C, 90% RH, 160hrs	1,2
Thermal Shock resistance	Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.	-30°C,30min -> 25°C,5min -> 80°C,30min = 1 cycle 100 cycles	
Vibration test	Endurance test applying vibration to simulate transportation and use.	10-55Hz , 15mm amplitude. 60 sec in each of 3 directions X,Y,Z For 15 minutes	3
Static electricity test	Endurance test applying electric static discharge.	VS=4KV, RS=330k Ω , CS=150pF Five times	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

Note 3: Test performed on product itself, not inside a container.

Precautions for using LCDs/LCMs

See Precautions at www.newhavendisplay.com/specs/precautions.pdf

Warranty Information and Terms & Conditions

http://www.newhavendisplay.com/index.php?main_page=terms