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# NHD-C0216AZ-FN-GBW

# **COG (Chip-on-Glass) Liquid Crystal Display Module**

NHD- Newhaven Display

CO216- COG, 2 lines x 16 characters

AZ- Model

F- Transflective
N- No Backlight
G- STN- Gray
B- 6:00 View Angle

W- Wide Temp  $(-20^{\circ} \text{ c} \sim +70^{\circ} \text{ c})$ 

**RoHS Compliant** 

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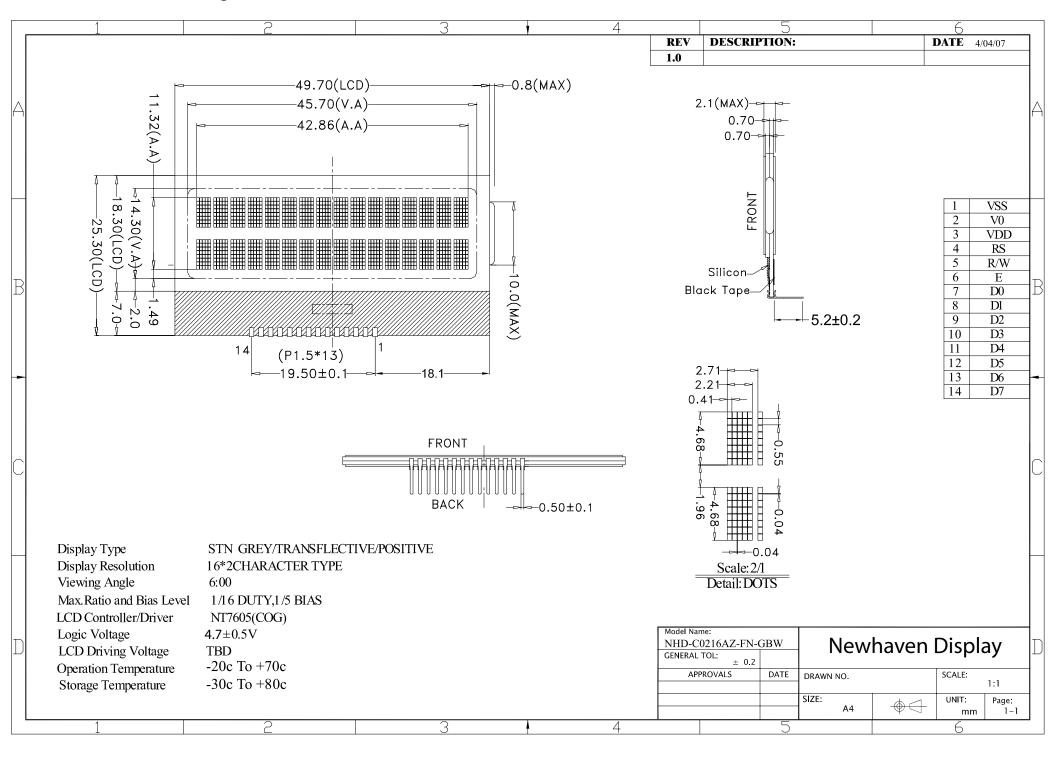
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**Document Revision History** 

Revision	Date	Description	Changed by
0	7/20/2007	Initial Release	-
1	8/1/2007	Edit temp. range errors	CL
2	6/4/2008	Edit incorrect pinout	CL
3	9/9/2009	User guide reformat	BE
4	10/9/2009	Updated Electrical Characteristics information	MC
5	10/15/2009	Updated Block Diagram	MC
6	6/2/2011	Timing characteristics updated	AK

#### **Functions and Features**

- 2 lines x 16 characters
- Built-in NT7605 controller
- 5x8 dots with cursor
- +5V power supply
- 1/16 duty, 1/5 bias
- RoHS Compliant

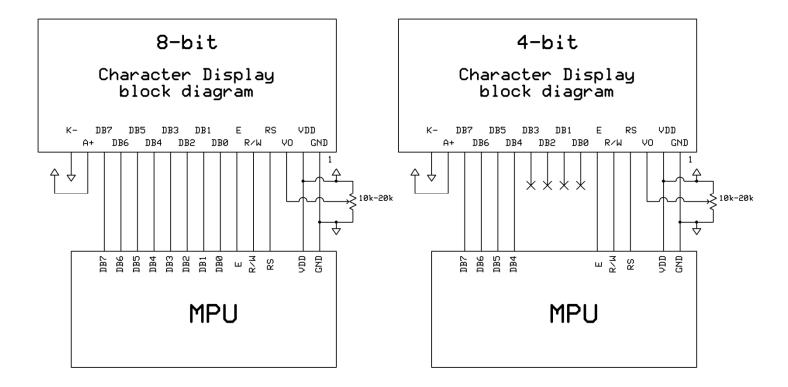


#### **Pin Description and Wiring Diagram**

Pin No.	Symbol	External	Function Description
		Connection	
1	Vss	Power Supply	Ground
2	Vo	Adj. Power supply	Power supply for contrast (approx. 0.3V)
3	VDD	Power Supply	Supply voltage for logic (5.0V)
4	Rs	MPU	Register select signal. RS=0: Command, RS=1: Data
5	R/W	MPU	Read/Write select signal, R/W=1: Read R/W=0: Write
6	E		Operation enable signal. Falling edge triggered.
7-10	DB0-DB3		Four low order bi-directional three state data bus lines. These four
		MPU	are not used during 4-bit operation.
11-14	DB4-DB7		Four high order bi-directional three state data bus lines.

Recommended LCD connector: 1.5 mm pitch, 14 pins Soldered to PCB

Backlight connector: --- Mates with: ---



#### **Electrical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Operating Temperature Range	Тор	Absolute Max	-20	-	+70	°C
Storage Temperature Range	Tst	Absolute Max	-30	-	+80	°C
Supply Voltage	VDD		4.7	5.0	5.5	V
Supply Current	IDD	Ta=25°C,VDD=5.0V	-	1.0	1.5	mA
Supply for LCD (contrast)	VDD-Vo	<b>Ta=25</b> °C	-	4.7	-	V
"H" Level input	VIH		2.2	-	VDD	V
"L" Level input	VIL		0	-	0.6	V
"H" Level output	Voн		2.4	-	-	V
"L" Level output	Vol		-	-	0.4	V

# **Optical Characteristics**

Item	Symbol	Condition	Min.	Тур.	Max.	Unit
Viewing Angle - Vertical	AV	Cr ≥ 2	-60	-	+35	0
Viewing Angle - Horizontal	AH	Cr ≥ 2	-40	-	+40	0
Contrast Ratio	Cr		-	6	-	-
Response Time (rise)	Tr	-	-	150	250	ms
Response Time (fall)	Tr	-	-	150	250	ms

#### **Controller Information**

Built-in NT7605. Download specification at <a href="http://www.newhavendisplay.com/app">http://www.newhavendisplay.com/app</a> notes/NT7605.pdf

## **Table of Commands**

Instruction	6				c	ode					Function	Execution time (max)
	RS	RW	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0		(fosc = 250KHz)
Display Clear	0	0	0	0	0	0	0	0	0	1	Clear entire display area, Restore display from shift, and load address counter with DD RAM address 00H.	1.64ms
Display/ Cursor Home	0	0	0	0	0	0	0	0	1	:: <u>+</u> :	Restore display from shift and load address counter with DD RAM address 00H.	1.64ms
Entry Mode Set	0	0	0	0	0	0	0	া	I/D	s	Specify direction of cursor movement and display shift mode. This operation takes place after each data transfer (read/write).	<b>4</b> 0µ <b>s</b>
Display ON/OFF	0	0	0	0	0	0	1	D	С	В	Specify activation of display (D) cursor (C) and blinking of character at cursor position (B).	40μs
Display/ Cursor Shift	0	0	0	0	0	1	S/C	R/L	;◆	10.00	Shift display or move cursor.	40µs
Function Set	0	0	0	0	1	DL	N	F	*	(*)	Set interface data length (DL), number of display line (N), and character font (F).	40µs
RAM Address Set	0	0	0	1			Ac	OG .			Load the address counter with a CG RAM address. Subsequent data access is for CG RAM data.	40μs
DD RAM Address Set	0	0	1				ADD	ļ			Load the address counter with a DD RAM address. Subsequent data access is for DD RAM data.	<b>4</b> 0μ <b>s</b>
Busy Flag/ Address Counter Read	0	1	BF				AC				Read Busy Flag (BF) and contents of Address Counter (AC).	1µs
CG RAM/ DD RAM Data Write	1	0	6		Write data Write di RAM.						Write data to CG RAM or DD RAM.	<b>4</b> 0μ <b>s</b>
CG RAM/ DD RAM Data Read	1	1				Read	data				Read data from CG RAM or DD RAM.	40µs
	S D C B S/C R/L DL N F BF	= 1 : Di = 1 : Di = 1 : Cu = 1 : Cu = 1 : Si = 1 : Si = 1 : B- = 1 : Du = 1 : 5x = 1 : Int	irsor Bli hift Disp hift Righ Bit hal Line 10 dots ternal O	nift On n splay Or nk On lay t	ř.	S/I R/ DL	C = 0: L = 0: L = 0: = 0:	Move C Shift Le 4-Bit	ursor aft Line		DD RAM : Display Data RAM  CG RAM : Character Generator RAM  ACG : Character Generator RAM Address  ADD : Display Data RAM Address  AC : Address Counter	

## **DDRAM address location:**

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F

## **Timing Characteristics**

#### **Read Operation**

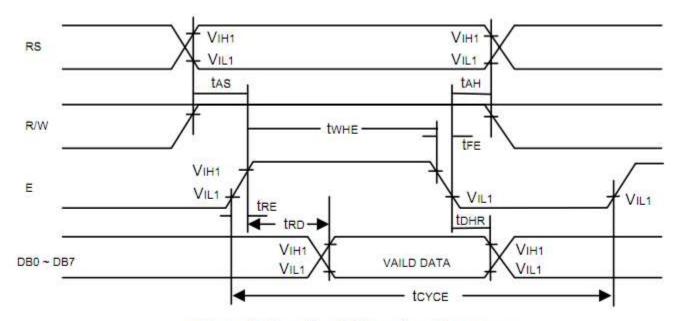


Figure 1. Bus Read Operation Sequence (Reading out data from NT7605 to MPU)

Read Cycle (VDD = 5.0V, GND = 0V, TA = 25°C)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Conditions
tcyce	Enable Cycle Time	500	-	-	ns	Figure 1
twhe	Enable "H" Level Pulse Width	300	-	-	ns	Figure 1
tre, tre	Enable Rise/Fall Time	-	-	25	ns	Figure 1
tas	RS, R/W Setup Time	60 <sup>1</sup>	-	-	ns	Figure 1
		100 <sup>2</sup>	1			
tah	RS, R/W Address Hold Time	10	-	-	ns	Figure 1
tro	Read Data Output Delay	-	-	190	ns	Figure 1
<b>t</b> DHR	Read Data Hold Time	20	-	-	ns	Figure 1

Notes: 1: 8-bit operation mode 2: 4-bit operation mode

#### **Write Operation**

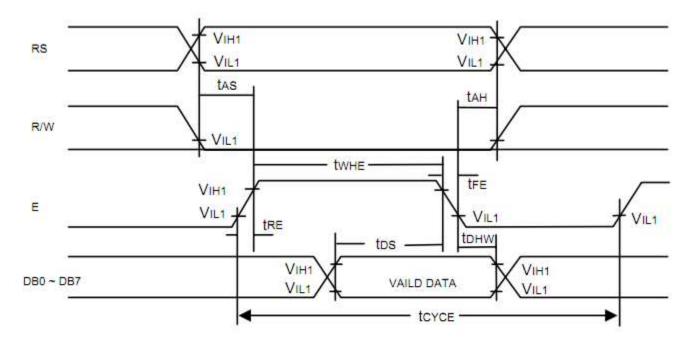


Figure 2. Bus Write Operation Sequence (Writing data from MPU to NT7605)

Write Cycle (VDD = 5.0V, GND = 0V, TA = 25°C)

Symbol	Parameter	Min.	Тур.	Max.	Unit	Conditions
tcyce	Enable Cycle Time	500	-	-	ns	Figure 2
twhe	Enable "H" Level Pulse Width	300	-	-	ns	Figure 2
tre, tre	Enable Rise/Fall Time	-	-	25	ns	Figure 2
tas	RS, R/W Setup Time	60 <sup>1</sup>	-	-	ns	Figure 2
		100 <sup>2</sup>				
tah	RS, R/W Address Hold Time	10	-	-	ns	Figure 2
tos	Data Output Delay	100	-	-	ns	Figure 2
tohw	Data Hold Time	10	-	-	ns	Figure 2

Notes: 1: 8-bit operation mode 2: 4-bit operation mode

## **Built-in Font Table**

Upper 4					I	I	I									
Lower Bits	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
xxxx0000	CG RAM (1)			0	a	P	`	P					9	Ξ.	α	þ
xxxx0001	(2)		!	1	A	Q	a	9			0	7	Ŧ	4	ä	q
xxxx0010	(3)		11	2	В	R	b	r			Г	1	ij	×	F	0
xxxx0011	(4)		#	3	C	S	C	s			7	Ċ	Ť	ŧ	w	60
xxxx0100	(5)		\$	4	D	T	d	t.				I	ŀ	þ	Н	Ω
xxxx0101	(6)		%	5	E	U	e	u			-	7	t	ュ	σ	ü
xxxx0110	(7)		&	6	F	Ų	f	V			7	Ħ		3	ρ	Σ
xxxx0111	(8)		7	7	G	W	9	W			7	<b>†</b>	Z	Ŧ	9	π
xxxx1000	(1)		(	8	H	X	h	X			4	7	ネ	IJ	5	$\overline{\mathbf{x}}$
xxxx1001	(2)		)	9	I	Υ	i	У			Ċ	ኃ	J	լե	-1	y
xxxx1010	(3)		*	=	J	Z	j	z			I		n	V	j	Ŧ
xxxx1011	(4)		+	;	K		k	{			7	Ħ	L		×	ħ
xxxx1100	(5)		,	<	L	¥	1				t	Ð	J	ס	4	Ħ
xxxx1101	(6)			=	M	]	M	)			ュ	Z	^	ر	Ł	÷
xxxx1110	(7)			>	N	^	n	÷			3	せ	#	*	ħ	
xxxx1111	(8)		•	?	0		0	÷			ij	y	7		ö	

# **Example Initialization Program**

'INIT	
A = &H30	
Call Writecom	'wake up
Waitms 100	
Call Writecom	'wake up
Waitms 10	
Call Writecom	'wake up
Waitms 10	
A = &H38	
'function set	
Call Writecom	
A = &H10	
'shift display=no	
Call Writecom	
A = &HOC	
'display on	
Call Writecom	
A = &H06	
'entry mode set	
Call Writecom	
1	
Sub Writecom	
P1 = A	
Reset P3.0	
'instruction	
Reset P3.7	
'RW	
Waitms 1	
Set P3.4	
'E	
Waitms 1	
Reset P3.4	<b>'</b> E
End Sub	
Sub Writedata	
P1 = A	
Set P3.0	
'data	
Reset P3.7	
'RW	
Waitms 1	
Set P3.4	
'E	
Waitms 1	
Reset P3.4	'E
End Sub	
•	

## **Quality Information**

Test Item	Content of Test	Test Condition	Note
High Temperature storage	Endurance test applying the high storage temperature for a long time.	+80°C , 48hrs	2
Low Temperature storage	Endurance test applying the low storage temperature for a long time.	-30°C , 48hrs	1,2
High Temperature Operation	Endurance test applying the electric stress (voltage & current) and the high thermal stress for a long time.	+70°C , 48hrs	2
Low Temperature Operation	Endurance test applying the electric stress (voltage & current) and the low thermal stress for a long time.	-20°C , 48hrs	1,2
High Temperature / Humidity Operation	Endurance test applying the electric stress (voltage & current) and the high thermal with high humidity stress for a long time.	+40°C, 90% RH, 48hrs	1,2
Thermal Shock resistance	Endurance test applying the electric stress (voltage & current) during a cycle of low and high thermal stress.	0°C,30min -> 25°C,5min -> 50°C,30min = 1 cycle 10 cycles	
Vibration test	Endurance test applying vibration to simulate transportation and use.	10-55Hz , 15mm amplitude. 60 sec in each of 3 directions X,Y,Z For 15 minutes	3
Static electricity test	Endurance test applying electric static discharge.	VS=800V, RS=1.5k $\Omega$ , CS=100pF One time	

Note 1: No condensation to be observed.

Note 2: Conducted after 4 hours of storage at 25°C, 0%RH.

**Note 3:** Test performed on product itself, not inside a container.

# **Precautions for using LCDs/LCMs**

See Precautions at <a href="https://www.newhavendisplay.com/specs/precautions.pdf">www.newhavendisplay.com/specs/precautions.pdf</a>

#### **Warranty Information and Terms & Conditions**

http://www.newhavendisplay.com/index.php?main\_page=terms