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■GENERAL DESCRIPTION

NJG1507R is a GaAs SPDT switch IC which exhibits low loss and high isolation, and ideally suitable for T/R switch of the digital wireless phone.

This switch is operated in the wide frequency range from 50MHz to 3.0GHz at low operating voltage from +2.5V with small VSP8 package.

■PACKAGE OUTLINE



NJG1507R

■FEATURES

RC

- Single and low positive supply voltage
- Low insertion loss
- Transmission power
- High isolation
- Low control current
- Package

■PIN CONFIGURATION

R TYPE (Top View) 1 2 3 777 4 777 6 777 6 777 5

+2.5~+5.5V 0.5dB typ. @ f=2GHz, P_{in}=22dBm 27dBm max. @ f=2GHz, V_{CTL}=3.0V 33dB typ. @ f=2GHz, P_{in}=22dBm 5uA typ. @ f=0.05~2.5GHz, P_{in}=22dBm VSP8 (Mount Size: 4.0x2.9x1.2mm)

Pin Connection
$1.V_{CTR2}$
2.P2
3.GND
4.GND
5.PC
6.GND
7.P1
8.V _{CTR1}

■TRUTH TABLE

"H"=V_{СТВ (Н),} "L"=V_{СТВ (L)}

V _{CTR1}	Н	L	L	н
V _{CTR2}	L	Н	L	н
P1-PC	OFF	ON	Loss =15dB P ₁ Return Loss =-3dB	Loss =16dB P₁ Return Loss =-2dB
P2-PC	ON	OFF	Loss =15dB P ₂ Return Loss =-3dB	Loss =16dB P ₂ Return Loss =-2dB

Note) The values of "Loss" and "Return Loss" are typical values at 2.0GHz.

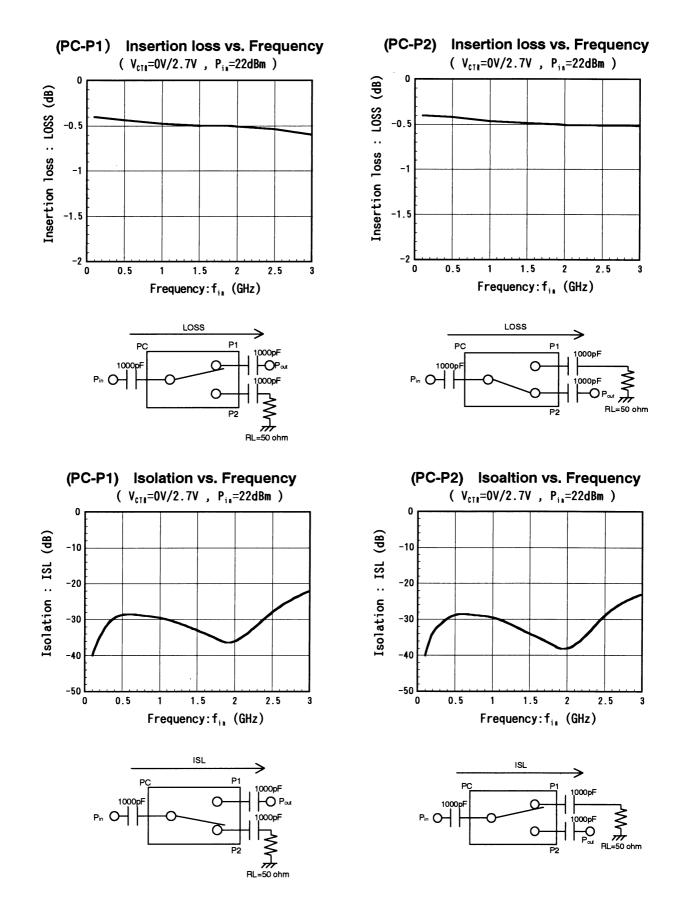
■ABSOLUTE MAXIMUM RATINGS

		(T _a =25°C, Z _s =	=Z _I =50Ω)
PARAMETER	SYMBOL	RATINGS	UNITS
Input power	P _{in}	33	dBm
Control voltage	V_{CTR}	6.0	V
Power dissipation	PD	320	mW
Operating Temp.	T _{opr}	-30~+85	°C
Storage Temp.	T _{stg}	-40~+150	°C

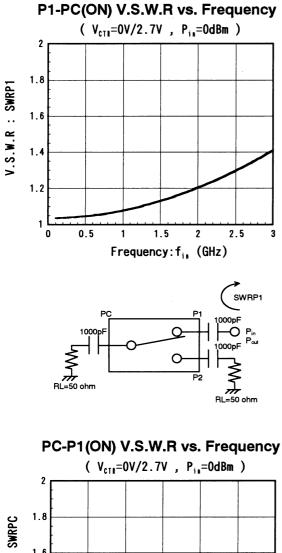
■ELECTRICAL CHARACTERISTICS

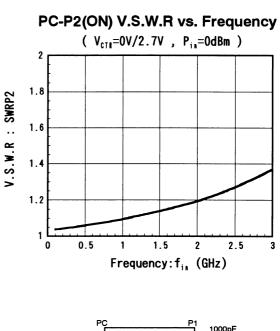
$(V_{CTR (L)}=0V, V_{CTR (H)}=2.7V, Z_{s}=Z_{o}=50\Omega, T_{a}=25^{\circ}C)$							
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS	
Control voltage (L)	V _{CTR (L)}	f=0.05~2.5GHz, P _{in} =22dBm	-0.2	0	0.2	V	
Control voltage (H)	V _{CTR (H)}	f=0.05~2.5GHz, P _{in} =22dBm	2.5	2.7	5.5	V	
Control current	I _{CTR}	f=0.05~2.5GHz, P _{in} =22dBm	-	5.0	8.0	uA	
Insertion loss1	LOSS1	f=1.0GHz, P _{in} =22dBm	-	0.4	0.7	dB	
Insertion loss2	LOSS2	f=2.0GHz, P _{in} =22dBm	-	0.5	0.8	dB	
Isolation 1 (PC-P1, PC-P2, P1-P2)	ISL1	f=1.0GHz, P _{in} =22dBm	25	31	-	dB	
Isolation 2 (PC-P1, PC-P2, P1-P2)	ISL2	f=2.0GHz, P _{in} =22dBm	25	33	-	dB	
Pin at 1dB compression point 1	P _{-1dB (1)}	f=2.0GHz	26	28	-	dBm	
Pin at 1dB compression point 2	P-1dB (2)	V _{CTR (H)=} 3.0V, f=2.0GHz	27	30	-	dBm	
VSWR	VSWR	f=0.05~2.5GHz, ON STATE	-	1.2	1.5		
Switching time	T _{sw}	f=0.05~2.5GHz	-	15	-	ns	

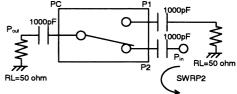
■TYPICAL CHARACTERISTICS



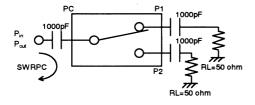
TYPICAL CHARACTERISTICS



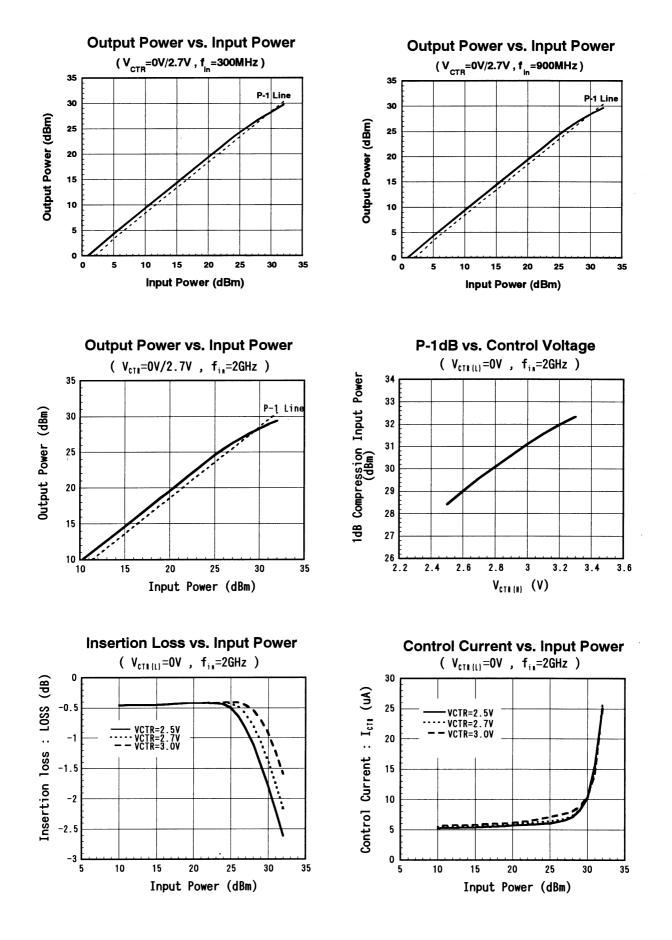




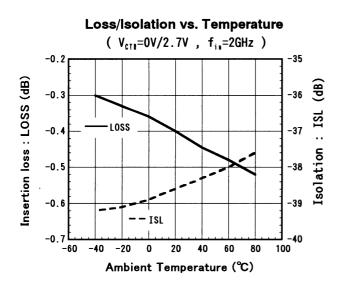
PC-P1(ON) V.S.W.R vs. Frequency $(V_{CII}=0V/2.7V, P_{i_1}=0dBm)$

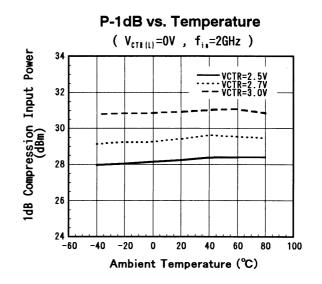


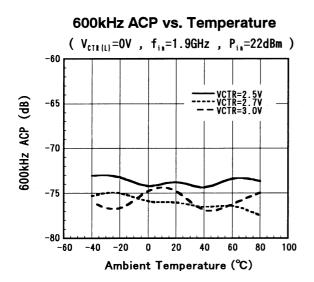
■TYPICAL CHARACTERISTICS

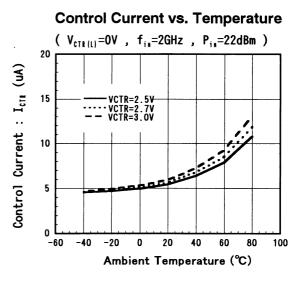


TYPICAL CHARACTERISTICS









■TYPICAL CHARACTERISTICS

```
(Ta=25°C)
600kHz ACP
    DQPSK Modulation Signal (without D.U.T)
       f<sub>in</sub>=1.9GHz P<sub>in</sub>=22dBm
                            RNYQ \alpha = 0.5 1/4 \pi DQPSK
      MODULATION : 384Kbps
     ATTEN 2008
      RL 10.0d5m
                             1023/
                                          FWR
MAX
                                                        17.508m
71.3305
                                                T×
                                   MAYINA
                -71.83dB
-72.27dB
        LOW
        UP:
                500.04Hz
192.04Hz
        CSP
        CBW
                                <del>م</del>حم
                                         Y.K
                           M
                                            Ynen
        show
                                                   must frank
      CENTER 1. 900000GHz
                                             SPAN 3. OOOMHz
     #RBW 1. OKHz #VBW 10KHz
                                               -SWP 10.2sec
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D.U.T Output Signal

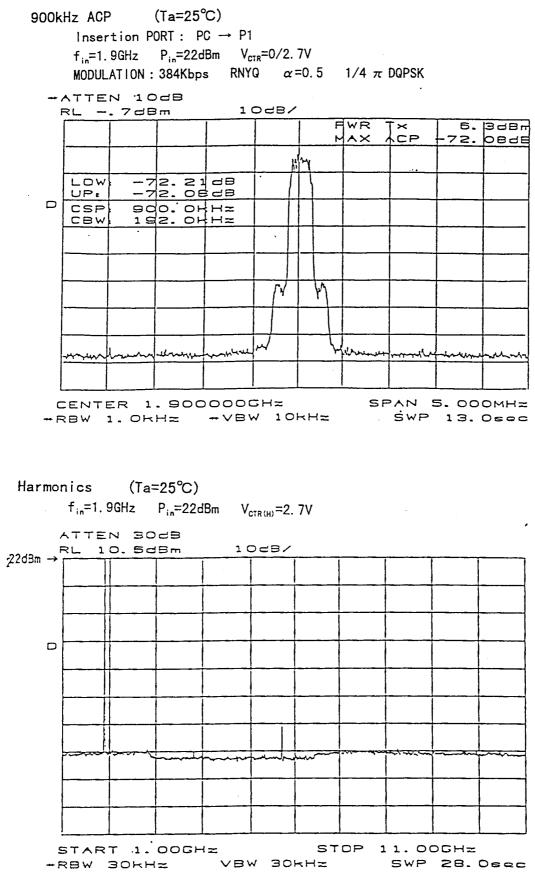
Insertion PORT: $PC \rightarrow P1$ f_{in}=1.9GHz P_{in}=22dBm V_{CTR}=0/2.7V

MODULATION : 384Kbps RNYQ $\alpha = 0.5$ 1/4 π DQPSK

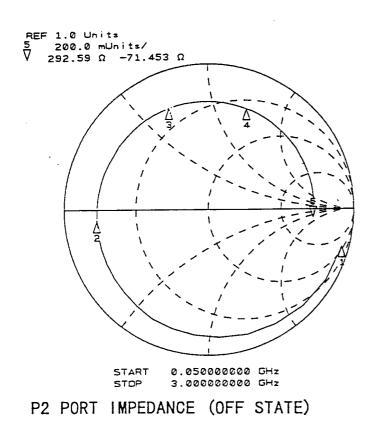
+ATTEN 2008

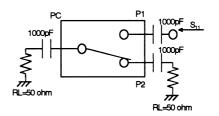
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TYPICAL CHARACTERISTICS

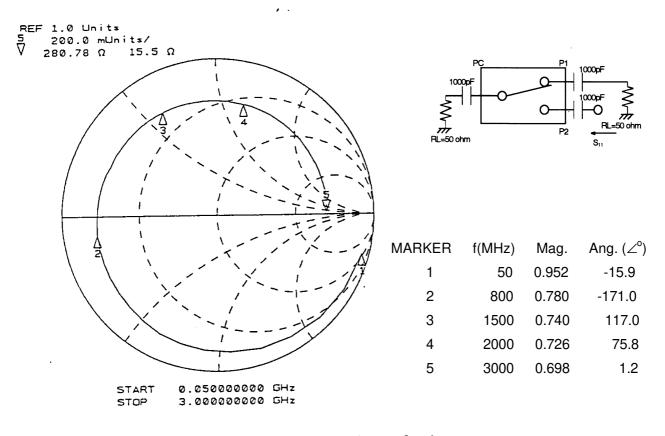


■TYPICAL CHARACTERISTICS P1 PORT IMPEDANCE (OFF STATE)





f(MHz)	Mag.	Ang. (\angle°)
50	0.952	-15.9
800	0.780	-171.0
1500	0.740	117.0
2000	0.726	75.8
3000	0.698	1.2
	50 800 1500 2000	50 0.952 800 0.780 1500 0.740 2000 0.726



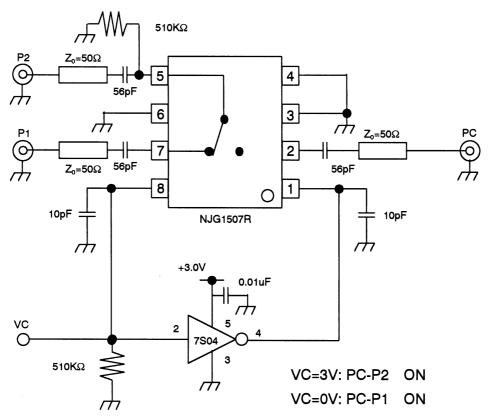
■TYPICAL CHARACTERISTICS

	P1 F	PORT	P2 P	ORT		
f(MHz)	Mag.	Ang.(∠°)	Mag.	Ang.(∠°)		
50	0.951	-15.8	0.952	-15.9		
100	0.941	-30.9	0.938	-31.0		
200	0.911	-59.2	0.911	-59.2		
300	0.879	-84.9	0.877	-84.6		
400	0.849	-107.4	0.850	-106.7		
500	0.823	-127.2	0.824	-126.0		
600	0.803	-144.6	0.805	-142.8		
700	0.787	-160.1	0.791	-157.6		
800	0.777	-174.1	0.780	-171.0		
900	0.768	173.1	0.770	176.8		
1000	0.761	161.2	0.763	165.5		
1100	0.750	149.6	0.749	154.2		
1200	0.751	139.8	0.749	144.7		
1300	0.748	129.7	0.745	135.1		
1400	0.748	120.1	0.744	125.9		
1500	0.745	110.9	0.740	117.0		
1600	0.746	102.1	0.737	108.5		
1700	0.743	93.6	0.732	100.3		
1800	0.742	85.1	0.732	91.9		
1900	0.740	76.9	0.728	83.7		
2000	0.739	68.9	0.726	75.8		
2100	0.740	61.3	0.725	68.3		
2200	0.739	53.3	0.723	60.5		
2300	0.735	45.8	0.718	52.7		
2400	0.735	38.4	0.717	45.2		
2500	0.736	30.8	0.714	37.7		
2600	0.732	23.8	0.711	30.5		
2700	0.734	17.0	0.708	23.4		
2800	0.729	9.5	0.706	15.8		
2900	0.727	2.5	0.703	8.6		
3000	0.721	-4.6	0.698	1.2		

Scattering Parameters: S11 (OFF STATE)

(V_{CTR}=0/2.7V, 50 Ω System)

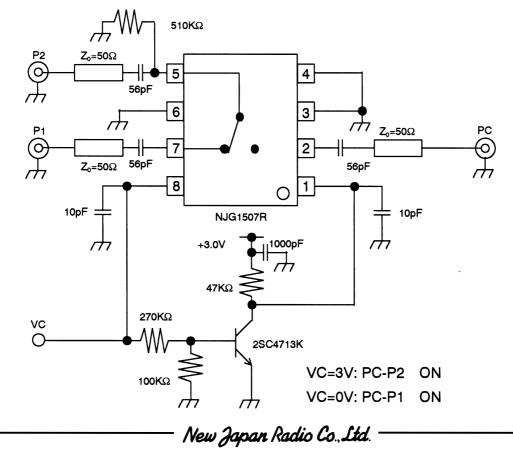
■APPLICATION CIRCUIT 1: Single control signal operation by using C-MOS inverter.



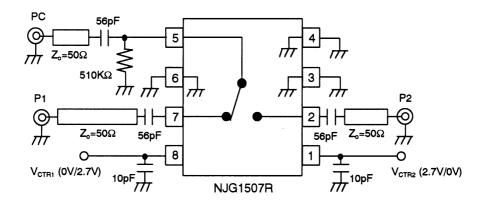
[1]Please connect bypass capacitors to the supply terminals of the C-MOS inverter.

[2]In order to stabilize input impedance of inverter, please pull down using $510K\Omega$ resister from the input terminal of the C-MOS inverter to the ground plane.

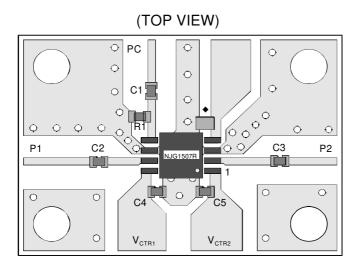
■APPLICATION CIRCUIT 2: Single control signal operation by using a transistor.



■TEST CIRCUIT



■RECOMMENDED PCB DESIGN



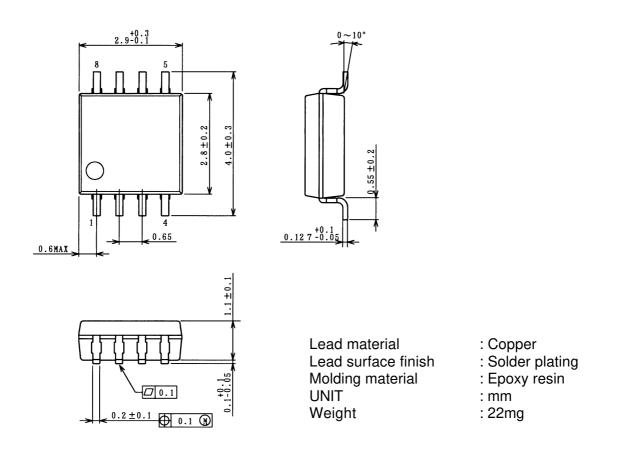
PCB SIZE=19.5x14.0mm PCB: FR-4, t=0.2mm CAPACITOR: size 1005 STRIPLINE WIDTH=0.5mm C1~C3: 56pF C4, C5: 10pF R1: 510KΩ ◆: Please short between Pin4 and ground plane directly as

close as possible.

Precautions

- [1]External capacitors should be connected to the input and output RF terminals (P1, P2, PC) to block the DC current. The above example is a circuit at 1.9GHz. Please select the capacitor value suitable for actual frequency from 10pF to 1000pF.
- [2]Decoupling capacitors should be connected to the control terminals (V_{CTR1} , V_{CTR2}) as close as possible. The values of these capacitors should be selected from 5pF to 100pF range. Please consider that these values are very effective to switching time (Larger capacitor gives longer switching time).
- [3]In order to keep good isolation characteristics, the ground terminals (3, 4, 6pin) should be connected to the ground pattern with wider width as close as possible, and through-hole in the ground plane should also be placed as close as possible.

PACKAGE OUTLINE (VSP8)



Cautions on using this product

- This product contains Gallium-Arsenide (GaAs) which is a harmful material.
- Do NOT eat or put into mouth.
- Do NOT dispose in fire or break up this product.
- Do NOT chemically make gas or powder with this product.
- To waste this product, please obey the relating law of your country.

[CAUTION]

The specifications on this databook are only given for information , without any guarantee as regards either mistakes or omissions. The application circuits in this databook are described only to show representative

usages of the product and not intended for the guarantee or permission of any right including the industrial rights.

This product may be damaged with electric static discharge (ESD) or spike voltage. Please handle with care to avoid these damages.