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## **DUAL OPERATIONAL AMPLIFIER**

#### **■ GENERAL DESCRIPTION**

The NJM14558 is dual operational amplifier, which can operate from ±2V supply. The features are low offset voltage, low bias current and low current consumption.

The package lineup is DIP, DMP and others, so that the NJM14558 is suitable for portable audio and any kind of signal amplifier.

#### **■ FEATURES**

( ±2.0V~±7.0V ) Operating Voltage • Input Offset Voltage (3mV max.) Slew Rate (2.5V/µs typ.)

Bipolar Technology

 Package Outline DIP8, DMP8, EMP8, SSOP8,

VSP8,SIP8

#### **■ PACKAGE OUTLINE**







NJM14558M



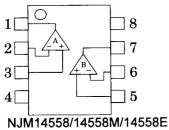




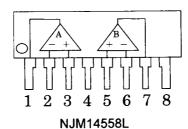




#### **■ PIN CONFIGURATION**

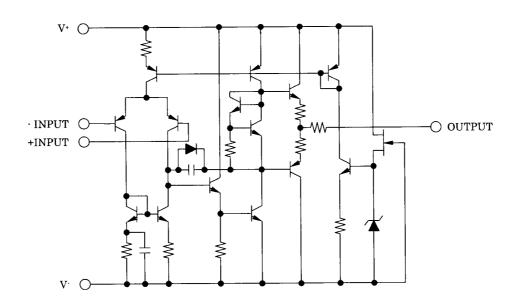


NJM14558V/14558R



**PIN FUNCTION** 1.A OUTPUT 2.A -INPUT 3.A +INPUT 4.V 5.B +INPUT 6.B -INPUT **7.B OUTPUT** 

### ■ EQUIVALENT CIRCUIT (1/2 Shown)



### ■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

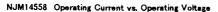
PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V <sup>+</sup> /V	± 7.5	V
Differential Input Voltage	$V_{\text{ID}}$	± 14	V
Input Voltage	V <sub>IC</sub>	±7 ( note )	V
Power Dissipation	P <sub>D</sub>	( DIP8 ) 500 ( DMP8 ) 300 ( EMP8 ) 300 ( SSOP8 ) 250 ( VSP8 ) 320 ( SIP8 ) 800	mW
Operating Temperature Range	T <sub>opr</sub>	-40~+85	°C
Storage Temperature Range	T <sub>stg</sub>	-40~+125	°C

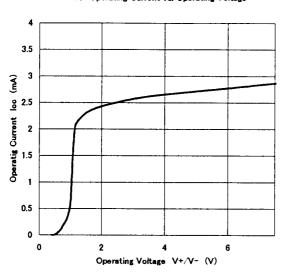
( note ) For supply voltage less than  $\pm 7$ V, the absolute maximum input voltage is equal to the supply voltage.

### **■ ELECTRICAL CHARACTERISTICS**

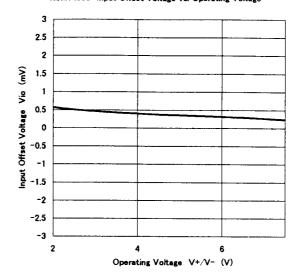
( V<sup>+</sup>/V<sup>-</sup>=±5V,Ta=25°C )

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	$V_{opr}$		±2	-	±7	V
Input Offset Voltage	V <sub>IO</sub>	R <sub>S</sub> ≤10kΩ	-	0.5	3	mV
Input Offset Current	I <sub>IO</sub>		-	5	50	nA
Input Bias Current	$I_{B}$		-	70	250	nA
Input Resistance	R <sub>IN</sub>		0.3	5	-	ΜΩ
Large Signal Voltage Gain	Av	R <sub>L</sub> ≥2kΩ,V <sub>O</sub> =±3V	86	100	-	dB
Maximum Output Voltage Swing (+)	$V_{OM}^{\dagger}$	R <sub>L</sub> ≥2kΩ	3.5	4.0	-	V
Maximum Output Voltage Swing ( - )	V <sub>OM</sub>	R <sub>L</sub> ≥2kΩ	-	-3.5	-3.0	V
Input Common Mode Voltage Range	V <sub>ICM</sub>		± 3.0	± 4.0	-	V
Common Mode Rejection Ratio	CMR	R <sub>S</sub> ≤10kΩ	70	90	-	dB
Supply Voltage Rejection Ratio	SVR	R <sub>S</sub> ≤10kΩ	76.5	90	-	dB
Operating Current	Icc		-	2.7	4.5	mA
Slew Rate	SR		-	2.5	-	V/µs
Equivalent Input Noise Voltage	$V_{NI}$	RIAA,R <sub>S</sub> =2.2kΩ,30kHz:LPF	-	1.4	-	μVrms
Gain Bandwidth Product	GB		-	5	-	MHz

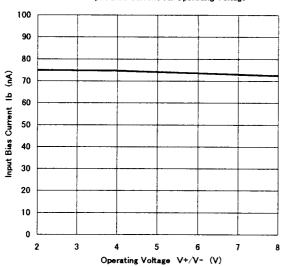




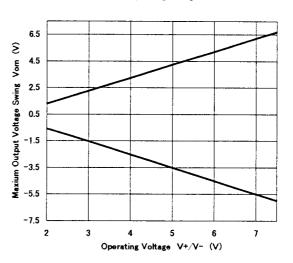
### NJM14558 Input Offset Voltage vs. Operating Voltage



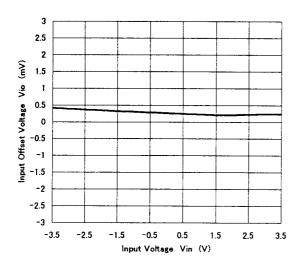
NJM14558 Input Bias Current vs. Operating Voltage



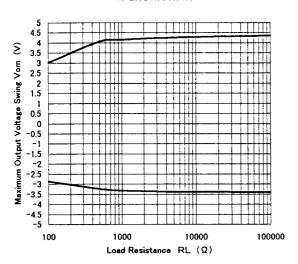
NJM14558 Maximum Output Voltage Swing vs. Operating Voltage



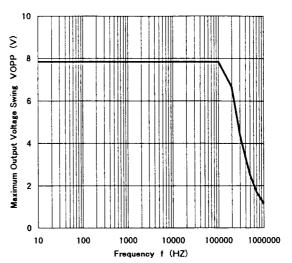
NJM14558 Input Common Mode Voltage Range

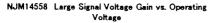


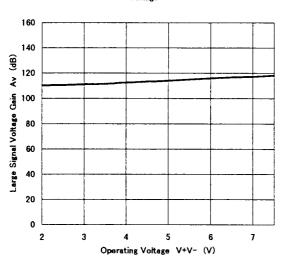
NJM14558 Maximum Output Voltage Swing vs. Load Resistance



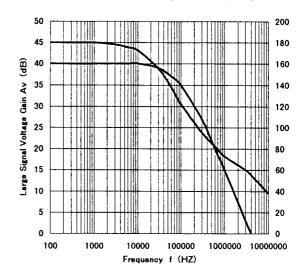
NJM14558 Maximum Output Voltage Swing vs. Frequency



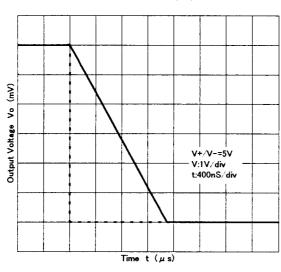




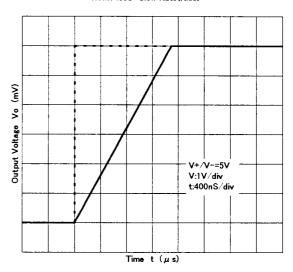
#### NJM14558 Large Signal Voltage Gain vs. Frequency



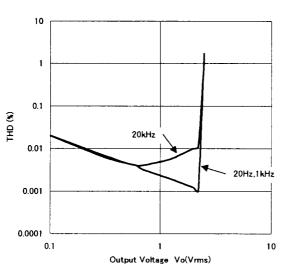
NJM14558 Slew Rate(Fall)



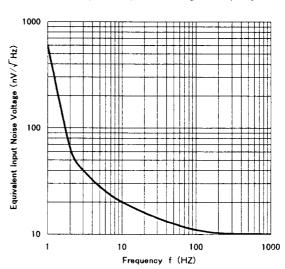
NJM14558 Slew Rate(Rise)



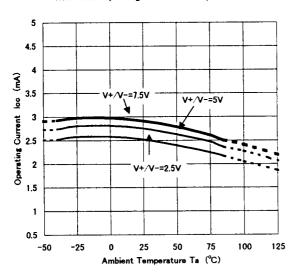
NJM14558 THD vs. Output Voltage



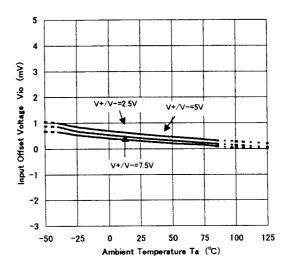
NJM14558 Equivalent Input Noise Voltage vs. Frequency



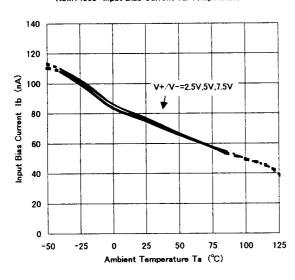
NJM14558 Operating Current vs. Temperature



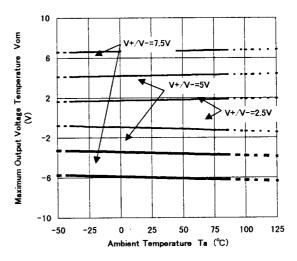
NJM14558 Input Offset Voltage vs. Temperature

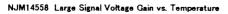


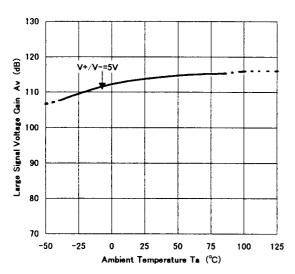
NJM14558 Input Bias Current vs. Temperature



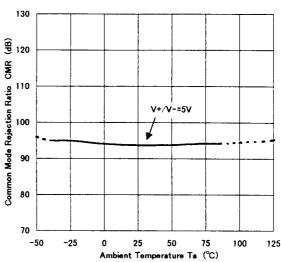
NJM14558 Maximum Output Voltage Swing vs. Temperature



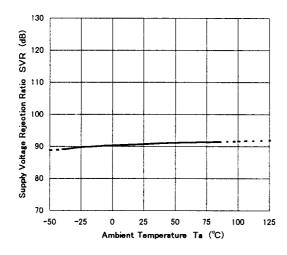




#### NJM14558 Common Mode Rejection Ratio vs. Temperature



#### NJM14558 Supply Voltage Rejection Ratio vs. Temperature



### [CAUTION]

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