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Contact us

Tel: +86-755-8981 8866 Fax: +86-755-8427 6832

Email & Skype: info@chipsmall.com Web: www.chipsmall.com

Address: A1208, Overseas Decoration Building, #122 Zhenhua RD., Futian, Shenzhen, China









VIDEO COLOR SUPERIMPOSER

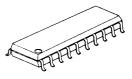
■ GENERAL DESCRIPTION

NJM2256 is the multi-functional color super-imposer IC for videobase band (Y, R-Y, B-Y), Various type of Y, R-Y, B-Y output signals can bemade by the digital controlled signals.

The signal control at the base band, made it possible on operation with less extermal parts, as well as for non adjustment on operation.

NJM2256 can be operated much higher switching speed comparing to **NJM2247**.

■ PACKAGE OUTLINE



NJM2256M

■ FEATURES

- 5V single Power Supply
- 8 Types Color Super-imposer
- Burst Flag Insert Function
- Y Inversion, C Inversion Function
- NTSC / PAL Matching
- Non Operational Adjustment
- Less External Parts
- Higher switching speed can be made comparing to NJM2247
- Package Outline DMP20
- Bipolar Technology

■ RECOMMENDED INPUT CONDITIONS

Y Signal 0.7V_{P→P}

R-Y Signal 1.0V_{P-P}

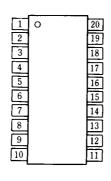
• B-Y Signal 0.7V_{P-P}

Control Voltage

• Low Level 0 to 0.25V

• High Level 4.75 to 5V

■ PIN CONFIGURATION



Pin Function

Y_{out}
 Y⁺
 HBF Pulse
 R
 BF Pulse
 G
 NTSC/PAL Switching

5. B 15. Clamp Pulse
6. B-Y_{in} 16. Character Pulse

7. B-Y_{out} 17. Yin

8. R-Y_{in} 18. Inversion Set Up Correction

9. R-Y_{out} 19. Y Inversion 10. C Inversion 20. BLK Pulse

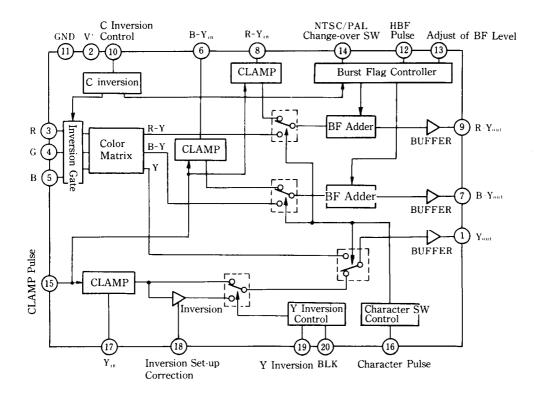
NJM2256M

■ CONTROL PIN CHARACTERISTICS

(\	/+:	=5	V

PIN NO.	PIN FUNCTIONS	THRESHOL	D LEVEL (V)	SINK / SOURCE	CURRENT (µA)
1 IIV IVO.	T INT ONCTIONS	LOW	HIGH	0V	5V
3 4 5 3	R G B	0.7	0.8	-500	500
4 5	(at C Inversion)	2.5	2.6	-100	100
10	C Inversion	3.5	4.5	-200	400
12	HBF Pulse	0.5	2.0	-2	1
14	NTSC / PAL	0.7	0.8	0	150
15	Clamp Pulse	2.5	2.8	-2	0
16	Character Pulse	0.5	0.9	-0.5	0
19	Y Inversion	0.4	0.8	-0.5	0
20	BLK Pulse	0.4	0.8	-0.5	0

■ BLOCK DIAGRAM



■ INFORMATIONS

Following four points are the outstanding function of the **NJM2256**. These functions are to go through three input (Y, R-Y, B-Y) signals control by ten control pins.

1. Color Superimpose

DC Level of each equivalent colors shall be supplied to Y, R-Y and B-Y inputs.

2. Burst Flag Insertion

150 mV burst flag shall be added to R-Y, B-Y input signals.

Burst flag is selected by the NTSC / PAL switch.

3. C Inversion

The color phase of the picture shall be inverted for one hundred and eighty degrees. The color phase of the imposed character shall not be altered. This function shall be proceeded when inverting the burst flag, and at the same time, the imposed character level shall be inverted too.

4. Y Inversion

It is the brightness level inversion. The imposed character color shall not be changed. This function shall be proceeded the switching Y signal output to the inverter side.

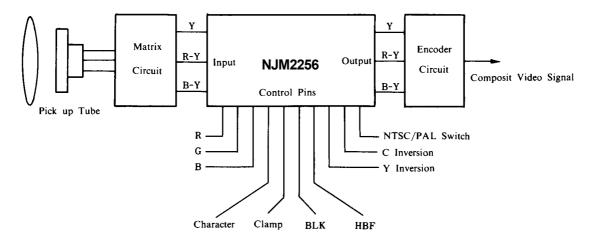


Fig.1 Video Camera Application

■ EQUIVALENT CIRCUIT

EQUIVAL	EQUIVALENT CIRCUIT								
PIN NO.	PIN FUNCTION	INSIDE EQUIVALENT CIRCUIT	PIN NO.	PIN FUNCTION	INSIDE EQUIVALENT CIRCUIT				
1	Yout	V+ 1	6	B-Y _{in}	6 REF.				
2	V ₊								
3	R	3 v·	7	B-Y _{out}	7				
4	G	4 V·	8	R-Y _{in}	8 REF.				
5	В	5 V*	9	R-Y _{out}	V+ 9				

■ EQUIVALENT CIRCUIT

	PIN			PIN	
PIN NO.	FUNCTION	INSIDE EQUIVALENT CIRCUIT	PIN NO.	FUNCTION	INSIDE EQUIVALENT CIRCUIT
10	C Inversion	5	15	Clamp Pulse	V+
11	GND		16	Character Pulse	16
- 11	GIND				,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,
12	HBF Pulse	12	17	Yin	V+ PULSE REF.
13	BF Level	13	18	Inversion Set up Correction	18
14	NTSC/PAL	14 V+	19 20	Y Inversion BLK	19 20

NJM2256

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V ⁺	8	V
Power Dissipation	P _D	350	mW
Operating Temperature Range	T _{opr}	-20 to +75	°C
Storage Temperature Range	T _{stg}	-40 to +125	°C

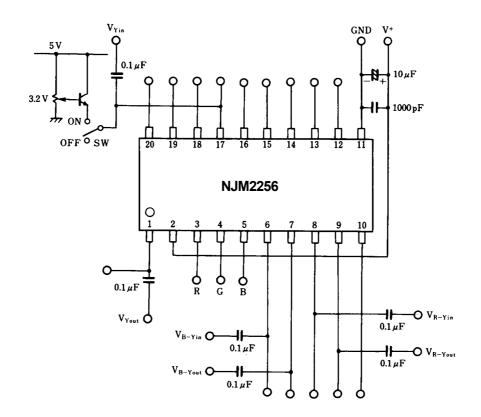
■ ELECTRICAL CHARACTERISTICS

PARAMETER	SYMBOL				COI	NTF	ROL	.PIN	1			TEST CONDITION	MIN.	TYP.	MAX.	UNIT
		3	4	(5)	10	12	14)	15	16	19	20					
Operating Current	Ι _{cc}	0	0	0	0	0	0	0	0	0	0		12	18.5	26	mA
Terminal Sink Current 1	117	0	0	0	0	0	0	0	0	0	0	V(17) = 2.5V Current when application	0		10	μΑ
Terminal Sink Current 1	16	0	0	0	0	0	0	0	0	0	0	V 6= 3.0V Current when application	0		6	μΑ
Terminal Sink Current 3	18	0	0	0	0	0	0	0	0	0	0	V 8=3.0V Current when application	0		6	μΑ
Terminal Voltage 1	V_1	0	0	0	0	0	5	0	0	0	0	① Open Voltage	1.68		1.92	V
Terminal Voltage 2	V_7	0	0	0	0	0	5	0	0	0	0	Open Voltage	2.18		2.42	V
Terminal Voltage 3	V 9	0	0	0	0	0	5	0	0	0	0	Open Voltage	2.18		2.42	V
Terminal Voltage 4	V ₁₃	0	0	0	0	0	5	0	0	0	0	① Open Voltage	0.23		0.37	V
Terminal Voltage 5	V ₁₈	0	0	0	0	0	5	0	0	0	0	(18) Open Voltage	1.68		1.92	V
Y Non Inversion																
Voltage Gain	G_YP	0	0	0	0	0	0	0	0	0	0	$V(Y_{IN}) = 1V_{P-P}, 1MHz$	-0.5	0	0.5	dB
Frequency	DG_P	0	0	0	0	0	0	0	0	0	0	G_{YP} (6MHz) – G_{YP} (1MHz)	-1	0	1	dB
Differential Gain	DP _P	0	0	0	0	0	0	0	0	0	0	$V(Y_{IN}) = 1V_{P-P}$, Standard Staircase	-3	0	3	%
Differential Phase	DP_P	0	0	0	0	0	0	0	0	0	0		-3	0	3	deg
Y Inversion Voltage Gain Frequency	G _{YN} G _{FYN}	0	0	0	0	0	0	0	0	5 5	5 5	$V(Y_{IN}) = 0.6V_{P-P}, 1MHz$ $G_{YN}(6MHz) - G_{YN}(1MHz)$	-2.3 -2	-1.3 -0.1	0.3	dB dB
Differential Gain	DG _N	0	0	0	0	0	0	0	0	5	5	$V(Y_{IN}) = 0.5V_{P-P}$, Standard	-8	0	8	%
			_	-	Ĭ					_	-	Staircase				
Differential Phase	DP_P	0	0	0	0	0	0	0	0	5	5		-3	0	3	deg
Inversion Block Level	BL _N	0	0	0	0	0	0	5 5	0	5 5	5 5	(1) Voltage: a 1) Voltage: b BL _N = a - b	0.59	0.68	0.77	V
Inversion BLK R-Y		0	0	0	0	0	0	5	0	5	0	① Voltage: c BLK = c - b	-0.1	0	0.1	V
Voltage Gain	G_{R-Y}	0	0	0	0	0	0	5	0	0	0	$V(R-Y_{IN}) = 1V_{P-P}, 1MHz$	-0.5		0.5	dB
Burst Level Non Inversion	BF _{RP}	0	0 0	0	0 5	0 5	0	5 5	0 0	0	0	9 Voltage: d 9 Voltage: e BF _{RP} = e - d	135	150	165	mV
Burst Level Inversion B-Y	BF _{RN}	0	0	0	5	5	0	5	0	0	0	9 Voltage: f BF _{RN} =f-d	-165	-150	-135	mV
Voltage Gain	G _{R-Y}	0	0	0	0	0	0	5	0	0	0	$V (B-Y_{IN}) = 1V_{P-P}, 1MHz$	-0.5	0	0.5	dB
Burst Level Non Inversion	BF _{HP}	0	0	0	0	5 0	5 5	5 5	0	0	0	7 Voltage: g 7 Voltage: h BF _{RP} = g - h	135	150	165	mV
Burst Level Inversion	BF _{RN}	0	0	0	5	5	5	5	0	0	0	7 Voltage: i BF _{RN} =g-i	-165	-150	-135	mV
R-Y Switching Speed		х	0	0	0	0	0	5	5	0	0	$X = 1MHz$ $5V_{PP}$ Rectangular Wave			*100	nS
B-Y Switching Speed		X	0	0	0	0	0	5	5	0	0	X = 1MHz 5V _{PP} Rectangular Wave			*100	nS

^{*}Remark 1) *Item indicates design assurance rating.

■ ELECTRICAL CHARACTERISTICS

PARAMET	TER	SYMBOL			C	1O:	NTF	ROL	. PII	N				TEST CONDITION	MIN.	TYP.	MAX.	UNIT
			3	(4)	(5)	10	12	14)	15(16)	19	20						
Character Outp																		
C Inversi			_	_	_	^	^	^	_	_	^	^		\	000	700	770	\
White	Y	M _{PWY}	5	5	5	U	U	U	5	5	U	U	(1)	•	630	700	770	mV
	R-Y	M _{PWR}											9	Voltage: B, M _{PWR} = B - V ₉	-16	0	16	mV
Valley	B-Y	M _{PWB}	_	_	^	^	^	^	_	_	^	^	7	Voltage: C, $M_{PWB} = C - V_7$	-14	0	14	mV
Yellow	Y	M _{PYY}	5	5	0	U	U	U	5	5	U	U		Voltage: A, M _{PYY} = A - V ₁	472	525	578	mV
	R-Y	M _{PYR}											9	Voltage: B, M _{PYR} = B - V ₉	13	33	53	mV
Cumana	B-Y	M _{PYB}	_	_	_	^	^	^	5	_	٥	^	7	Voltage: C, M _{PYR} = C - V ₇	165	-146	-127 501	mV
Cyanoge	Y	M _{PCY}	U	5	5	U	U	U	5	Э	U	U	(1)	Voltage: A, M _{PCY} = A - V ₁	409	455		mV
	R-Y	M _{PCR}											9	Voltage: B, M _{PCR} = B - V ₉	-232	-209	-186	mV
Crass	B-Y	M _{PCB}	_	_	^	^	^	^	_	_	^	^	7	Voltage: C, M _{PCB} = C - V ₉	28	50	72	mV
Green	Y	M _{PGY}	U	5	0	U	U	U	5	Э	U	U	(1)	Voltage: A, M _{PGY} = A - V ₁	252	280	308	mV
	R-Y B-Y	MPGR											9	Voltage: B, M _{PGR} = B - V ₉	-197	-176	-155 -77	mV m\/
Maganta		M _{PCB}	_	^	_	^	^	0	E	F	0	0	7	Voltage: C, M _{PGB} = C - V ₇	-117 270	-97 420		mV m\/
Magenta	Y	M _{PMY}	5	0	5	U	U	U	5	Э	U	U		Voltage: A, M _{PMY} = A - V ₁	378 155	420 476	462	mV m\/
	R-Y B-Y	M _{PMR}											(7) (7)	Voltage: B, M _{PMR} = B - V ₉	155 77	176 97	197 117	mV mV
Ded	Б- î Ү	M _{PMB}	_	0	^	^	^	^	5	_	^	^	\sim	Voltage: C, M _{PMB} = C - V ₇	220			
Red	r R-Y	MPRY	Э	U	0	U	U	U	5	Э	U	U	(1)	Voltage: A, M _{PRY} = A - V ₁ Voltage: B, M _{PRR} = B - V ₉		245	270 232	mV m\/
	R-1 B-Y	MPRR											7	•	186	209		mV
Blue	Б- î Ү	M _{PRB}	۸	0	0	0	0	Λ	5	_	Λ	^	(1)	Voltage: C, M _{PRB} = C - V ₇	-72 156	-50 175	-28 194	mV mV
Diue	R-Y	M _{PBY}	U	U	U	U	U	U	5	5	U	U	9	Voltage: C, M _{PBY} = A - V ₁			-13	
	R-1 B-Y	M _{PBR}											7	Voltage: B, M _{PBR} = B - V ₉	-53 127	-33 146	165	mV mV
Dlook	Б- i Y	M _{PBB}	۸	0	Λ	0	0	Λ	5	_	Λ	^	\sim	Voltage: C, M _{PBB} = C - V ₇	-20			
Black	r R-Y	MPPY	U	0	0	0	U	U	5	Э	U	U	(1)	Voltage: A, M _{PPY} = A - V ₁ Voltage: B, M _{PPR} = B - V ₉	-20 -14	0	20 14	mV mV
	B-Y	M _{PPR}											7	Voltage: C, $M_{PPB} = C - V_7$	-14	0	12	mV
Character Outp		M _{PPB}												Vollage. C, IVIPPB - C - V7	-12	0	12	IIIV
C Inversi																		
White	Υ	M _{NWY}	5	5	5	Λ	٥	Λ	5	5	٥	Λ	(1)	Voltage: A, M _{NWY} =A-V ₁	630	700	770	mV
VVIIIC	R-Y	M _{NWR}		J	Ü	Ü	Ü	Ü	J	J	Ü	U	9	Voltage: B, M_{NWR} = B - V_9	-16	0	16	mV
	B-Y	M _{NWB}											7	Voltage: C, $M_{NWB} = C - V_7$	-14	0	14	mV
Yellow	Y	M _{NYY}	5	5	0	5	0	0	5	5	n	n	(1)	Voltage: A, $M_{NYY} = A - V_1$	472	525	578	mV
TCIIOVV	R-Y	M _{NYR}		J	Ü	J	Ü	Ü	J	J	Ü	U	9	Voltage: B, $M_{NYR} = B - V_9$	-53	-33	-13	mV
	B-Y	M _{NYB}											7	Voltage: C, $M_{NYB} = C - V_7$	127	146	165	mV
Cyanoge	Y	M _{NCY}	0	5	5	5	0	0	5	5	n	n	(1)	Voltage: A, $M_{NCY} = A - V_1$	409	455	501	mV
Cyanoge	R-Y	M _{NCR}		Ü	Ü	Ü	Ü	Ü	Ü	Ü	Ü	Ü	9	Voltage: B, M_{NCR} = B - V_9	186	209	232	mV
	B-Y	M _{NCB}											7	Voltage: C, $M_{NCB} = C - V_7$	-72	-50	-28	mV
Green	Υ Υ	M _{NGY}	0	5	0	5	0	0	5	5	0	0	$ \widecheck{1}\rangle$	Voltage: A, $M_{NGY} = A - V_1$	252	280	308	mV
0.00	R-Y	M _{NGR}		Ū	Ŭ	Ŭ	Ŭ	Ū	Ŭ	Ŭ	Ŭ	Ū	9	Voltage: B, M _{NGR} = B - V ₉	155	176	197	mV
	B-Y	M _{NGB}											7	Voltage: C, M _{NGB} = C - V ₇	77	97	117	mV
Magenta	Y	M _{NMY}	5	0	5	5	0	0	5	5	0	0	9	Voltage: A, $M_{NMY} = A - V_1$	378	420	462	mV
	R-Y	M _{NMR}		٠	•	-	•	•	•	-	•	-	9	Voltage: B, $M_{NMR} = B - V_9$	-197	-176	-155	mV
	B-Y	M _{NMB}											7	Voltage: C, M _{NMB} = C - V ₇	-117	-97	-77	mV
Red	Υ Υ	M _{NRY}	5	0	0	5	0	0	5	5	0	0	(1)	Voltage: A, $M_{NRY} = A - V_1$	220	245	270	mV
	R-Y	M _{NRR}		•	-	-	-	-	-	-	-	-	9	Voltage: B, $M_{NRR} = B - V_9$	-232	-209	-186	mV
	B-Y	M _{NRB}											7	Voltage: C, $M_{NRB} = C - V_7$	28	50	72	mV
Blue	Υ Υ	M _{NBY}	0	0	5	5	0	0	5	5	0	0	$ \widecheck{1}\rangle$		156	175	194	mV
	R-Y	M _{NBR}	•	•	-	-	•	•	•	_	-	-	9	Voltage: B, $M_{NBR} = B - V_9$	13	33	53	mV
	B-Y	M _{NBR}											7	Voltage: C, M _{NBR} = C - V ₇	-165	-146	-127	mV
Black	Υ Υ	M _{NPY}	0	0	0	5	0	0	5	5	0	0	(1)	Voltage: A, $M_{NPY} = A - V_1$	-20	0	20	mV
	R-Y	M _{NPR}	•	•	-	-	•	•	•	_	-	-	9		-14	0	14	mV
	B-Y	M _{NPB}											7		-12	0	12	mV
	ו-ט	ININER											\cup	VOILUGE. O, IVINPB - O - V7	14		'-	1117



■ APPLICATION NOTES

I / O Explanation			
 Supply Voltage 	V^{+}	5V	2
	GND		11
 Input Signals 	Υ	$0.7V_{P-P}$	17
	R-Y	$1.0V_{P-P}$	8
	B-Y	$0.7V_{P-P}$	6
 Output Signals 	Υ	$0.7V_{P-P}$	(1)
	R-Y	$1.0V_{P-P}$	9
	B-Y	0.7\/p_p	(7)

■ APPLICATION NOTES

I / O Explanation

• Control Pin Low=0V, HIGH=5V

R(3)									
G (4)	Superimposed	Superimposed color adjustment							
в ⑤ —	I								
Clamp Pu	lse (15) —								
Character	Pulse 16	Y, R-Y, B-Y signal process pulse input							
HBF Pulse	e <u>12</u>	1, K-1, B-1 signal process pulse input							
BLK Pulse	; 20—								
C Inversion	10	olor difference, brightness inverting pin							
		Jioi uiliererioe, brightiliess iriverting pin							

NTS / PAL Switch (14)

Adjusting Pin (Normally open → non adjustment)

BF level

Y Inversion

13 Burst flag insert level adjusting pin.

Inversion set up correction (18) Y inversion signal level adjusting pin.

1. Input Signal

Superimposed color level shall be determined by the following standard signal level.

The character output standard level on the specification shall be determined through calculation out of 75% of superimposed color level.

(In order to avoide the clipping of the encoding signal, the character output level is determined to lower level)

• The character output level converting expression

The basic expression

$$\begin{split} E_R - E_Y &= 0.70 E_R - 0.59 E_G - 0.11 E_B \\ E_B - E_Y &= -0.30 E_R - 0.59 E_G + 0.89 E_B \\ E_Y &= 0.30 E_R + 0.59 E_G + 0.11 E_B \end{split}$$

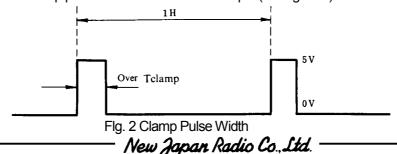
From standard level and practical input level, each color signal level imposed in R-Y, B-Y and Y signals are as in the following.

$$\begin{split} V_{R-Y} &= 0.75 \times 1 \, [V_{P-P}] \times E_{R-Y} / \, 1.4 \\ &= 0.375 E_R - 0.316 E_G - 0.059 E_B \\ V_{B-Y} &= 0.75 \times 0.7 \, [V_{P-P}] \times E_{B-Y} / \, 1.78 \\ &= -0.088 E_R - 0.174 E_G + 0.263 E_B \\ V_Y &= 0.75 \times 0.7 \, [V_{P-P}] \times E_Y / \, 1 \\ &= 0.158 E_R + 0.310 E_G + 0.058 E_B \\ (E_R, E_G, E_B, LOW 0, HIGH 1) \end{split}$$

2. Clamp Pulse

During the interval of blanking, input the pulse through clamp pulse pin 20 the blanking level (0 level) of input signal (Y, R-Y, B-Y) is to be fixed at the bias point within the IC.

Note) The pulse width of clamp pulse shall be set more than 3 µs. (see figure 2)



3. Character Color adjustment

Superimposed color adjustment of the character can be determined in eight different colors, by choosing R, G, B input levels.

(LOW 0V, HIGH 5V) R G В **COLOR** 5 5 5 White 5 5 0 Yellow 0 5 5 Cyan 5 0 0 Green 5 0 5 Magenta 5 0 0 Red 0 0 5 blue 0 0 Black 0

Character Color Selecting Code

4. Character Insertion

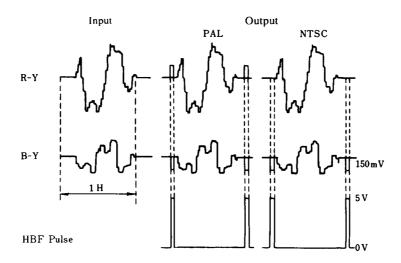
Pulse informations from outside character generater shall be given input at the character pulse pin (16). During the period of pulse process, the selected color level shall be inserted into each Y, R-Y, B-Y.

5. Burst Flag Insertion

Inputting burst period pulse at the HBF pin 12, the burst flag (150mV) can be inserted in the B-Y, R-Y signals. At the same time, by putting NTSC / PAL switch 14, the burst flag can be altered to NTSC or PAL system.

	NTSC/PALSWITCH (14)							
	LOW 0V (PAL) HIGH 5V (NTSC							
R-Y Signal B-Y Signal	+150mV -150mV	non insertion -150mV						

Burst Flag Inserting



Flg.3 Burst Flag Inserting Example

6. C Inversion

The color phase of the picture shall be inverted for one hundred and eighty degrees setting C inversion pin applied that the reference signal (burst flag) shall be inverted into one hundred and eighty degrees at the time of de-coding.

Superimposed character color do not change at the picture inversion.

	C INVERSI	ON PIN (10)
	LOW 0V	HIGH 5V
Burst	Non Inversion	Inversion

C Inversion Form

7. Y Inversion

The brightness of the picture shall be inverted by setting Y inversion (19). It is that Y signal shall be inverted by the inverter, and then blanking period signal shall be adjusted to the black level with blanking pulse.

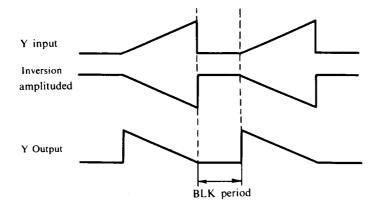


Figure 4. Y Inversion Output Example

	Y INVERSI	ON PIN (19)
	LOW 0V	HIGH 5V
Y Output	Non inversion	Inversion

Y Inversion Form

8. Adjusting pin

(1) BF Level Pin (13)

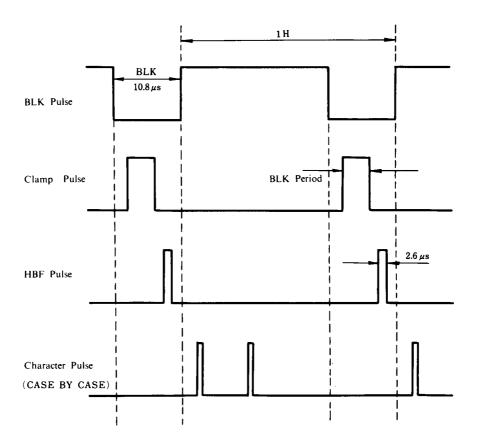
It is the burst flag minor adjusting pin. The burst level shall be adjusted at the open voltage, 0.3V level adjustment. Therefore, the most recommended on operation with the open condition, as it has been controlled st 135 at 165 mV (burst level) on specification.

(2) Inversion Set Up Correction Pin (18)

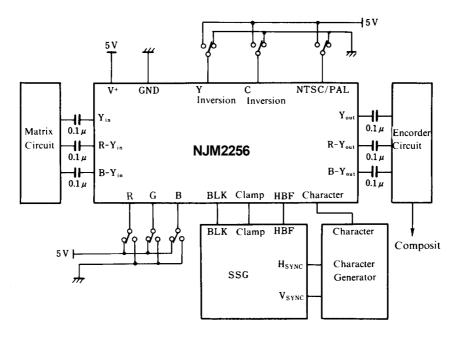
It is the minor adjusting pin of Y inversion signal level. The inverting black level shall be adjusted at the open voltage, 1.8V level adjustment. Therefore, the most recommended on operation with the open condition, as it has been controlled with 0.59 to 0.77 V (inverting black level) on specification.

9. Pulse Timing

The pulse input timing should be proceeded as in the following.

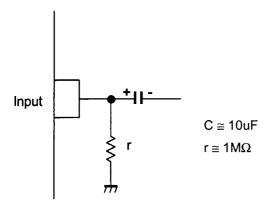


■ TYPICAL APPLICATION



■ APPLICATION

This IC requires $1M\Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



[CAUTION]
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