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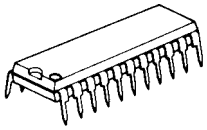
VIDEO EQUALIZER

■ GENERAL DESCRIPTION

NJM2258 is the IC functioning the gain high pass correction, as well as for equalizing function of wave distortion correction, generated by bright signal of group delay feature like low band filter. It has internalizing REC line, one circuit, and then the playback line 2cirucuit.

■ PACKAGE OUTLINE



NJM2258L

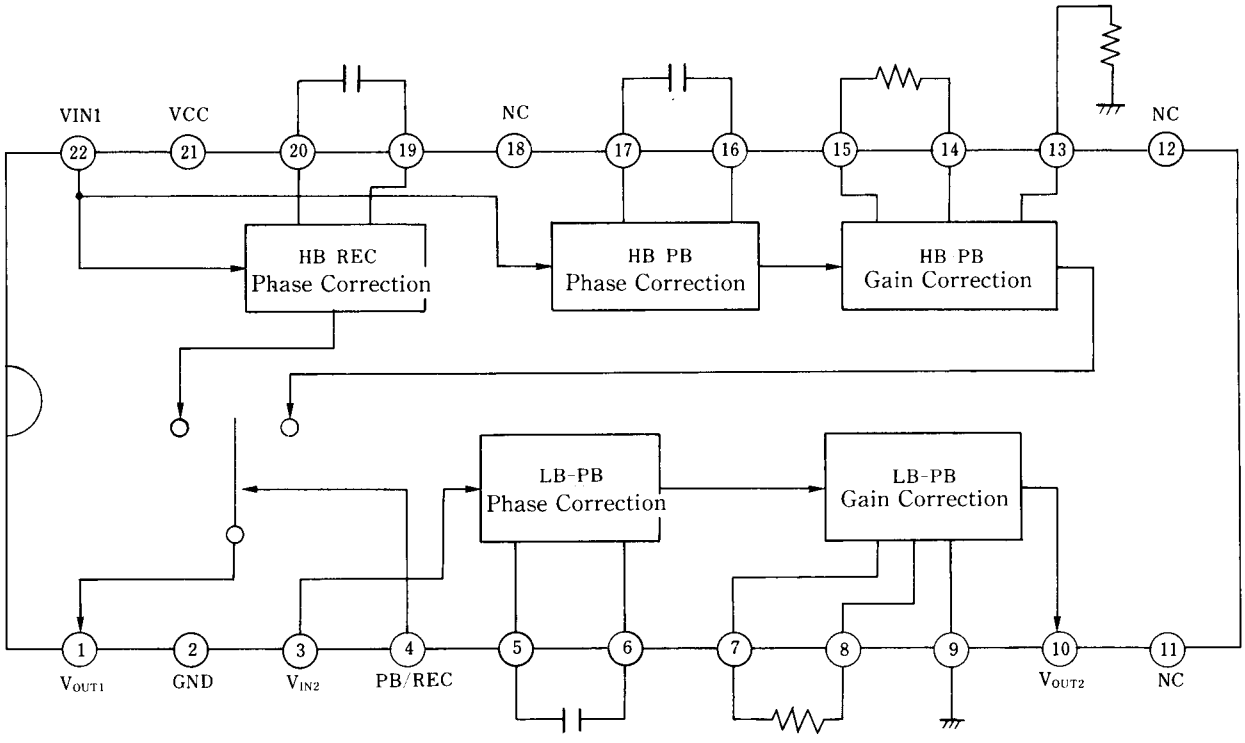
■ FEATURES

- 5V Spec, (Recmmended Operating Voltage Range)
- Wide Band Width, 10MHz
- REC / PLAYBACK Change over function attached
- Package Outline SDIP22
- Bipolar Technology

■ APPLICATION

- VCR (S-VHS compatible)
- Video Camera
- Laser Disc

■ BLOCK DIAGRAM



■ ABSOLUTE MAXIMUM RATINGS

(T_a=25°C)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+	7	V
Power Dissipation	P_D	700	mW
Operating Temperature Range	T_{opr}	-20 to +75	°C
Storage Temperature Range	T_{stg}	- 40 to +125	°C

■ ELECTRICAL CHARACTERISTICS

(V^+ = 5V, T_a = 25°C)

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I_P	No Signal	-	26	34	mA
HB-REC						
Phase Positive Gain	G_{af}	f_{IN} = 100KHz 19PINOUT	-1	0	+1	dB
Phase Negative Gain	G_{ar}	f_{IN} = 100KHz 20PINOUT	-6.4	-5.4	-4.4	dB
19pin Impedance	AT19	f_{IN} = 100KHz	-7.0	-6.0	-5.0	dB
Output Gain LOW	G_{al}	f_{IN} = 100KHz	-1	0	+1	dB
Output Gain HIGH	G_{ah}	f_{IN} = 5MEGHz	-1	0	+1	dB
Output Gain f Feature	ΔG_a	Gah - Gal	-1	0	+1	dB
Output Secondary Distortion	DA2	FIIN = 5MEG v_{IN} = 1.0V_{PP}		-40	-30	dB
Output the third Distortion	DA3	f_{IN}5MEG v_{IN} = 1.0V_{PP}		-36	-30	dB
HB-PB						
Positive Phase Gain	G_{bf}	f_{IN} = 100KHz 16PINOUT	-1	0	+1	dB
Negative Phase Gain	G_{br}	f_{IN} = 100KHz 17PINOUT	-0.2	0.8	1.8	dB
16 Impedance	AT_{16}	f_{IN} = 100KHz	-7.0	-6.0	-5.0	dB
Output Gain	G_{ca}	f_{IN} = 100KHz	-6.1	-5.0	-4.0	dB
15-14 Impedance	ΔG_c	15PIN-14PIN = 1.7KΩ	4.5	5.5	6.5	dB
Output Gain LOW	G_{cbl}	f_{IN} = 100KHz	-1	0	+1	dB
Output Gain HIGH	G_{cbh}	f_{IN} = 5MEGHz	2	3	4	dB
Output Gain f Feature	ΔG_b	Gcbh - Gcbl	2	3	4	dB
Output Secondary Distortion	DC2	f_{IN} = 5MEG v_{IN} = 1.0V_{PP}		-30	-25	dB
Output the third Distortion	DC3	f_{IN} = 5MEG v_{IN} = 1.0V_{PP}		-27	-22	dB
LB-PB						
Positive Phase Gain	G_{df}	f_{IN} = 100KHz 6PINOUT	-1	0	+1	dB
Negative Phase Gain	G_{dr}	f_{IN} = 100KHz 5PINOUT	-0.2	0.8	1.8	dB
6 Impedance	AT6	f_{IN} = 100KHz	-7.0	-6.0	-5.0	dB
Output Gain	G_{da}	f_{IN} = 100KHz	-6.1	-5.1	-4.1	dB
7-8 Impedance	ΔG_d	7PIN - 8PIN = 1.7KΩ	4.5	5.5	6.5	dB
Output Gain LOW	G_{ebl}	f_{IN} = 100KHz		+1	0	dB
Output Gain HIGH	G_{ebh}	f_{IN} = 5MEGHz	-1	0	+1	dB
Output Gain f Feature	ΔG_e	Gebh - Gebl	-1	0	+1	dB
Output Secondary Distortion	DE2	f_{IN} = 5MEG v_{IN} = 1.0V_{PP}		-35	-28	dB
Output the third Distortion	DE3	f_{IN} = 5MEG v_{IN} = 1.0V_{PP}		-36	-30	dB

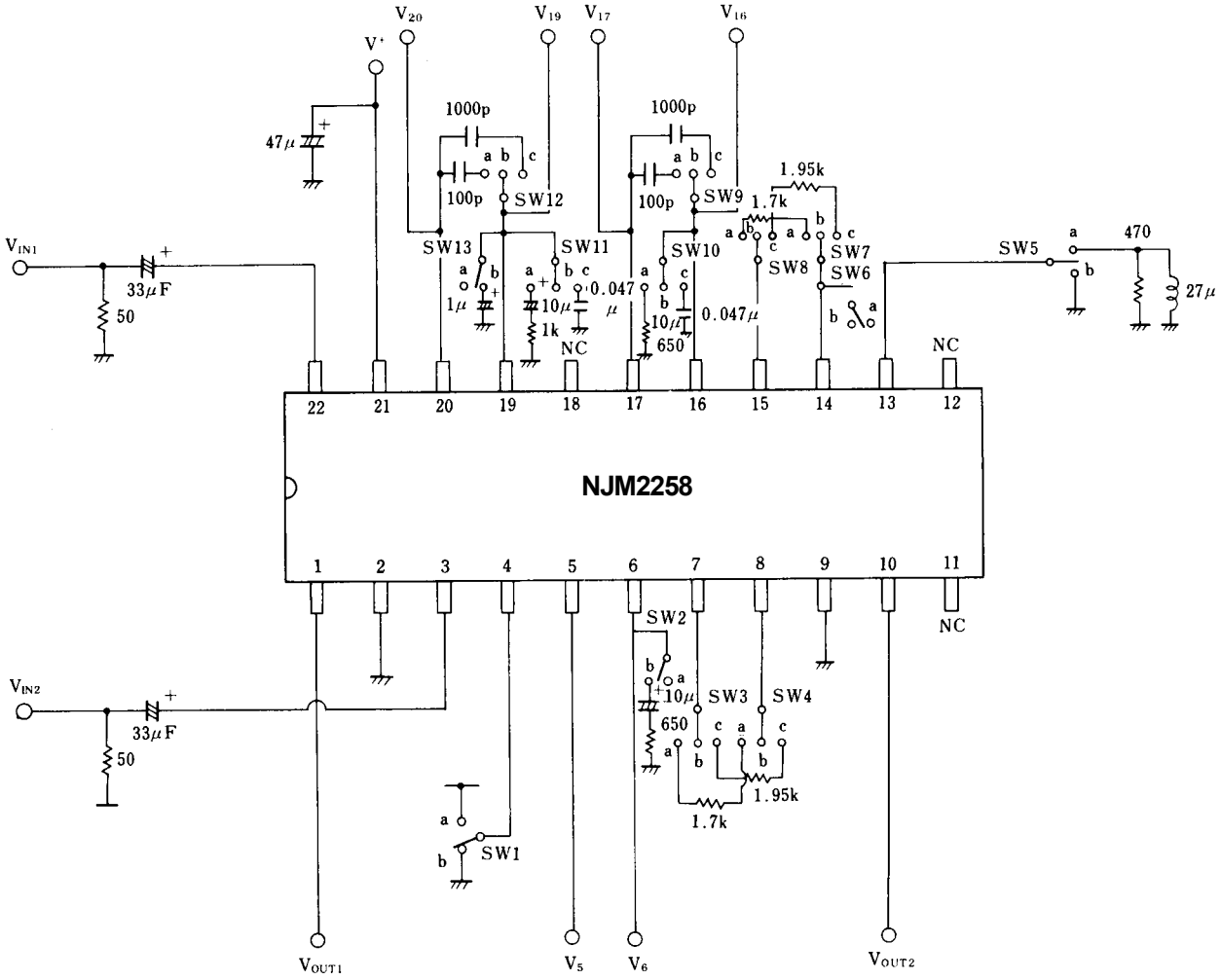
■ TERMINAL FUNCTION

PIN	PIN NAME	SYMBOL	FUNCTION
1	HB-REC / PB OUT	VOUT1	HB type (S-VHS) Correction Output Pin
2	GND	GND	Ground
3	LB-PB IN	VIN2	LB type, (VHS) play-back signal Input Pin
4	HB-REC / PB Change over	PB REC	HB type, Change-over output of REC signal or Play-back signal High makes play-back signal output, and low makes REC signal output.
5	LB-PB Phase Correction Pin 1	LPC1	Connecting capacitor between Pin 5 to 6, which helps to give feature of correcting the group delay.
6	LB-PB Phase Correction Pin 2	LPC2	Connecting capacitor between Pin 5 to 6, which helps to give feature of correcting the group delay.
7	LB-PB Gain Correction Pin 1	LGC1	Setting up Gain by connecting resistor between Pin 7 to 8.
8	LB-PB Gain Correction Pin 2	LGC2	Setting up Gain by connecting resistor between Pin 7 to 8.
9	LB-PB Gain Correction Pin 3	LP	Connecting L-C parallel resonance between pin 9 to GND, helps to give High band keeping, and if not required of keeping connect to GND.
10	LB-PB OUT	VOUT2	LB type Output pin
11	N.C		N.C pin
12	N.C		N.C pin
13	LB-PB Gain Correction Pin 3	HP	Connecting L-C parallel resonance between pin 13 to GND, helps to give High band keeping, and if not required of keeping connect to GND.
14	LB-PB Gain Correction Pin 2	HPG1	Setting up Gain by connecting resistor between Pin 14 to 15.
15	LB-PB Gain Correction Pin 1	HPG2	Setting up Gain by connecting resistor between Pin 14 to 15.
16	LB-PB Phase Correction Pin 2	HPC1	Connecting Capacitor between Pin 16 to 17, which helps to give feature of correcting the group delay.
17	LB-PB Phase Correction Pin 1	HPC2	Connecting Capacitor between Pin 16 to 17, which helps to give feature of correcting the group delay.
18	N.C		N.C pin
19	LB-PB Phase Correction Pin 2	HRC1	Connecting Capacitor between Pin 19 to 20, which helps to give feature of correcting the group delay.
20	LB-PB Phase Correction Pin 1	HRC2	Connecting Capacitor between Pin 19 to 20, which helps to give feature of correcting the group delay.
21	V^+	V_{CC}	Voltage Source.
22	HB-REC / PB IN		HB type Input pin.

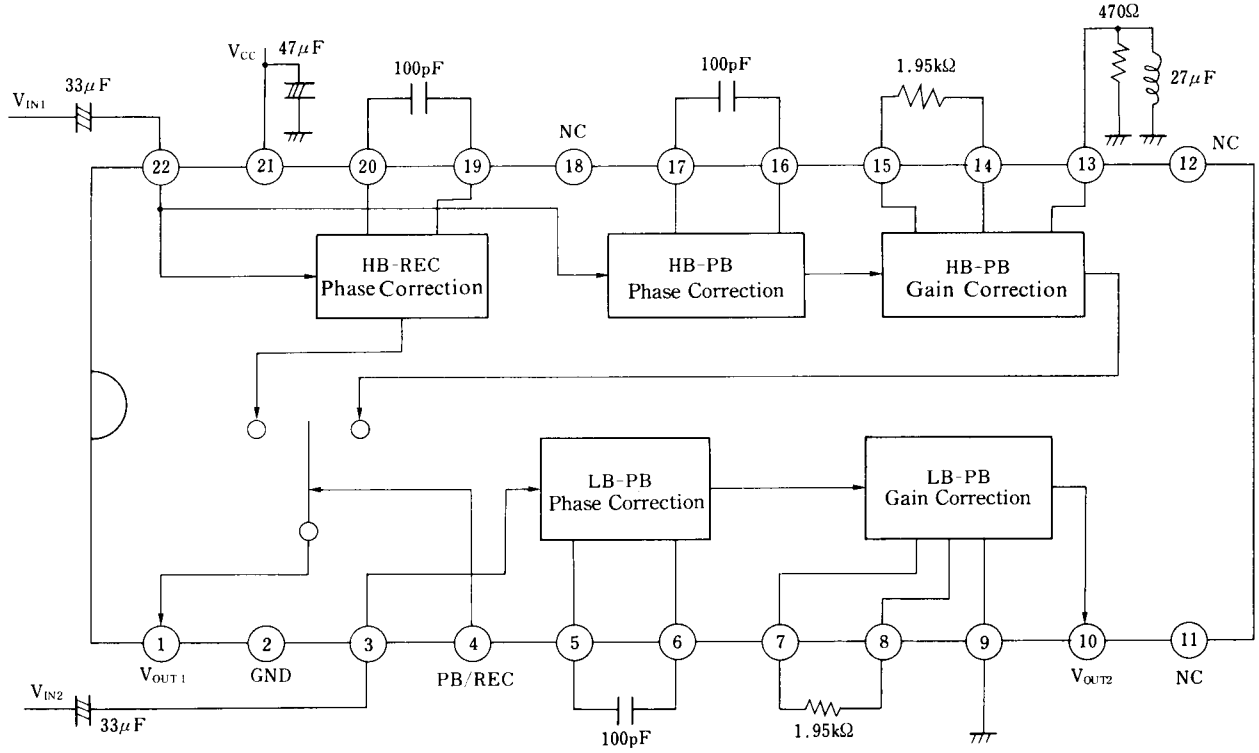
■ TEST CONDITION

PARAMETER	SW-CONDITION													TEST PIN	TEST CONDITION
	1	2	3	4	5	6	7	8	9	10	11	12	13		
I_P	a	a	b	b	b	a	b	b	b	b	b	b	a		
GA_f	b													V19	f=100kHz, V=0.5V_{P-P}
GA_r													b	V20	f=100kHz, V=0.5V_{P-P}
AT19											a		a	V19	f=100kHz, V=0.5V_{P-P}
Gal											b	a		VOUT1	f=100kHz, V=0.5V_{P-P}
Gah											b	a		VOUT1	f=5MHz, V=0.5V_{P-P}
DA2												c		VOUT1	f=5MHz, V=1.0V_{P-P}
DA3												c		VOUT1	f=5MHz, V=1.0V_{P-P}
Gbf														V16	f=100kHz, V=0.5V_{P-P}
Gbr														V17	f=100kHz, V=0.5V_{P-P}
AT16										a				V16	f=100kHz, V=0.5V_{P-P}
Gca	a								a	b				VOUT1	f=100kHz, V=0.5V_{P-P}
ΔGca							a	a						VOUT1	f=100kHz, V=0.5V_{P-P}
Gcb1					a		c	c						VOUT1	f=100kHz, V=0.5V_{P-P}
Gcbh					a		c	c						VOUT1	f=5MHz, V=0.5V_{P-P}
DC2					b		c	c	c					VOUT1	f=5MHz, V=1.0V_{P-P}
DC3					b		c	c	c					VOUT1	f=5MHz, V=1.0V_{P-P}
Gdf	a										b	a		V6	f=100kHz, V=0.5V_{P-P}
Gdr														V5	f=100kHz, V=0.5V_{P-P}
AT6		b												V6	f=100kHz, V=0.5V_{P-P}
Gda		a												VOUT2	f=100kHz, V=0.5V_{P-P}
ΔGd			a	a										VOUT2	f=100kHz, V=0.5V_{P-P}
Geb1			c	c										VOUT2	f=100kHz, V=0.5V_{P-P}
Gebh			c	c										VOUT2	f=5MHz, V=0.5V_{P-P}
DE2			c	c										VOUT2	f=5MHz, V=1.0V_{P-P}
DE3			c	c										VOUT2	f=5MHz, V=1.0V_{P-P}

■ TEST CIRCUIT



■ APPLICATION CIRCUIT



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