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VIDEO SUPER INPOSER WITH Y-C MIXER

■ GENERAL DESCRIPTION

■ PACKAGE OUTLINE

NJM2509V

The **NJU2509** is video super imposer, including Y/C mix circuit. Y-signal input terminal have sink-chip clamp function and it is applied to fixed DC level of video signal.

Impose voltage is fixed internally to white level and black level, and includes 6dB amplifier.



- Internal Y/C Mix Circuit
- Internal Clamp Circuit (Y Signal), Bias Circuit (C Signal)
- Impose voltage fixed internally to white level and black level.

SSOP8

 V^{+}

- Internal 6dB AMP. (Input : 0.5V_{P-P} Output : 1.0 V_{P-P})
- Package Outline
- Bipolar Technology

■ RECOMMENDED OPERATING CONDITION

Operating Voltage

4.5V to 5.1V

- APPLICATION
 - Video Camera

BLOCK DIAGRAM



NJM2509V

■ ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	RATINGS	UNIT			
Supply Voltage	V ⁺	7.0	V			
Power Dissipation	PD	250	mW			
Operating Temperature Range	T _{opr}	-20 to +75	°C			
Storage Temperature Range	T _{stg}	-40 to +125	°C			

■ ELECTRICAL CHARACTERISTICS

 $(V^{+} = 4.8V, T_{a} = 25^{\circ}C, R_{L} = 10k\Omega)$

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Current	I _{CC}		5.3	7.0	8.7	mA
Clamp Voltage	Vcmp		2.4	2.5	2.6	V
Bias Voltage	V _{bias}		2.4	2.5	2.6	V
Voltage Gain	Gv	Vout / Vin 100kHz, 0.5VP-P Sine Wave	6.0	6.3	6.8	dB
Frequency Characteristic	Gı	0.5V _{P-P} Sine Wave vo (10MHz)/ vo (100kHz)	-0.7	-0.2	+0.3	dB
Background Voltage	V _{set}	From Pedestal Level	5.0	15.0	20.0	IRE
CHR, VOLTAGE	V _{chr}	From Pedestal Level	65.0	75.0	85.0	IRE
Input Resistance	R _{in}	Input C _{in}	-	30	-	kΩ
Differential Gain	DG	0.5V _{P-P} , 10STEP Stair wave	-	-	3.0	deg
Differential Phasa	DP	0.5V _{P-P} , 10STEP Stair wave	-	-	3.0	%
BACKGROUND	V _{ch}	BACKGROUND SW : ON	2.4	-	-	V
Switch Change Voltage	Vd	BACKGROUND SW : OFF	-	-	0.8	V
CHR MUTE	V _{ch} MUTE	CHRMUTE SW : ON	2.4	-	-	V
Switch Change Voltage	V _d MUTE	CHRMUTE SW : OFF	-	-	0.8	V
Crosstalk 1	CT1	C _{in} →BACKGROUND VOLTAGE (*1)	-	-50	-	dB
Crosstalk 2	CT2	C _{in} →CHR VOLTAGE (*2)	-	-50	-	dB
Crosstalk 3	CT3	Y _{in} →BACKGROUND VOLTAGE (*1)	-	-50	-	dB
Crosstalk 4	CT4	Y _{in} →CHR VOLTAGE (*1)	-	-50	-	dB

*1. Crosstalk : 4.43MHz. 0.5V $_{\text{P-P}}$ Sine wave, V $_{\text{out}}$ / V $_{\text{in}}$

■ TEST CIRCUIT



■ TERMINLAL EXPLANATION

 $(V^+ = 4.8V, T_a = 25^{\circ}C)$

PIN No.	UNIT	FUNCTION	EQUIVALENT CIRCUIT	PIN No.	UNIT	FUNCTION	EQUIVALENT CIRCUIT
1	YIN	Input 2.5V clamp 0.5V _{PP} Y-signal or Compozitto signal		5	MUTE	Character signal ON/OFF Switch Hi Charactor signal OFF Lo Charactor signal ON	5 30k \$ \$30k 26k \$ 26k
2	GND	GROUND		6	CHR	Character signal Input pin Hi White level Lo Composit signal	6 19k \$ 9 k \$ 777 777
3	CIN	Input 2.5V Bias, 0.5V _{PP} C-signal	V ⁺ 30k 500 70μ F	7	V+ +	Supply voltage	
4	SET	Character signal Input Pin H Black level i L Composit o signal	(4) 19k ≩ 9 k ₹ 777 777	8	OUT	Output-1 V _{PP} Composit signal, Impose Voltage	V ⁺ - - - - - - - - - - - - -

■ APPLICATION

This IC requires 1MΩ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.



[CAUTION]

New Japan Radio Co., Ltd.

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