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## 3-INPUT / 2-INPUT VIDEO SWITCH

## - GENERAL DESCRIPTION

The NJM2513 is a switching IC for switching over from one audio or video input signal to another. Internalizing 3 input-1 output, and 2 input-1 output and then each set can be operated independently. Side of 2 input-1 output are "Clamp type", and they can be operated while setting DC level fixed in position of the video signal. It is a higher efficiency video switch, featuring the operating voltage 4.75 to 13 V , the frequency feature 10 MHz , and then the Crosstalk 75 dB (at 4.43 MHz ).

## - FEATURES

- Operating Voltage $(+4.75 \mathrm{~V}$ to $+13 \mathrm{~V})$
- 3 Input-1 Output/2 Input-1 output.
- Crosstalk 75dB (at 4.43 MHz )
- Wide Bandwidth Frequency 10 MHz ( $2 \mathrm{~V}_{\mathrm{P}-\mathrm{P}}$ Input)
- Package Outline

DIP16, DMP16

- Bipolar Technology


## ■ RECOMMENDED OPERATING CONDITION

- Operating Voltage
$\mathrm{V}^{+}$
4.75 V to 13.0 V


## ■ APPLICATIONS

- VCR, Video Camera, AV-TV, Video Disk Player.


## - PACKAGE OUTLINE



NJM2513D


NJM2513M

## - BLOCK DIAGRAM



NJM2513D
NJM2513M

|  |  |  |  |
| :--- | :---: | :---: | :---: |
| MAXIMUM RATINGS | $\left(\mathrm{T}_{\mathrm{a}}=25^{\circ} \mathrm{C}\right)$ |  |  |
| PARAMETER | SYMBOL | RATINGS | UNIT |
| Supply Voltage | $\mathrm{V}^{+}$ | 14 | V |
| Power Dissipation | $\mathrm{P}_{\mathrm{D}}$ | (DIP16) 700 | mW |
| Operating Temperature Range | $\mathrm{T}_{\mathrm{opr}}$ | (DMP16) 350 | mW |
| Storage Temperature Range | $\mathrm{T}_{\mathrm{stg}}$ | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |

## ■ ELECTRICAL CHARACTERISTICS

| PARAMETER | SYMBOL | CONDITION | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Operating Current (1) | lcc 1 | $\mathrm{V}^{+}=5 \mathrm{~V}$ (Note1) | 6.7 | 9.7 | 12.7 | mA |
| Operating Current (2) | 1 cc 2 | $\mathrm{V}^{+}=9 \mathrm{~V}$ (Note1) | 8.6 | 12.3 | 16.0 | mA |
| Voltage Gain | $\mathrm{G}_{V}$ | $\mathrm{V}_{1}=100 \mathrm{kHz}, 2 \mathrm{~V}_{\text {P-P, }} \mathrm{V}_{0} / \mathrm{V}_{1}$ | -0.6 | -0.1 | +0.4 | dB |
| Frequency Gain | $\mathrm{G}_{\mathrm{F}}$ | $\mathrm{V}_{\mathrm{I}}=2 \mathrm{~V}_{\mathrm{P}-\mathrm{P},} \mathrm{V}_{\mathrm{O}}(10 \mathrm{MHz}) / \mathrm{V}_{\mathrm{O}}(100 \mathrm{kHz})$ | -1.0 | 0 | +1.0 | dB |
| Differential Gain | DG | $V_{1}=2 V_{\text {P-P, }}$, Standerd Staircase Signal | - | 0.3 | - | \% |
| Differential Phasa | DP | $\mathrm{V}_{1}=2 \mathrm{~V}_{\text {P-P }}$, Standerd Staircase Signal | - | 0.3 | - | deg |
| Output offset Voltage (1) | Vos1 | (Note2) | -15 | 0 | +15 | mV |
| Output offset Voltage (2) | Vos2 | (Note3) | -25 | 0 | +25 | mV |
| Crosstalk | CT | $\mathrm{V}_{\mathrm{I}}=2 \mathrm{~V}_{\mathrm{P}-\mathrm{P},} 4.43 \mathrm{MHz}, \mathrm{V}_{\mathrm{O}} / \mathrm{V}_{\mathrm{I}}$ | - | -75 | - | dB |
| Switch Change Over Voltage | $\mathrm{V}_{\mathrm{CH}}$ | All inside Switches ON | 2.5 | - | - | V |
| Switch Change Over Voltage | $\mathrm{V}_{\mathrm{CL}}$ | All inside Switches OFF | - | - | 1.0 | V |

(Note1) S1 = S2 = S3 = S4 = S5 = S6 = S7 = 1
(Note2) S1 = S2 = S3 = S4 = S5 = 1, S8 = 2, S7 = 1, S6 = $1 \rightarrow 2$ Measure the output DC voltage difference
(Note3) $\mathrm{S} 1=\mathrm{S} 2=\mathrm{S} 3=\mathrm{S} 4=\mathrm{S} 5=1, \mathrm{~S} 8=1, \mathrm{~S} 7=1, \mathrm{~S} 6=1 \rightarrow 2(\mathrm{~S} 6=1, \mathrm{~S} 7=1 \rightarrow 2)$ Measure the output DC voltage difference

## ■ TEST CIRCUIT


－TERMINLAL EXPLANATION

| PIN No． | PIN NAME | Voltage | INSIDE EQUIVALENT CIRCUIT |
| :---: | :---: | :---: | :---: |
| $\begin{gathered} 8 \\ 9 \\ 11 \end{gathered}$ | IN 1 A <br> IN 1 B <br> IN 1 C <br> 〔Input〕 | $\begin{array}{\|l} 2.5 \mathrm{~V} \\ \left(\frac{1}{2} \mathrm{v}^{+}\right) \end{array}$ |  |
| $\begin{gathered} 16 \\ 1 \end{gathered}$ | IN 2 A IN 2 B〔Input〕 | $\begin{aligned} & 1.5 \mathrm{~V} \\ & \left(\frac{3}{10} \mathrm{v}^{+}\right) \end{aligned}$ |  |
| $\begin{gathered} 7 \\ 12 \\ 2 \end{gathered}$ | CTL 1A <br> CTL 1 B <br> CTL 2 ［Switching］ |  |  |
| 5 | OUT1 ［Output］ | $\begin{aligned} & 1.8 \mathrm{~V} \\ & \left(\frac{1}{2} \mathrm{~V}^{+}-0.7\right) \end{aligned}$ |  |
| 3 | OUT2 ［Output］ | $\begin{aligned} & 0.8 \mathrm{~V} \\ & \left(\frac{3}{10} \mathrm{~V}^{+}-0.7\right) \end{aligned}$ |  |
| 13 | $\mathrm{V}^{+}$ | 5 V |  |
| $\begin{gathered} 15 \\ 4 \\ 10 \end{gathered}$ | GND 1 <br> GND 2 <br> GND 3 |  |  |

## - APPLICATION

This IC requires $1 \mathrm{M} \Omega$ resistance between INPUT and GND pin for clamp type input since the minute current causes an unstable pin voltage.


This IC requires $0.1 \mu \mathrm{~F}$ capacitor between INPUT and GND, $1 \mathrm{M} \Omega$ resistance between INPUT and GND for clamp type input at mute mode.


This IC requires $0.1 \mu$ F capacitor between INPUT and GND for bias type input at mute mode.


