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TIMER

■ GENERAL DESCRIPTION

The **NJM555** monolithic timing circuit is a highly stable controller capable of producing accruate time delays or oscillation. In the time delay mode, delay time is precisely controlled by only two external parts : a resistor and a capacitor. For operation as an oscillator, both the free running frequency and the duty cycle are accurately controlled by two external resistors and a capacitor.

Terminals are provided for triggering and resetting. The circuit will trigger and reset on falling waveforms. The output can source or sink up to 200mA or drive TTL circuits.

FEATURES

- Operating Voltage
- (4.5V to 16V)
- Less Number of External Components
- Package Outline
- Bipolar Technology





DIP8, DMP8, SSOP8, SIP8

■ EQUIVALENT CIRCUIT



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PACKAGE OUTLINE





NJM555D

NJM555M





NJM555L

NJM555V



■ ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	RATINGS	UNIT		
Supply Voltage	V+	18	V		
Power Dissipation	PD	(DIP8) 1000(Note1)	mW		
		(DMP8) 580(Note1)	mW		
		(SSOP8) 480(Note1)	mW		
		(SIP8) 1600(Note1)	mW		
Operating Temperature Range	T _{opr}	-40 to +85	°C		
Storage Temperature Range	T _{stg}	-40 to +125	S		

Note1: Mounted on the EIA/JEDEC standard board (76.2×114.3×1.6mm, four layer, FR-4).

■ ELECTRICAL CHARACTERISTICS

 $(V^+=5 \text{ to } 15V, T_a=25^{\circ}C)$

PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V ⁺		4.5	-	16	V
Operating Current	Icc	V ⁺ =5V, R _L =∞(Note 2)	-	3.0	6.0	mA
Operating Current	Icc	V ⁺ =15V, R _L =∞(Note 2)	-	10	15	mA
Timing Error						
Initial Accuracy	Et	T_a =-20 to 75°C, V ⁺ =5 to 15V(Note 3)	-	1.0	-	%
Drift with Temperature	Et	T_a =-20 to 75°C, V ⁺ =5 to 15V(Note 3)	-	50	-	ppm / °C
Drift with Supply Voltage	Et	T_a =-20 to 75°C, V ⁺ =5 to 15V(Note 3)	-	0.1	-	%/V
Threshold Voltage	Vth		-	2/3	-	×V ⁺
Trigger Voltage	VT	V ⁺ =15V	-	5.0	-	V
Trigger Voltage	VT	V ⁺ =5V	-	1.67	-	V
Trigger Current	Ι _Τ		-	0.5	-	μA
Reset Voltage	V _R		0.4	0.5	1.0	V
Reset Current	I _R		-	0.1	-	mA
Threshold Curret	l _{th}		-	0.1	0.25	μA
Control Voltage Level	V _{CL}	V ⁺ =15V	9	10	11	V
Control Voltage Level	V _{CL}	V ⁺ =5V	2.6	3.33	4.0	V
Output Voltage (Low)	V _{OL}	V ⁺ =15V Isink=10mA	-	0.1	0.25	V
Output Voltage (Low)	V _{OL}	V ⁺ =15V Isink=50mA	-	0.4	0.75	V
Output Voltage (Low)	V _{OL}	V ⁺ =15V Isink=100mA	-	2.0	2.5	V
Output Voltage (Low)	V _{OL}	V ⁺ =15V Isink=200mA	-	2.5	-	V
Output Voltage (Low)	V _{OL}	V ⁺ =5V Isink=5mA	-	0.25	0.35	V
Output Voltage (High)	V _{OH}	V ⁺ =15V Isource=200mA	-	12.5	-	V
Output Voltage (High)	V _{OH}	V ⁺ =15V Isource=100mA	12.75	13.3	-	V
Output Voltage (High)	V _{OH}	V ⁺ =15V Isource=40mA	-	13.5	-	V
Output Voltage (High)	V _{OH}	V ⁺ =5V Isource=100mA	2.75	3.3	-	V
Rise time of Output	tr	No Loading	-	100	-	ns
Fall time of Output	ŧ	No Loading	-	100	-	ns

Note 2 : Low output condition (When the output is high, it is lower than the low output condition by 1mA in the standard specificatio.) Note 3 : R_A , R_B =1k to 100k Ω , C=0.1 μ F, V⁺=15V from 5V

■ TYPICAL APPLICATION

(1) 50% Duty Cycle Oscillator



(2) Oscillation frequency can be changed by changing the control voltage.



(3) Pulse Width Modulation



TYPICAL CHARACTERISTICS





High Output Voltage Drop vs. Output Source Current



Low Output Voltage vs. Output Sink Current



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TYPICAL CHARACTERISTICS

1. Monostable Operation



Fig. 2 shows a typical example of the monostable operation. $T_H = 1.1R_A \cdot C$ assuming that T_H be the time at the high output level in this figure.

2. Free Running Operation



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