# imall

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# **Dual Complementary Transistors**

# **General Purpose PNP Transistor and NPN Transistors with Monolithic Bias** Network

NSM21356DW6T1G contains a single PNP transistor and a monolithic bias network NPN transistor with two resistors; a series base resistor and a base-emitter resistor. This device is designed to replace multiple transistors and resistors on customer boards by integrating these components into a single device. NSM21356DW6T1G is housed in a SC-88/SOT-363 package which is ideal for low power surface mount applications in space constrained applications.

#### Features

- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- Q1: NPN BRT, R1 = R2 = 47 k
- O2: PNP
- This is a Pb-Free Device

#### Applications

- Logic Switching
- Amplification
- Driver Circuits
- Interface Circuits

### **MAXIMUM RATINGS**

(T<sub>A</sub> = 25°C unless otherwise noted)

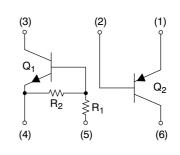
Rating – Q1 (NPN BRT)	Symbol	Value	Unit
Collector-Base Voltage	V <sub>CBO</sub>	50	Vdc
Collector-Emitter Voltage	V <sub>CEO</sub>	50	Vdc
Collector Current	۱ <sub>C</sub>	100	mAdc
Rating – Q2 (PNP)	Symbol	Value	Unit
Collector - Base Voltage	V <sub>(BR)CBO</sub>	-80	Vdc
Collector - Emitter Voltage	V <sub>(BR)CEO</sub>	-65	Vdc
Emitter - Base Voltage	V <sub>(BR)EBO</sub>	- 5.0	Vdc
Collector Current – Continuous	۱ <sub>C</sub>	-100	mAdc

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.



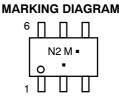
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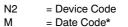
http://onsemi.com





SC-88/SOT-363 CASE 419B STYLE 1





= Date Code\*

(Note: Microdot may be in either location)

\*Date Code orientation and/or position may vary depending upon manufacturing location.

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
NSM21356DW6T1G	SC-88 (Pb-Free)	3000/Tape & Reel

<sup>†</sup>For information on tape and reel specifications. including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

<sup>=</sup> Pb-Free Package

#### THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation	PD		
$T_A = 25^{\circ}C$		180 (Note 1)	mW
Derate above 25°C		1.44 (Note 1)	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	692 (Note 1)	°C/W
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation,	PD		
$T_A = 25^{\circ}C$		230	mW
Derate above 25°C		1.83	mW/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	544	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

1. FR-4 @ Minimum Pad of 1.45 mm<sup>2</sup>, 1 oz Cu.

# **ELECTRICAL CHARACTERISTICS – Q1 NPN BRT** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Мах	Unit
OFF CHARACTERISTICS					
Collector-Base Cutoff Current $(V_{CB} = 50 \text{ V}, I_E = 0)$	I <sub>CBO</sub>	-	-	100	nAdc
Collector-Emitter Cutoff Current $(V_{CE} = 50 \text{ V}, I_B = 0)$	I <sub>CEO</sub>	-	-	500	nAdc
Emitter-Base Cutoff Current $(V_{EB} = 6.0 \text{ V}, I_C = 0)$	I <sub>EBO</sub>	-	-	0.1	mAdc
Collector-Base Breakdown Voltage $(I_C = 10 \ \mu A, I_E = 0)$	V <sub>(BR)CBO</sub>	50	-	-	Vdc
Collector-Emitter Breakdown Voltage (Note 2) $(I_{C} = 2.0 \text{ mA}, I_{B} = 0)$	V <sub>(BR)CEO</sub>	50	-	-	Vdc
ON CHARACTERISTICS (Note 2)					
DC Current Gain $(V_{CE} = 10 \text{ V}, I_C = 5.0 \text{ mA})$	h <sub>FE</sub>	80	140	-	
Collector-Emitter Saturation Voltage $(I_C = 10 \text{ mA}, I_B = 0.3 \text{ mA})$	V <sub>CE(sat)</sub>	-	-	0.25	Vdc
Output Voltage (on) (V <sub>CC</sub> = 5.0 V, V <sub>B</sub> = 3.5 V, R <sub>L</sub> = 1.0 k $\Omega$ )	V <sub>OL</sub>	-	-	0.2	Vdc
Output Voltage (off) (V <sub>CC</sub> = 5.0 V, V <sub>B</sub> = 0.5 V, R <sub>L</sub> = 1.0 k $\Omega$ )	V <sub>OH</sub>	4.9	-	-	Vdc
Input Resistor	R1	32.9	47	61.1	kΩ
Resistor Ratio	R1/R2	0.8	1.0	1.2	

2. Pulse Test: Pulse Width < 300  $\mu$ s, Duty Cycle < 2.0%

# **ELECTRICAL CHARACTERISTICS – Q2 PNP** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (I <sub>C</sub> = –10 mA)	V <sub>(BR)CEO</sub>	-65	-	-	V
Collector – Emitter Breakdown Voltage ( $I_C = -10 \ \mu A, \ V_{EB} = 0$ )	V <sub>(BR)CES</sub>	-80	-	-	V
Collector – Base Breakdown Voltage $(I_{C} = -10 \ \mu A)$	V <sub>(BR)CBO</sub>	-80	-	-	V
Emitter – Base Breakdown Voltage (I <sub>E</sub> = −1.0 μA)	V <sub>(BR)EBO</sub>	-5.0	-	-	V
Collector Cutoff Current (V <sub>CB</sub> = -30 V) (V <sub>CB</sub> = -30 V, T <sub>A</sub> = 150°C)	I <sub>CBO</sub>	-		-15 -4.0	nA μA
ON CHARACTERISTICS					
DC Current Gain $(I_{C} = -10 \ \mu\text{A}, V_{CE} = -5.0 \ \text{V})$ $(I_{C} = -2.0 \ \text{mA}, V_{CE} = -5.0 \ \text{V})$	h <sub>FE</sub>	_ 220	150 290	- 475	-
Collector – Emitter Saturation Voltage ( $I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$ ) ( $I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$ )	V <sub>CE(sat)</sub>			-0.3 -0.65	V
Base – Emitter Saturation Voltage ( $I_C = -10 \text{ mA}, I_B = -0.5 \text{ mA}$ ) ( $I_C = -100 \text{ mA}, I_B = -5.0 \text{ mA}$ )	V <sub>BE(sat)</sub>		-0.7 -0.9	-	V
Base – Emitter On Voltage $(I_{C} = -2.0 \text{ mA}, V_{CE} = -5.0 \text{ V})$ $(I_{C} = -10 \text{ mA}, V_{CE} = -5.0 \text{ V})$	V <sub>BE(on)</sub>	-0.6 -	-	-0.75 -0.82	V

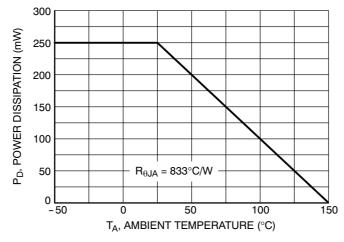
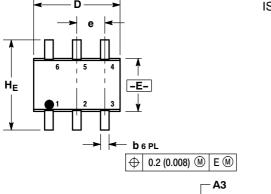
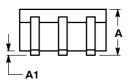


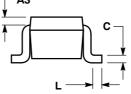
Figure 1. Derating Curve

#### PACKAGE DIMENSIONS

SC-88 (SOT-363) CASE 419B-02 ISSUE V







NOTES

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

2

3. 419B-01 OBSOLETE, NEW STANDARD 419B-02. MILLIMETERS INCHES 
 DIM
 MIN
 NOM
 MAX
 MIN
 NOM
 MAX

 A
 0.80
 0.95
 1.10
 0.031
 0.037
 0.043

 A1
 0.00
 0.05
 0.10
 0.000
 0.002
 0.004
 0.20 REF 0.008 REF Α3 b 0.10 0.21 0.30 0.004 0.008 0.012 
 C
 0.10
 0.14
 0.25
 0.004
 0.005
 0.010

 D
 1.80
 2.00
 2.20
 0.070
 0.078
 0.086
 Е 1.15 1.25 1.35 0.045 0.049 0.053 
 e
 0.65 BSC
 0.026 BSC

 L
 0.10
 0.20
 0.30
 0.004
 0.008
 0.012

 H<sub>E</sub>
 2.00
 2.10
 2.20
 0.078
 0.082
 0.086



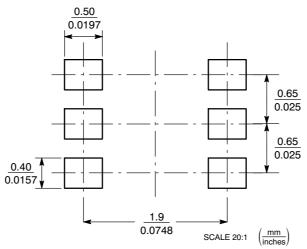
PIN 1. EMITTER 2

BASE 2 COLLECTOR 1 2. 3.

4. EMITTER 1

5 BASE 1 6. COLLECTOR 2

SOLDERING FOOTPRINT\*



\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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