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# NST3904DXV6T1G, NSVT3904DXV6T1G, NST3904DXV6T5G, SNST3904DXV6T5G

## Dual General Purpose Transistor

The NST3904DXV6T1G device is a spin-off of our popular SOT-23/SOT-323 three-leaded device. It is designed for general purpose amplifier applications and is housed in the SOT-563 six-leaded surface mount package. By putting two discrete devices in one package, this device is ideal for low-power surface mount applications where board space is at a premium.

### Features

- $h_{FE}$ , 100–300
- Low  $V_{CE(sat)}$ ,  $\leq 0.4$  V
- Simplifies Circuit Design
- Reduces Board Space
- Reduces Component Count
- AEC-Q101 Qualified and PPAP Capable – NSVT3904DXV6T1G, SNST3904DXV6T5G
- S and NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These are Pb-Free Devices

### MAXIMUM RATINGS

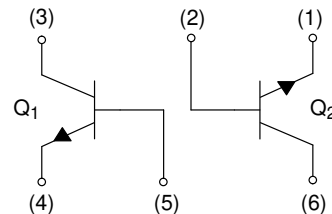
Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CEO}$	40	Vdc
Collector–Base Voltage	$V_{CBO}$	60	Vdc
Emitter–Base Voltage	$V_{EBO}$	6.0	Vdc
Collector Current – Continuous	$I_C$	200	mAdc
Electrostatic Discharge	HBM MM	ESD >16000 >2000	V

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.



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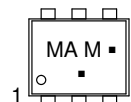


NST3904DXV6T1



SOT-563  
CASE 463A  
STYLE 1

### MARKING DIAGRAM



MA = Device Code

M = Date Code

▪ = Pb-Free Package

(Note: Microdot may be in either location)

### ORDERING INFORMATION

Device	Package	Shipping†
NST3904DXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
NSVT3904DXV6T1G	SOT-563 (Pb-Free)	4000/Tape & Reel
NST3904DXV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel
SNST3904DXV6T5G	SOT-563 (Pb-Free)	8000/Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

# NST3904DXV6T1G, NSVT3904DXV6T1G, NST3904DXV6T5G, SNST3904DXV6T5G

## THERMAL CHARACTERISTICS

Characteristic (One Junction Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$ (Note 1)	$P_D$	357 2.9	mW mW/ $^\circ\text{C}$
Thermal Resistance Junction-to-Ambient (Note 1)	$R_{\theta JA}$	350	$^\circ\text{C}/\text{W}$
Characteristic (Both Junctions Heated)	Symbol	Max	Unit
Total Device Dissipation $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$ (Note 1)	$P_D$	500 4.0	mW mW/ $^\circ\text{C}$
Thermal Resistance, Junction-to-Ambient (Note 1)	$R_{\theta JA}$	250	$^\circ\text{C}/\text{W}$
Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 to +150	$^\circ\text{C}$

1. FR-4 @ Minimum Pad

# NST3904DXV6T1G, NSVT3904DXV6T1G, NST3904DXV6T5G, SNST3904DXV6T5G

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
<b>OFF CHARACTERISTICS</b>				
Collector–Emitter Breakdown Voltage (Note 2) (I <sub>C</sub> = 1.0 mAdc, I <sub>B</sub> = 0)	V <sub>(BR)CEO</sub>	40	–	Vdc
Collector–Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	60	–	Vdc
Emitter–Base Breakdown Voltage (I <sub>E</sub> = 10 μAdc, I <sub>C</sub> = 0)	V <sub>(BR)EBO</sub>	6.0	–	Vdc
Base Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>BL</sub>	–	50	nAdc
Collector Cutoff Current (V <sub>CE</sub> = 30 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>CEX</sub>	–	50	nAdc

## ON CHARACTERISTICS (Note 2)

DC Current Gain (I <sub>C</sub> = 0.1 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 1.0 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 50 mAdc, V <sub>CE</sub> = 1.0 Vdc) (I <sub>C</sub> = 100 mAdc, V <sub>CE</sub> = 1.0 Vdc)	h <sub>FE</sub>	40 70 100 60 30	– – 300 – –	–
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>CE(sat)</sub>	– –	0.2 0.3	Vdc
Base–Emitter Saturation Voltage (I <sub>C</sub> = 10 mAdc, I <sub>B</sub> = 1.0 mAdc) (I <sub>C</sub> = 50 mAdc, I <sub>B</sub> = 5.0 mAdc)	V <sub>BE(sat)</sub>	0.65 –	0.85 0.95	Vdc

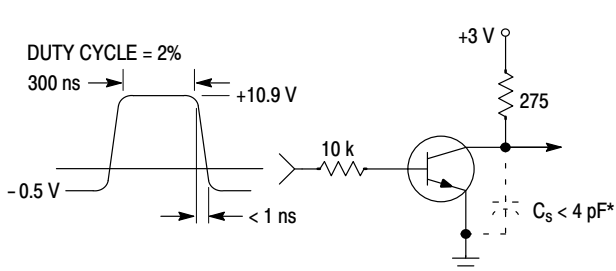
## SMALL–SIGNAL CHARACTERISTICS

Current–Gain – Bandwidth Product (I <sub>C</sub> = 10 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 MHz)	f <sub>T</sub>	300	–	MHz
Output Capacitance (V <sub>CB</sub> = 5.0 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	–	4.0	pF
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)	C <sub>ibo</sub>	–	8.0	pF
Input Impedance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>ie</sub>	1.0 2.0	10 12	k Ω
Voltage Feedback Ratio (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>re</sub>	0.5 0.1	8.0 10	X 10 <sup>–4</sup>
Small–Signal Current Gain (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>fe</sub>	100 100	400 400	–
Output Admittance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 1.0 mAdc, f = 1.0 kHz)	h <sub>oe</sub>	1.0 3.0	40 60	μmhos
Noise Figure (V <sub>CE</sub> = 5.0 Vdc, I <sub>C</sub> = 100 μAdc, R <sub>S</sub> = 1.0 k Ω, f = 1.0 kHz)	NF	– –	5.0 4.0	dB

## SWITCHING CHARACTERISTICS

Delay Time	(V <sub>CC</sub> = 3.0 Vdc, V <sub>BE</sub> = –0.5 Vdc)	t <sub>d</sub>	–	35	ns
Rise Time	(I <sub>C</sub> = 10 mAdc, I <sub>B1</sub> = 1.0 mAdc)	t <sub>r</sub>	–	35	
Storage Time	(V <sub>CC</sub> = 3.0 Vdc, I <sub>C</sub> = 10 mAdc)	t <sub>s</sub>	–	200	ns
Fall Time	(I <sub>B1</sub> = I <sub>B2</sub> = 1.0 mAdc)	t <sub>f</sub>	–	50	

2. Pulse Test: Pulse Width ≤ 300 μs; Duty Cycle ≤ 2.0%.



\* Total shunt capacitance of test jig and connectors

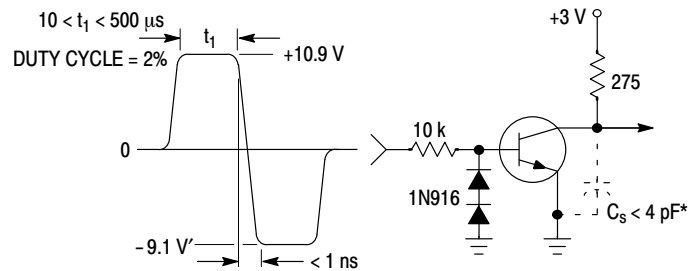


Figure 1. Delay and Rise Time Equivalent Test Circuit

Figure 2. Storage and Fall Time Equivalent Test Circuit

TYPICAL TRANSIENT CHARACTERISTICS

—  $T_J = 25^\circ\text{C}$   
 - - -  $T_J = 125^\circ\text{C}$

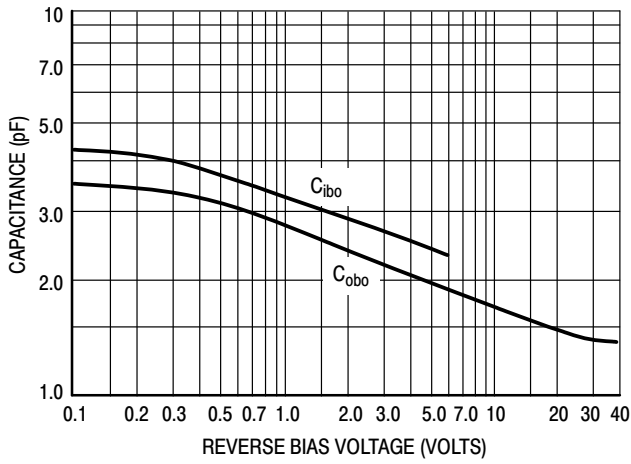


Figure 3. Capacitance

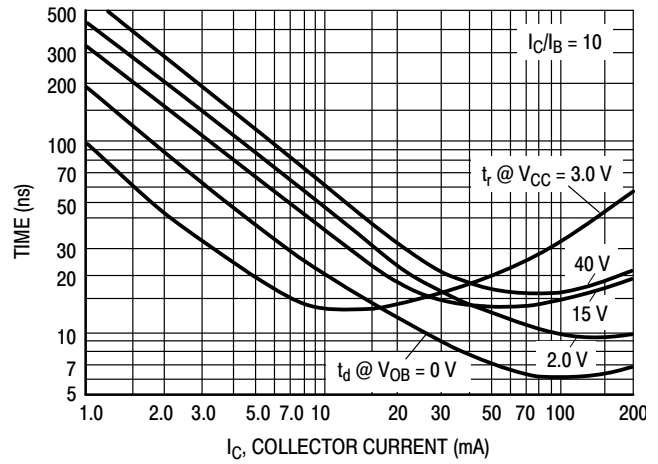


Figure 4. Turn-On Time

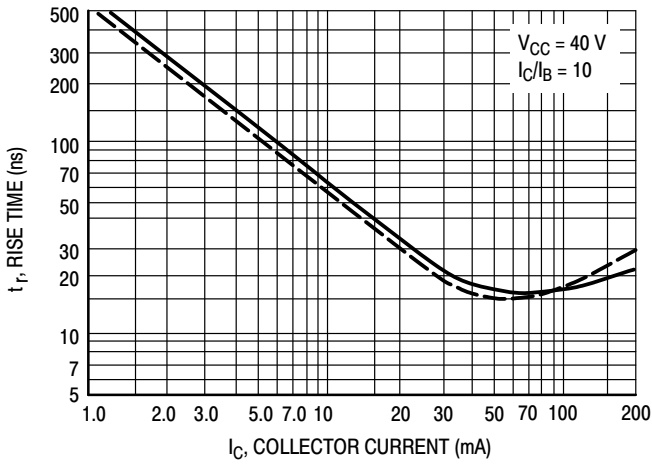


Figure 5. Rise Time

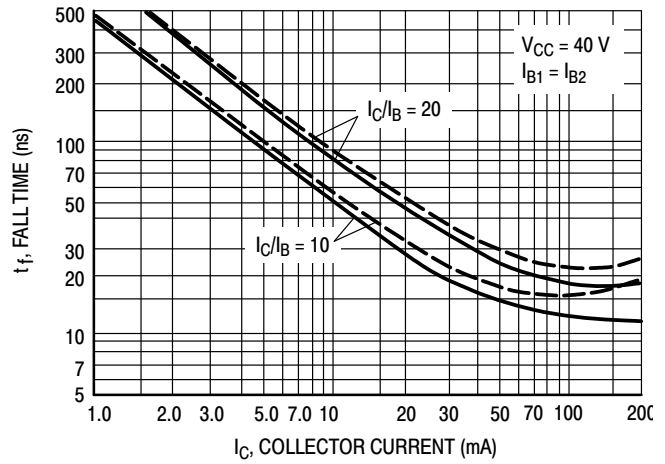


Figure 6. Fall Time

**TYPICAL AUDIO SMALL-SIGNAL CHARACTERISTICS  
NOISE FIGURE VARIATIONS**

( $V_{CE} = 5.0 \text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$ , Bandwidth = 1.0 Hz)

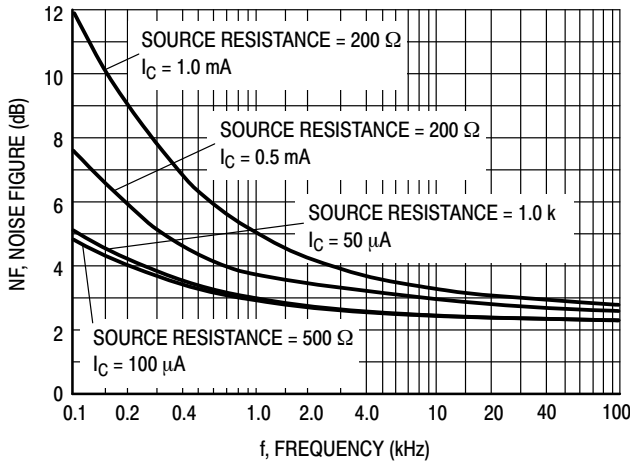


Figure 7. Noise Figure

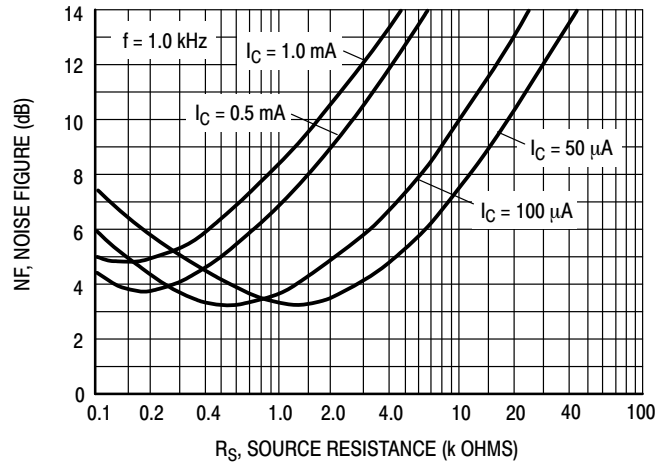


Figure 8. Noise Figure

**h PARAMETERS**

( $V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$ )

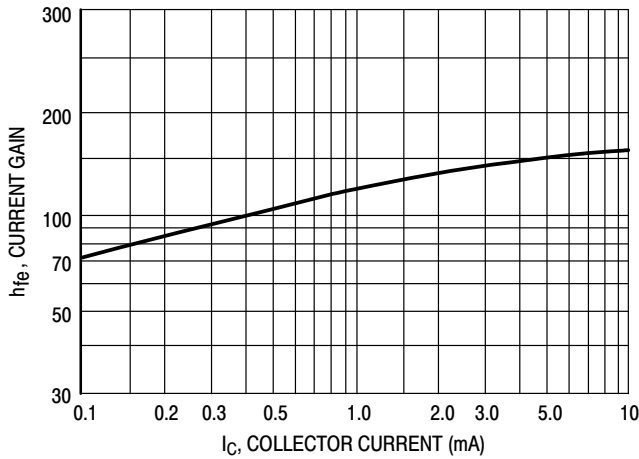


Figure 9. Current Gain

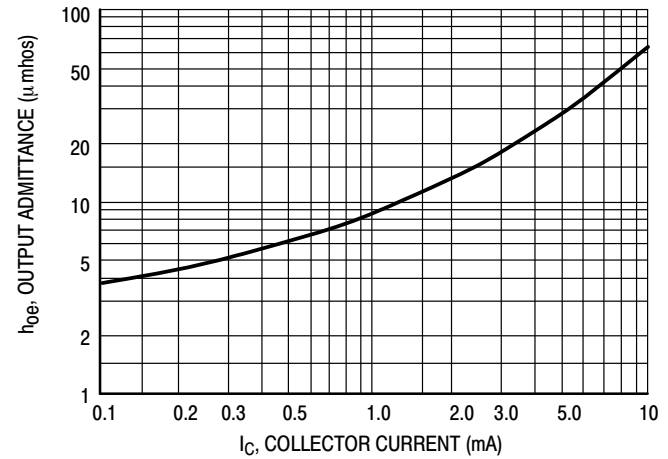


Figure 10. Output Admittance

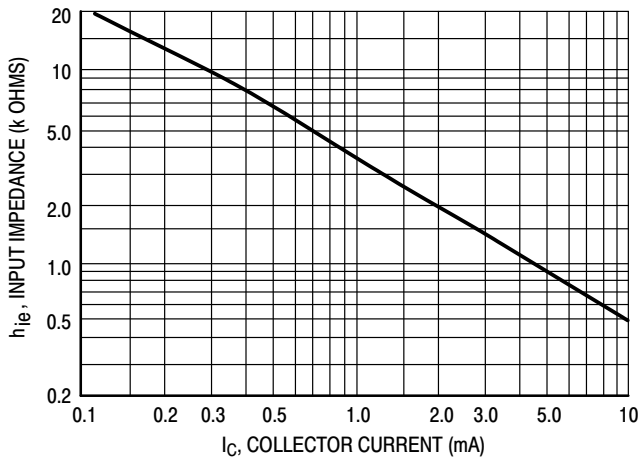


Figure 11. Input Impedance

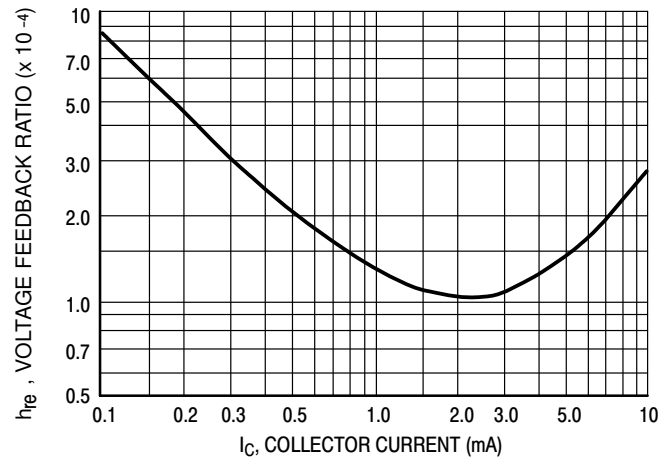


Figure 12. Voltage Feedback Ratio

TYPICAL STATIC CHARACTERISTICS

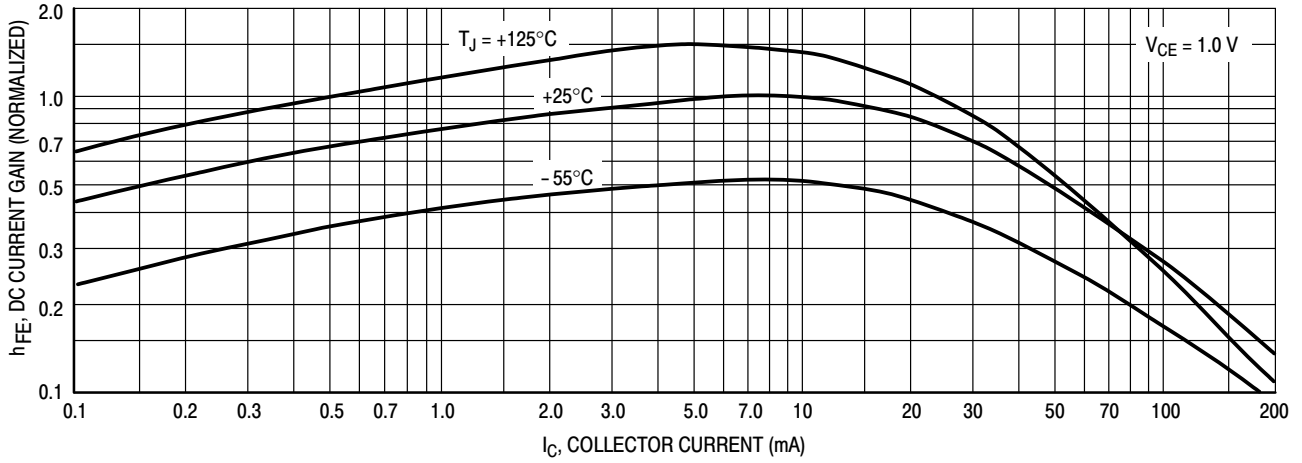


Figure 13. DC Current Gain

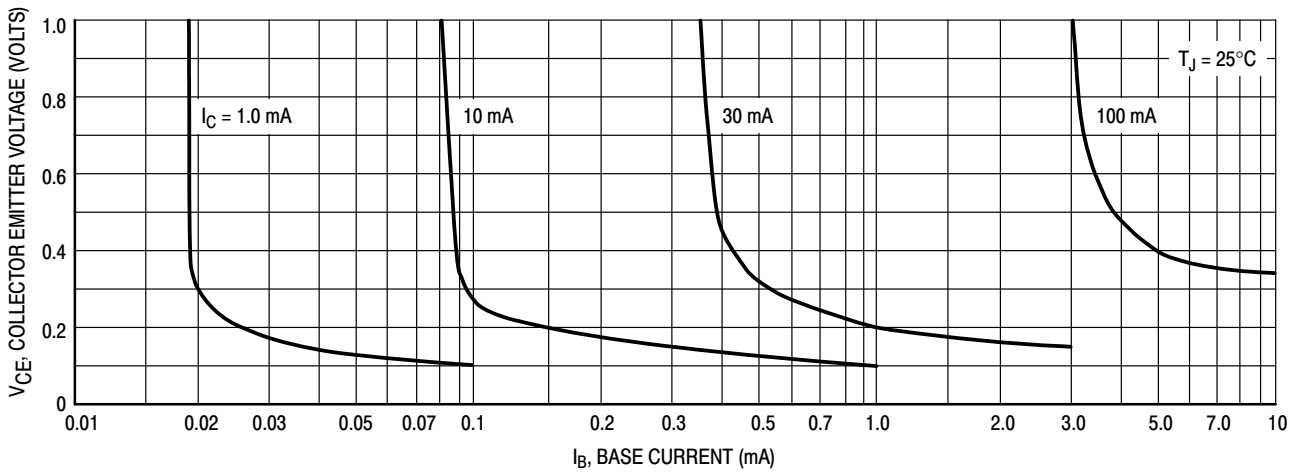


Figure 14. Collector Saturation Region

TYPICAL STATIC CHARACTERISTICS

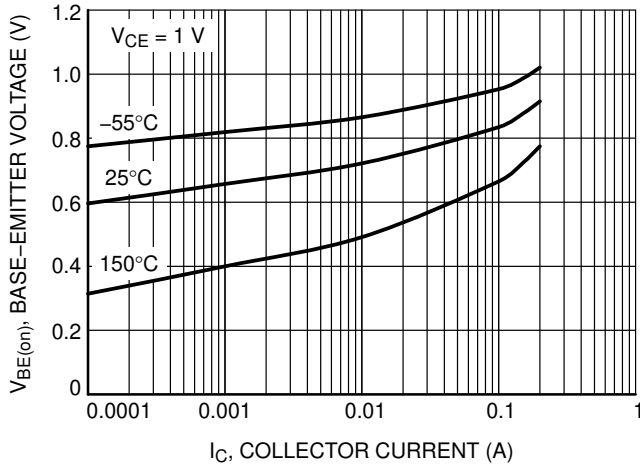


Figure 15. Base Emitter Voltage vs. Collector Current

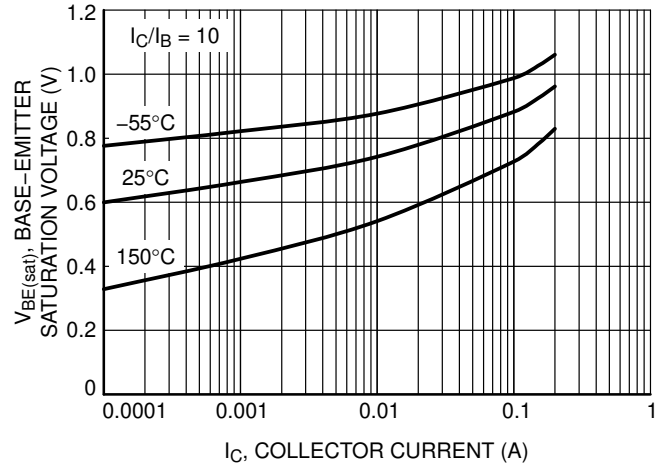


Figure 16. Base Emitter Saturation Voltage vs. Collector Current

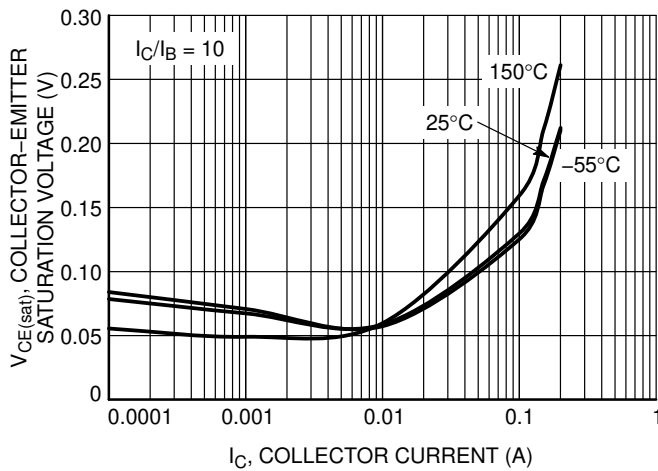


Figure 17. Collector Emitter Saturation Voltage vs. Collector Current

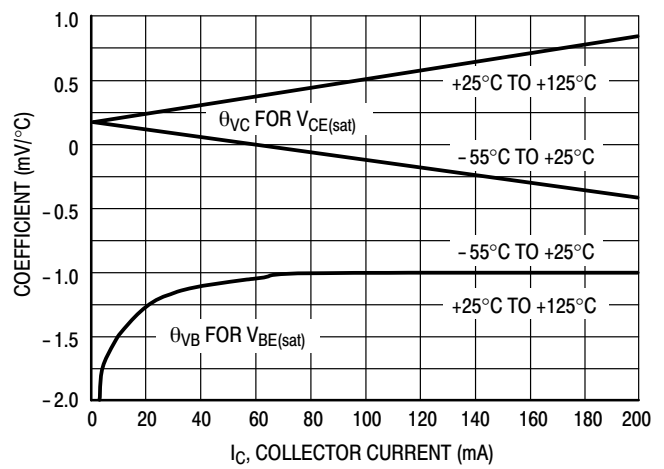
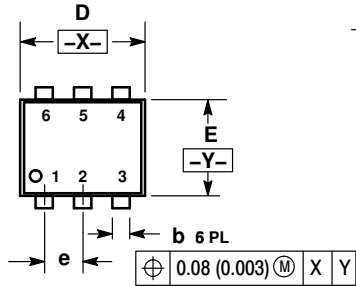


Figure 18. Temperature Coefficients



PACKAGE DIMENSIONS

SOT-563, 6 LEAD  
CASE 463A  
ISSUE F

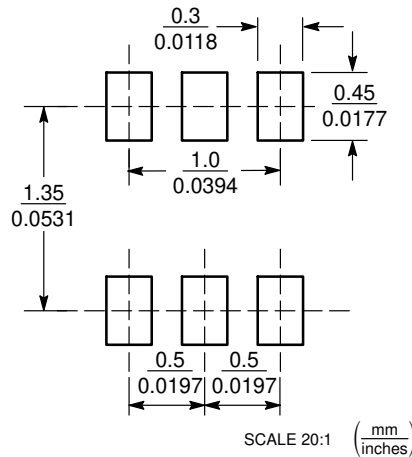


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS
3. MAXIMUM LEAD THICKNESS INCLUDES LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL.

DIM	MILLIMETERS			INCHES		
	MIN	NOM	MAX	MIN	NOM	MAX
A	0.50	0.55	0.60	0.020	0.021	0.023
b	0.17	0.22	0.27	0.007	0.009	0.011
C	0.08	0.12	0.18	0.003	0.005	0.007
D	1.50	1.60	1.70	0.059	0.062	0.066
E	1.10	1.20	1.30	0.043	0.047	0.051
e	0.5 BSC			0.02 BSC		
L	0.10	0.20	0.30	0.004	0.008	0.012
H <sub>E</sub>	1.50	1.60	1.70	0.059	0.062	0.066

SOLDERING FOOTPRINT\*



STYLE 1:

- PIN 1. EMITTER 1
- BASE 1
- COLLECTOR 2
- EMITTER 2
- BASE 2
- COLLECTOR 1

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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