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General Purpose Transistor

NPN Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-416/SC-75 package which is designed for low power surface mount applications.

Features

- NSV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant



Rating	Symbol	Max	Unit
Collector-Emitter Voltage	V _{CEO}	40	Vdc
Collector-Base Voltage	V _{CBO}	75	Vdc
Emitter-Base Voltage	V _{EBO}	6.0	Vdc
Collector Current – Continuous	Ic	600	mAdc

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation (Note 1) $T_A = 25^{\circ}C$	P _D	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{ hetaJA}$	833	°C/W
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-55 to +150	°C

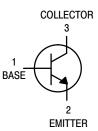
Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

 Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.



ON Semiconductor®

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CASE 463 SOT-416/SC-75 STYLE 1

MARKING DIAGRAM



1P = Specific Device Code

M = Date Code = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
MMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel
NSVMMBT2222ATT1G	SOT-416 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Chara	acteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (N $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{(BR)CEO}	40	_	Vdc	
Collector – Base Breakdown Voltage ($I_C = 10 \mu Adc, I_E = 0$)	V _{(BR)CBO}	75	-	Vdc	
Emitter – Base Breakdown Voltage ($I_E = 10 \mu Adc, I_C = 0$)		V _{(BR)EBO}	6.0	-	Vdc
Base Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)		I _{BL}	-	20	nAdc
Collector Cutoff Current (V _{CE} = 60 Vdc, V _{EB} = 3.0 Vdc)	I _{CEX}	-	10	nAdc	
ON CHARACTERISTICS (Note 2)					
$ \begin{array}{l} \text{DC Current Gain} \\ \text{(I}_{\text{C}} = 0.1 \text{ mAdc, V}_{\text{CE}} = 10 \text{ Vdc)} \\ \text{(I}_{\text{C}} = 1.0 \text{ mAdc, V}_{\text{CE}} = 10 \text{ Vdc)} \\ \text{(I}_{\text{C}} = 10 \text{ mAdc, V}_{\text{CE}} = 10 \text{ Vdc)} \\ \text{(I}_{\text{C}} = 150 \text{ mAdc, V}_{\text{CE}} = 10 \text{ Vdc)} \\ \text{(I}_{\text{C}} = 500 \text{ mAdc, V}_{\text{CE}} = 10 \text{ Vdc)} \\ \end{array} $		H _{FE}	35 50 75 100 40	- - - -	-
Collector-Emitter Saturation Voltage ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)		V _{CE(sat)}	- -	0.3 1.0	Vdc
Base – Emitter Saturation Voltage ($I_C = 150 \text{ mAdc}$, $I_B = 15 \text{ mAdc}$) ($I_C = 500 \text{ mAdc}$, $I_B = 50 \text{ mAdc}$)	V _{BE(sat)}	0.6	1.2 2.0	Vdc	
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (I _C = 20 mAdc, V _{CE} = 20 Vdc, f = 100 l	MHz)	f _T	300	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1.0 MHz)		C _{obo}	-	8.0	pF
Input Capacitance (V _{EB} = 0.5 Vdc, I _C = 0, f = 1.0 MHz)	C _{ibo}	_	30	pF	
Input Impedance (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 k	h _{ie}	0.25	1.25	kΩ	
Voltage Feedback Ratio (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 k	h _{re}		4.0	X 10 ⁻⁴	
Small – Signal Current Gain (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 k	h _{fe}	75	375	-	
Output Admittance (V _{CE} = 10 Vdc, I _C = 10 mAdc, f = 1.0 k	h _{oe}	25	200	μmhos	
Noise Figure ($V_{CE} = 10 \text{ Vdc}, I_{C} = 100 \mu \text{Adc}, R_{S} = 1.$	NF	-	4.0	dB	
SWITCHING CHARACTERISTICS		<u> </u>		!	+
Delay Time	$(V_{CC} = 3.0 \text{ Vdc}, V_{BE} = -0.5 \text{ Vdc},$	t _d	-	10	
Rise Time	I _C = 150 mAdc, I _{B1} = 15 mAdc)	t _r	_	25	ns
Storage Time	(V _{CC} = 30 Vdc, I _C = 150 mAdc,	t _s	-	225	no
Fall Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$	t _f	-	60	ns

Device mounted on FR4 glass epoxy printed circuit board using the minimum recommended footprint.
 Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2.0%.

SWITCHING TIME EQUIVALENT TEST CIRCUITS

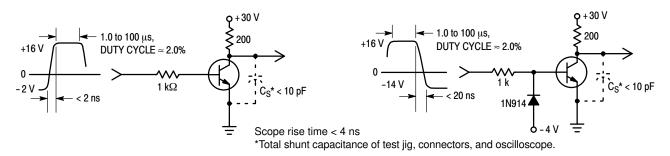


Figure 1. Turn-On Time

Figure 2. Turn-Off Time

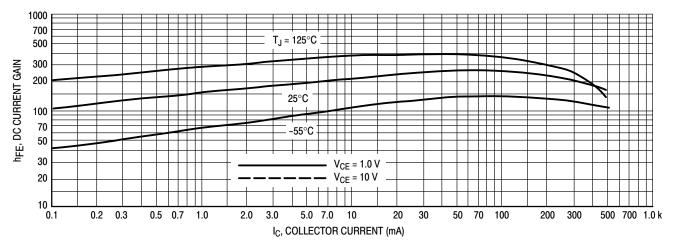


Figure 3. DC Current Gain

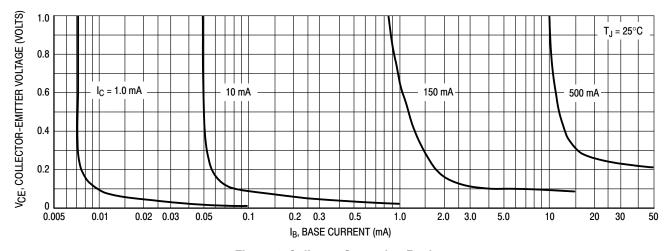


Figure 4. Collector Saturation Region

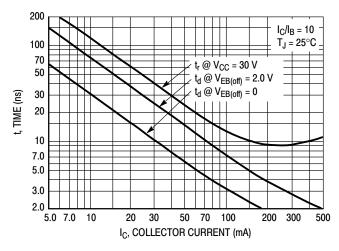


Figure 5. Turn - On Time

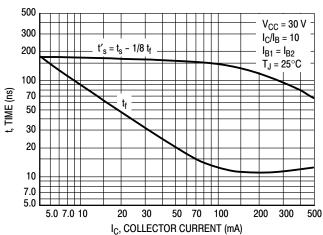


Figure 6. Turn-Off Time

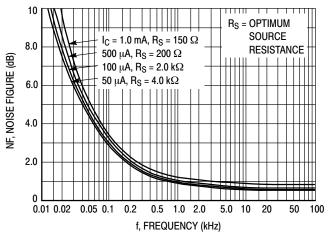


Figure 7. Frequency Effects

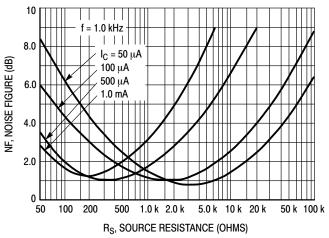


Figure 8. Source Resistance Effects

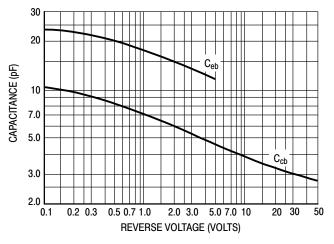


Figure 9. Capacitances

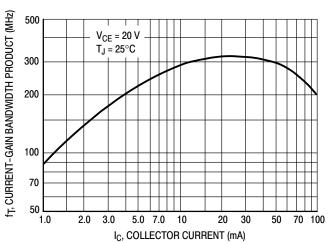


Figure 10. Current-Gain Bandwidth Product

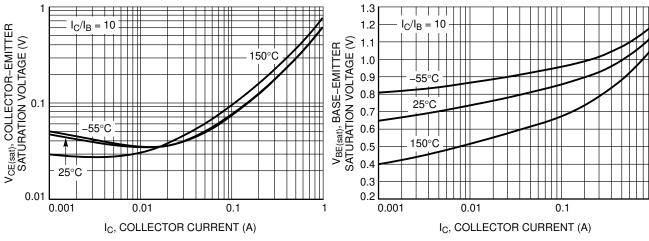
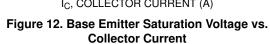


Figure 11. Collector Emitter Saturation Voltage vs. Collector Current



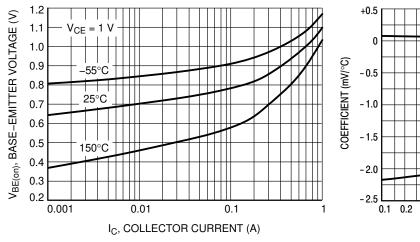


Figure 13. Base Emitter Voltage vs. Collector Current

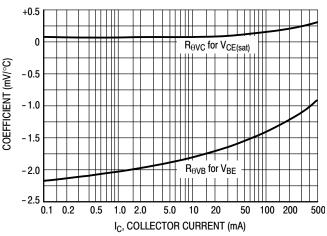


Figure 14. Temperature Coefficients

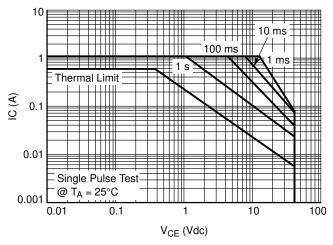
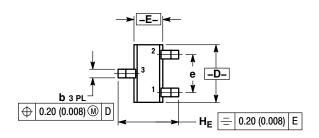
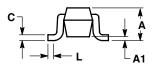


Figure 15. Safe Operating Area

PACKAGE DIMENSIONS

SC-75/SOT-416 **CASE 463 ISSUE G**





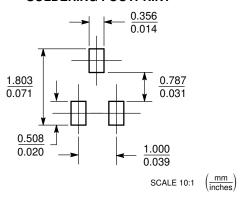
NOTES

- DIMENSIONING AND TOLERANCING PER ANSI Y14 5M 1982
- CONTROLLING DIMENSION: MILLIMETER.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.70	0.80	0.90	0.027	0.031	0.035
A1	0.00	0.05	0.10	0.000	0.002	0.004
b	0.15	0.20	0.30	0.006	0.008	0.012
С	0.10	0.15	0.25	0.004	0.006	0.010
D	1.55	1.60	1.65	0.061	0.063	0.065
E	0.70	0.80	0.90	0.027	0.031	0.035
е	1.00 BSC				0.04 BSC	
L	0.10	0.15	0.20	0.004	0.006	0.008
HE	1.50	1.60	1.70	0.060	0.063	0.067

STYLE 1: PIN 1. BASE 2. EMITTER 3. COLLECTOR

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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