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Power MOSFET

30 V, 2.5 A, Single N-Channel, SOT-23

Features

- Leading Planar Technology for Low Gate Charge / Fast Switching
- 4.5 V Rated for Low Voltage Gate Drive
- SOT-23 Surface Mount for Small Footprint (3 x 3 mm)
- NV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb-Free and are RoHS Compliant

Applications

- DC-DC Conversion
- Load/Power Switch for Portables
- Load/Power Switch for Computing

MAXIMUM RATINGS (T_J = 25°C unless otherwise noted)

Parameter			Symbol	Value	Unit	
Drain-to-Source Voltage			V_{DSS}	30	V	
Gate-to-Source Voltage	Gate-to-Source Voltage			±20	V	
Continuous Drain	Steady	T _A = 25°C	I _D	2.0	Α	
Current (Note 1)	State	T _A = 85°C		1.5		
	t ≤ 10 s	T _A = 25°C		2.5		
Power Dissipation (Note 1)	Steady State	T _A = 25°C	P _D	0.73	W	
Continuous Drain	Steady	T _A = 25°C	I _D	1.5	Α	
Current (Note 2)	State	T _A = 85°C		1.1		
Power Dissipation (Note 2)		T _A = 25°C	P _D	0.42	W	
Pulsed Drain Current	Ised Drain Current $t_p = 10 \mu s$			10	Α	
Operating Junction and S	T _J , T _{stg}	–55 to 150	°C			
Source Current (Body Diode)			I _S	2.0	Α	
Peak Source Current (Diode Forward) $t_p = 10 \; \mu s$			I _{SM}	4.0	Α	
Lead Temperature for Soldering Purposes (1/8" from case for 10 s)			TL	260	°C	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Max	Unit
Junction-to-Ambient - Steady State (Note 1)	$R_{\theta JA}$	170	°C/W
Junction-to-Ambient - t < 10 s (Note 1)	$R_{\theta JA}$	100	
Junction-to-Ambient - Steady State (Note 2)	$R_{\theta JA}$	300	

- 1. Surface-mounted on FR4 board using 1 in sq pad size.
- 2. Surface-mounted on FR4 board using the minimum recommended pad size.

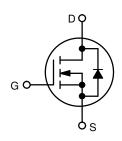


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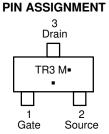
V _{(BR)DSS}	R _{DS(on)} TYP	I _D MAX
30 V	85 mΩ @ 10 V	2.5 A
	105 mΩ @ 4.5 V	_,,,,,

N-Channel





SOT-23 **CASE 318** STYLE 21



MARKING DIAGRAM/

TR3 = Specific Device Code

= Date Code = Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NTR4503NT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel
NVTR4503NT1G	SOT-23 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

	\ 0	,				
Parameter	Symbol	Test Conditions	Min	Тур	Max	Units
OFF CHARACTERISTICS						
Drain-to-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	30	36		٧
Zero Gate Voltage Drain Current					1.0	μΑ
		V _{GS} = 0 V, V _{DS} = 24 V, T _J = 125°C			10	1
Gate-to-Source Leakage Current	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
ON CHARACTERISTICS (Note 3)	1					
Gate Threshold Voltage	V _{GS(TH)}	$V_{GS} = V_{DS}, I_D = 250 \mu A$	1.0	1.75	3.0	٧
Drain-to-Source On-Resistance	R _{DS(on)}	$V_{GS} = 10 \text{ V}, I_D = 2.5 \text{ A}$		85	110	mΩ
		V _{GS} = 4.5 V, I _D = 2.0 A		105	140	1
Forward Transconductance	9FS	V _{DS} = 4.5 V, I _D = 2.5 A		5.3		S
CHARGES AND CAPACITANCES	1		1			
Input Capacitance	C _{iss}			135		pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = 15 \text{ V}$		52		1
Reverse Transfer Capacitance	C _{rss}	VDS = 15 V		15		1
Input Capacitance	C _{iss}			130	250	pF
Output Capacitance	C _{oss}	$V_{GS} = 0 \text{ V, f} = 1.0 \text{ MHz,}$ $V_{DS} = 24 \text{ V}$		42	75	'
Reverse Transfer Capacitance	C _{rss}	V _{DS} = 24 V		13	25	
Total Gate Charge	Q _{G(TOT)}			3.6	7.0	nC
Threshold Gate Charge	Q _{G(TH)}	Voc = 10 V Voc = 15 V		0.3		
Gate-to-Source Charge	Q _{GS}	$V_{GS} = 10 \text{ V}, V_{DS} = 15 \text{ V},$ $I_D = 2.5 \text{ A}$		0.6		
Gate-to-Drain Charge	Q _{GD}			0.7		1
Total Gate Charge	Q _{G(TOT)}			1.9		nC
Threshold Gate Charge	Q _{G(TH)}	V 4 5 V V 24 V		0.3		1
Gate-to-Source Charge	Q _{GS}	$V_{GS} = 4.5 \text{ V}, V_{DS} = 24 \text{ V},$ $I_D = 2.5 \text{ A}$		0.6		1
Gate-to-Drain Charge	Q _{GD}			0.9		1
SWITCHING CHARACTERISTICS (No	1		ı			
Turn-On Delay Time	t _{d(on)}			5.8	12	ns
Rise Time	t _r	V _{GS} = 10 V, V _{DD} = 15 V,		5.8	10	1
Turn-Off Delay Time	t _{d(off)}	$I_D = 1 \text{ A}, R_G = 6 \Omega$		14	25	1
Fall Time	t _f			1.6	5.0	1
Turn-On Delay Time	t _{d(on)}			4.8		ns
Rise Time	t _r	V _{GS} = 10 V, V _{DD} = 24 V,		6.7		1
Turn-Off Delay Time	t _{d(off)}	$I_D = 2.5 \text{ A}, R_G = 2.5 \Omega$		13.6		1
Fall Time	t _f			1.8		1
DRAIN-SOURCE DIODE CHARACTE		<u> </u>		1	1	
Forward Diode Voltage	V _{SD}	V _{GS} = 0 V, I _S = 2.0 A		0.85	1.2	٧
Reverse Recovery Time	t _{RR}	V _{GS} = 0 V, I _S = 2.0 A,		9.2		ns
Reverse Recovery Charge	Q _{RR}	$dI_{S}/dt = 100 \text{ A/}\mu\text{s}$		4.0		nC
					<u> </u>	

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

3. Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

4. Switching characteristics are independent of operating junction temperatures.

TYPICAL PERFORMANCE CURVES

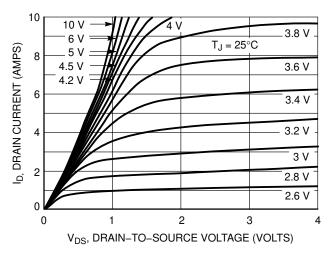


Figure 1. On-Region Characteristics

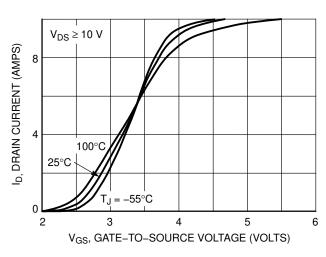


Figure 2. Transfer Characteristics

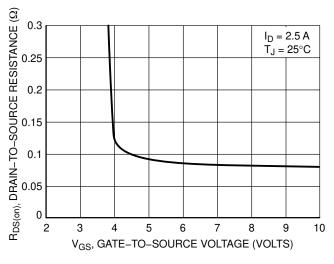


Figure 3. On-Resistance vs. Gate-to-Source Voltage

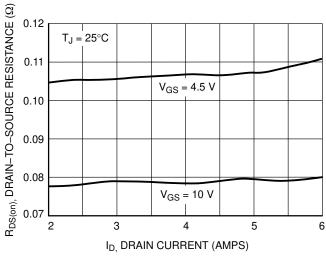


Figure 4. On–Resistance vs. Drain Current and Gate Voltage

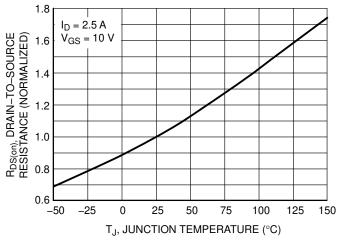


Figure 5. On–Resistance Variation with Temperature

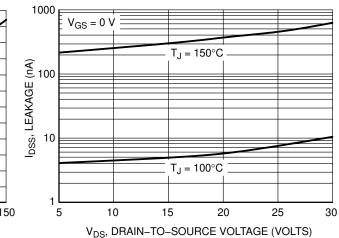
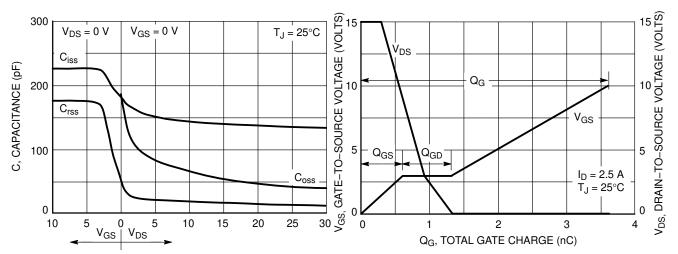


Figure 6. Drain-to-Source Leakage Current vs. Voltage

TYPICAL PERFORMANCE CURVES



GATE-TO-SOURCE OR DRAIN-TO-SOURCE VOLTAGE (VOLTS)

Figure 7. Capacitance Variation

Figure 8. Gate-to-Source and Drain-to-Source Voltage vs. Total Charge

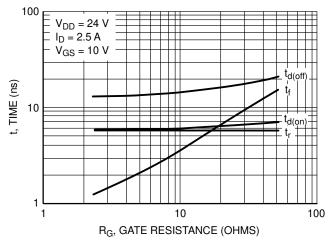


Figure 9. Resistive Switching Time Variation vs. Gate Resistance

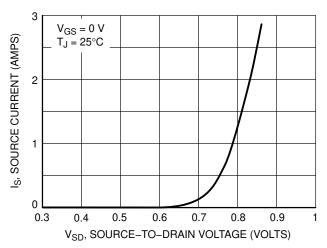
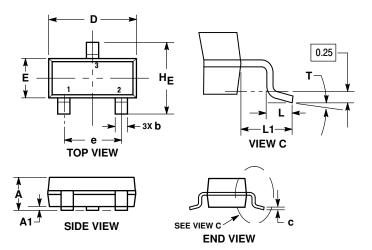


Figure 10. Diode Forward Voltage vs. Current

PACKAGE DIMENSIONS

SOT-23 (TO-236) CASE 318-08 **ISSUE AR**



NOTES:

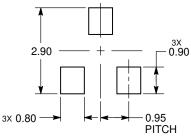
- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994. CONTROLLING DIMENSION: MILLIMETERS. MAXIMUM LEAD THICKNESS IN THE MINIMUM THICKNESS OF MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF THE BASE MATERIAL.
 DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH,
- PROTRUSIONS, OR GATE BURRS

	MILLIMETERS			INCHES			
DIM	MIN	NOM	MAX	MIN	NOM	MAX	
Α	0.89	1.00	1.11	0.035	0.039	0.044	
A1	0.01	0.06	0.10	0.000	0.002	0.004	
b	0.37	0.44	0.50	0.015	0.017	0.020	
С	0.08	0.14	0.20	0.003	0.006	0.008	
D	2.80	2.90	3.04	0.110	0.114	0.120	
E	1.20	1.30	1.40	0.047	0.051	0.055	
е	1.78	1.90	2.04	0.070	0.075	0.080	
L	0.30	0.43	0.55	0.012	0.017	0.022	
L1	0.35	0.54	0.69	0.014	0.021	0.027	
HE	2.10	2.40	2.64	0.083	0.094	0.104	
T	0°		10°	0°		10°	

STYLE 21:

- PIN 1. GATE
 - 2. SOURCE
 - 3. DRAIN

RECOMMENDED SOLDERING FOOTPRINT*



DIMENSIONS: MILLIMETERS

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^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.